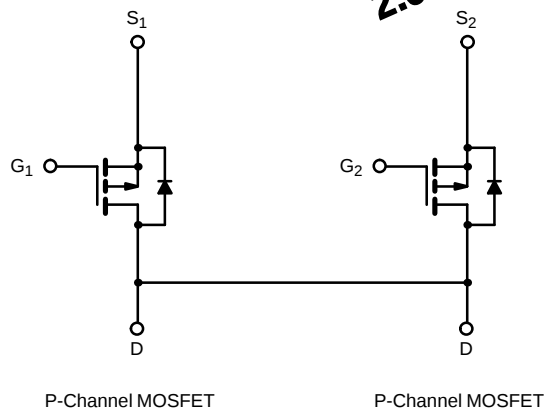
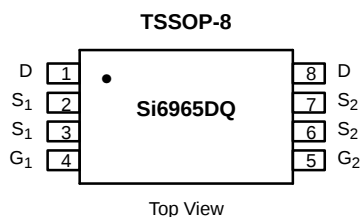




P-Channel 2.5-V (G-S) Battery Switch

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-20	0.035 @ $V_{GS} = -4.5$ V	± 5.0
	0.060 @ $V_{GS} = -2.5$ V	± 3.9



TrenchFET®
Power MOSFETs
2.5-V Rated

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	-20	V
Gate-Source Voltage		V_{GS}	± 12	
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^{a, b}	$T_A = 25^\circ\text{C}$	I_D	± 5.0	A
	$T_A = 70^\circ\text{C}$		± 4.0	
Pulsed Drain Current		I_{DM}	± 30	
Continuous Source Current (Diode Conduction) ^{a, b}		I_S	-1.5	
Maximum Power Dissipation ^{a, b}	$T_A = 25^\circ\text{C}$	P_D	1.5	W
	$T_A = 70^\circ\text{C}$		0.96	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	R_{thJA}		83	$^\circ\text{C/W}$
	Steady State		85		

Notes

a. Surface Mounted on FR4 Board.

b. $t \leq 10$ sec.

For SPICE model information via the Worldwide Web: <http://www.vishay.com/www/product/spice.htm>

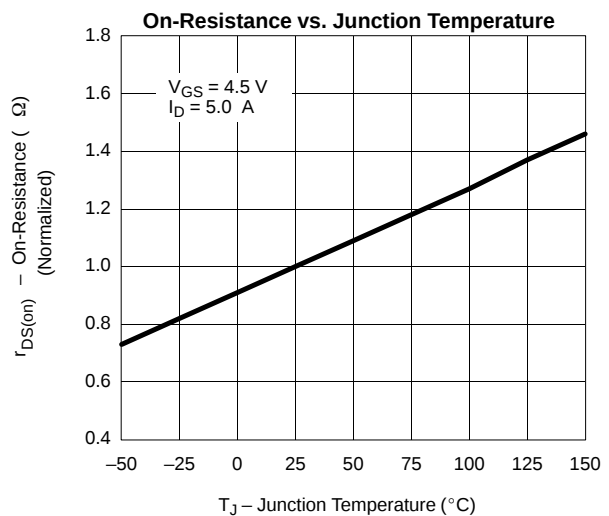
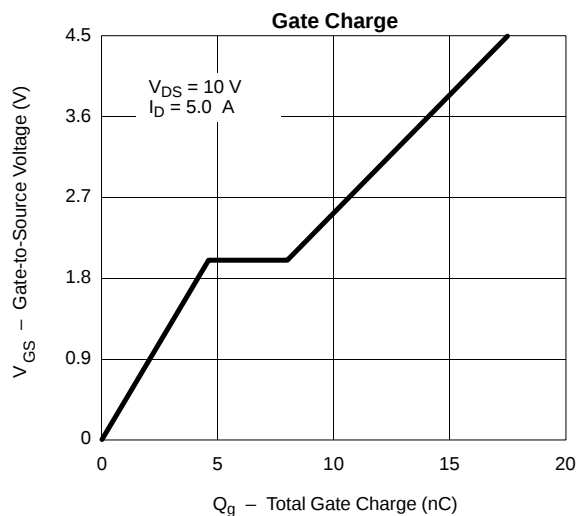
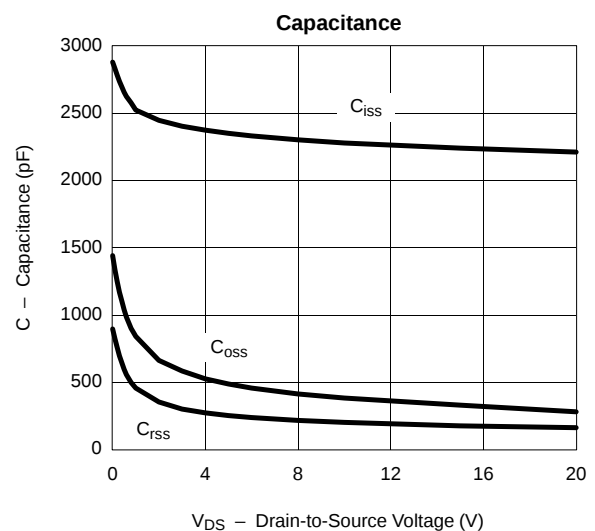
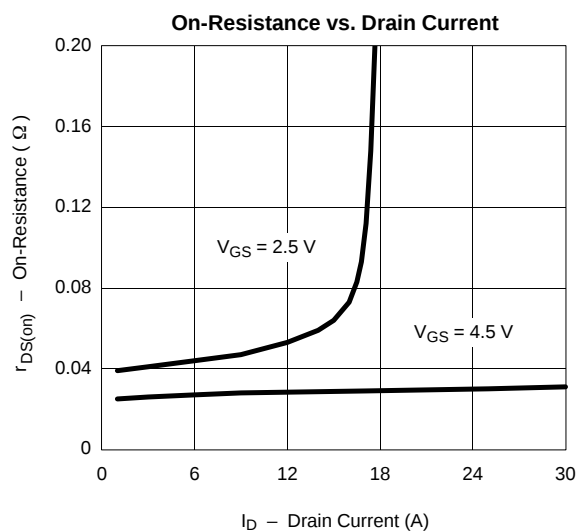
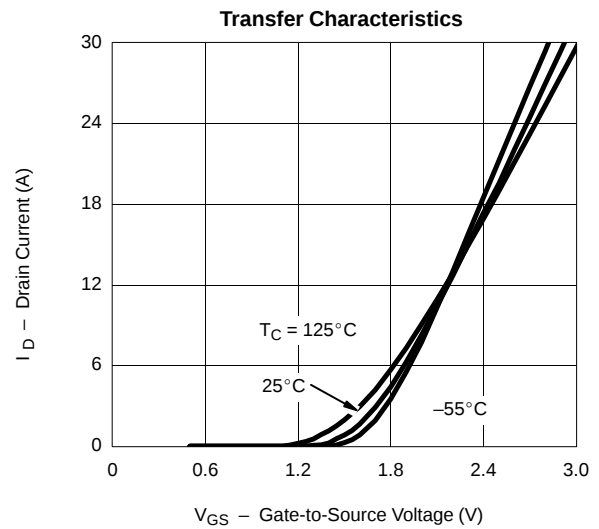
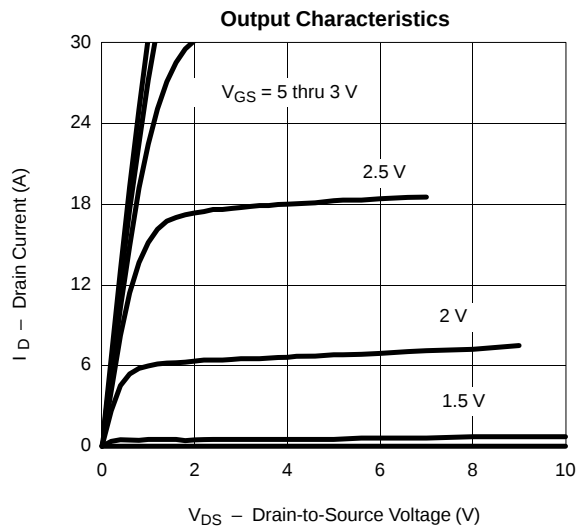
SPECIFICATIONS (T_J = 25°C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-0.6			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±12 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -16 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -16 V, V _{GS} = 0 V, T _J = 55°C			-5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ -5 V, V _{GS} = -4.5 V	-30			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = -4.5 V, I _D = -5.0 A		0.028	0.035	Ω
		V _{GS} = -2.5 V, I _D = -3.9 A		0.043	0.060	
Forward Transconductance ^a	g _{fs}	V _{DS} = -10 V, I _D = -5.0 A		15		S
Diode Forward Voltage ^a	V _{SD}	I _S = -1.5 A, V _{GS} = 0 V		-0.72	-1.2	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -10 V, V _{GS} = -10 V, I _D = -5.0 A		17.5	30	nC
Gate-Source Charge	Q _{gs}			4.6		
Gate-Drain Charge	Q _{gd}			3.4		
Turn-On Delay Time	t _{d(on)}	V _{DD} = -10 V, R _L = 10 Ω I _D ≈ -1 A, V _{GEN} = -4.5 V, R _G = 6 Ω		25	50	ns
Rise Time	t _r			30	60	
Turn-Off Delay Time	t _{d(off)}			110	200	
Fall Time	t _f			65	120	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = -1.5 A, di/dt = 100 A/μs		30	60	

Notes

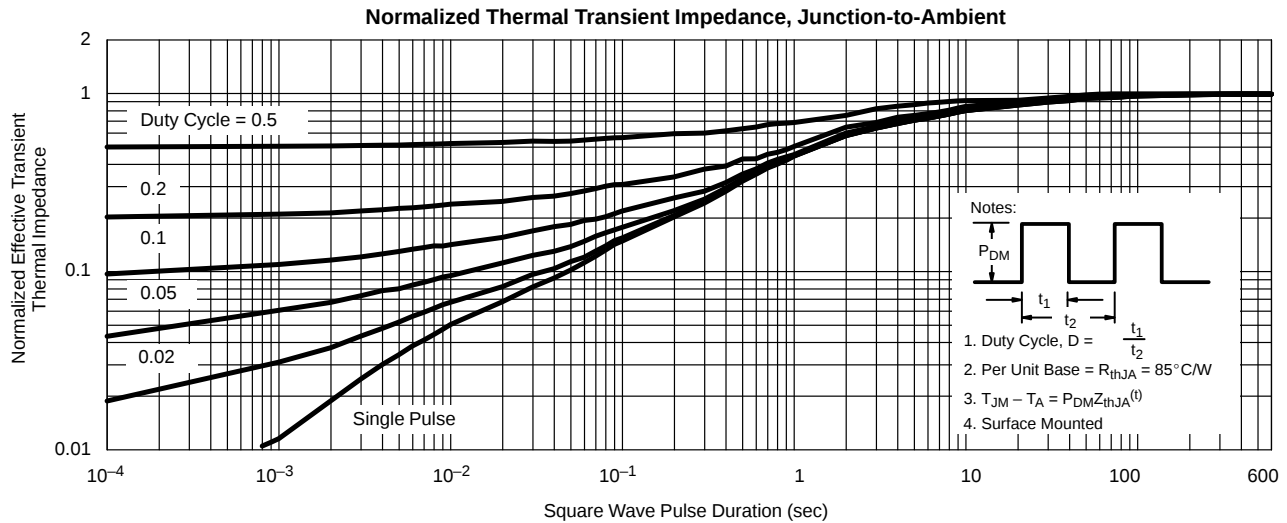
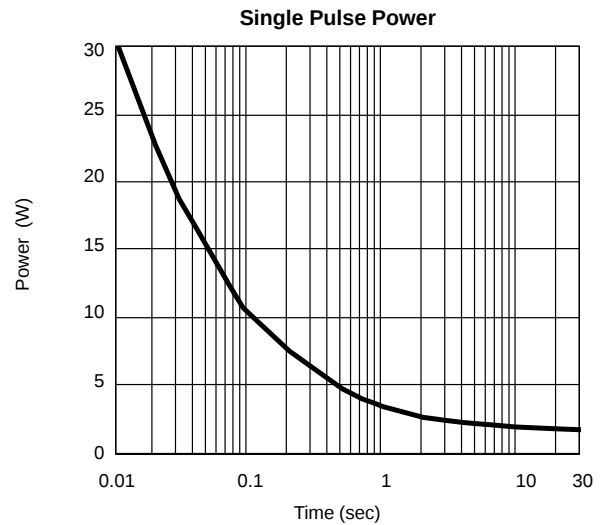
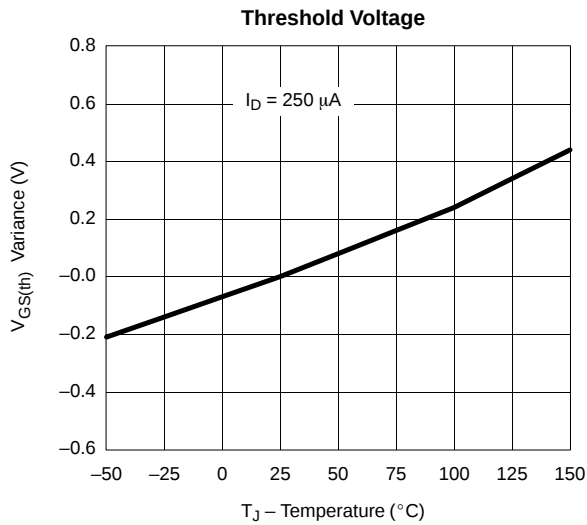
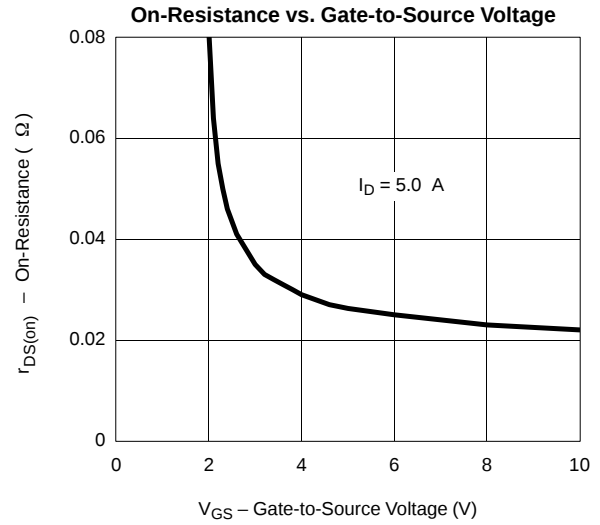
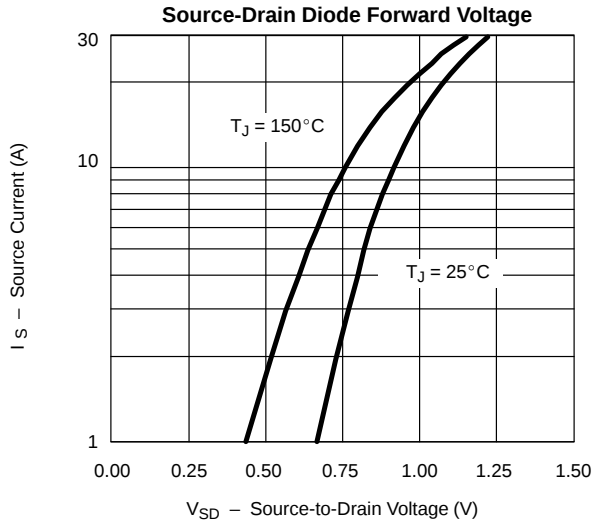
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



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