



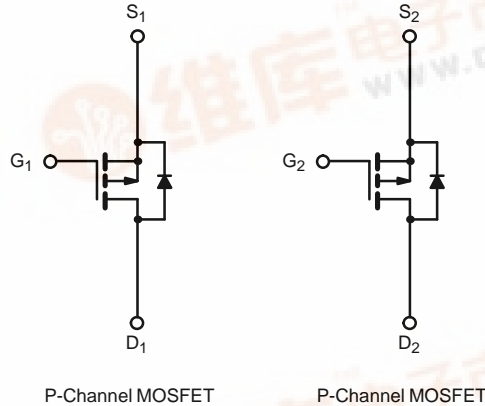
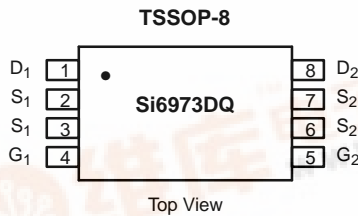
New Product

Si6973DQ
Vishay Siliconix

Dual P-Channel 1.8-V (G-S) MOSFET

TrenchFET®
Power MOSFETs
1.8-V Rated

PRODUCT SUMMARY		
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)
-20	0.030 @ V _{GS} = -4.5 V	-4.8
	0.039 @ V _{GS} = -2.5 V	-4.2
	0.055 @ V _{GS} = -1.8 V	-3.5



ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)				
Parameter	Symbol	10 secs	Steady State	Unit
Drain-Source Voltage	V _{DS}	-20		V
Gate-Source Voltage	V _{GS}	±8		
Continuous Drain Current (T _J = 150°C) ^a	T _A = 25°C	-4.8	-4.1	A
	T _A = 70°C	-3.9	-3.2	
Pulsed Drain Current (10 μs Pulse Width)	I _{DM}	-30		
Continuous Source Current (Diode Conduction) ^a	I _S	-1.0	-0.7	W
Maximum Power Dissipation ^a	T _A = 25°C	1.14	0.83	
	T _A = 70°C	0.73	0.53	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R _{thJA}	t ≤ 10 sec	86	110	°C/W
		Steady State	124	150	
Maximum Junction-to-Foot (Drain)	R _{thJF}	52	65		

Notes:
a. Surface Mounted on 1" x 1" FR4 Board.

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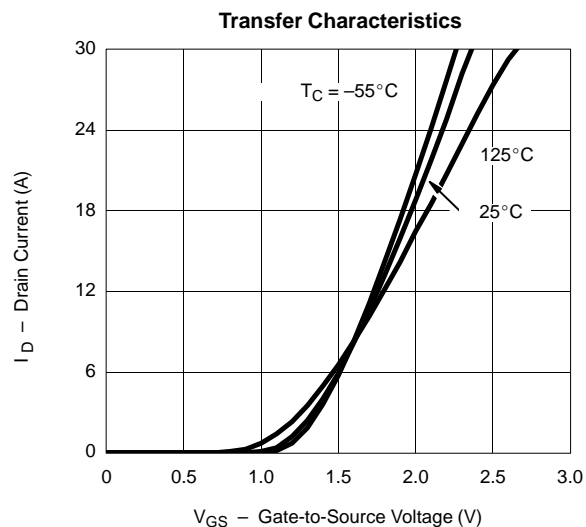
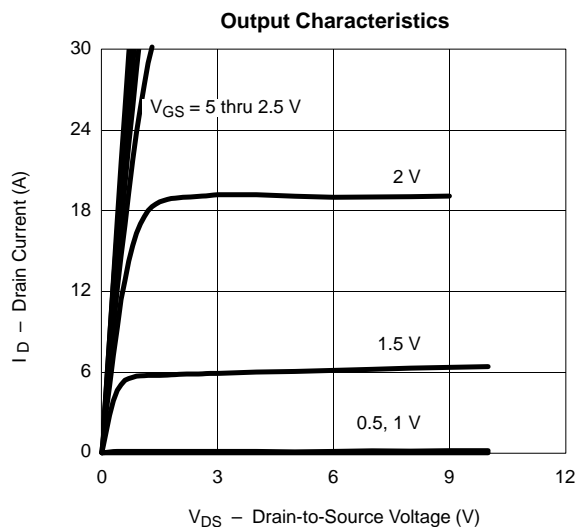
SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-0.45			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±8 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -16 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -16 V, V _{GS} = 0 V, T _J = 70 °C			-25	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = -5 V, V _{GS} = -4.5 V	-20			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = -4.5 V, I _D = -4.8 A		0.025	0.030	Ω
		V _{GS} = -2.5 V, I _D = -4.2 A		0.033	0.039	
		V _{GS} = -1.8 V, I _D = -3.5 A		0.046	0.055	Ω
Forward Transconductance ^a	g _{fs}	V _{DS} = -5 V, I _D = -4.8 A		21		S
Diode Forward Voltage ^a	V _{SD}	I _S = -1.0 A, V _{GS} = 0 V		-0.65	-1.1	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -10 V, V _{GS} = -4.5 V, I _D = -4.8 A		21	30	nC
Gate-Source Charge	Q _{gs}			4.4		
Gate-Drain Charge	Q _{gd}			3.3		
Turn-On Delay Time	t _{d(on)}	V _{DD} = -10 V, R _L = 10 Ω I _D ≅ -1 A, V _{GEN} = -4.5 V, R _G = 6 Ω		27	40	ns
Rise Time	t _r			27	40	
Turn-Off Delay Time	t _{d(off)}			93	140	
Fall Time	t _f			43	65	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = -1.0 A, di/dt = 100 A/μs		30	50	

Notes

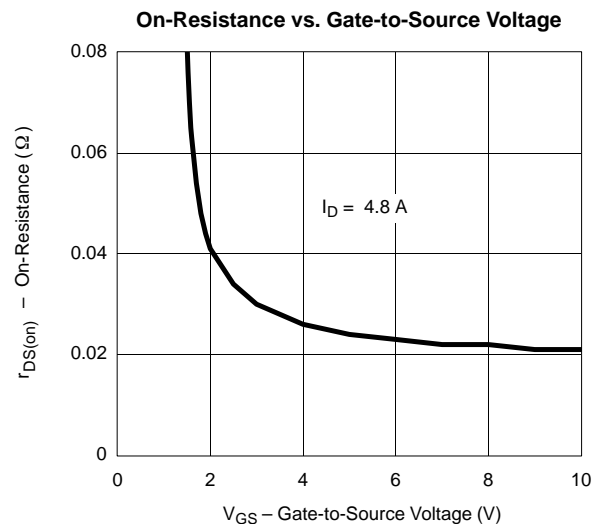
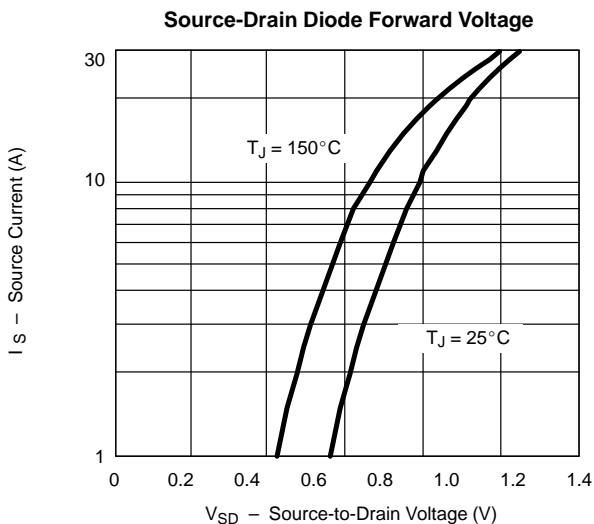
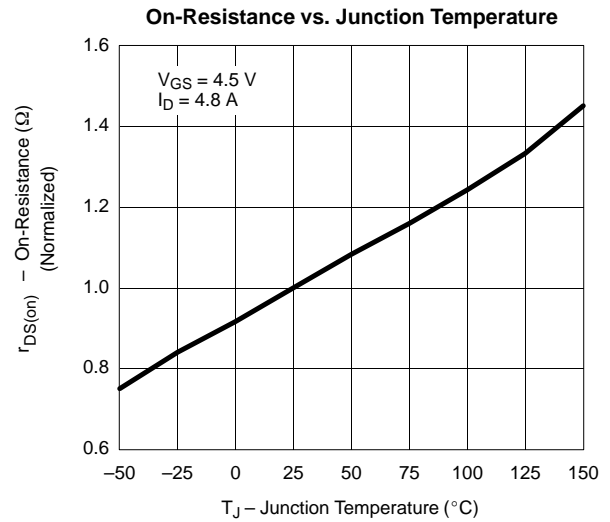
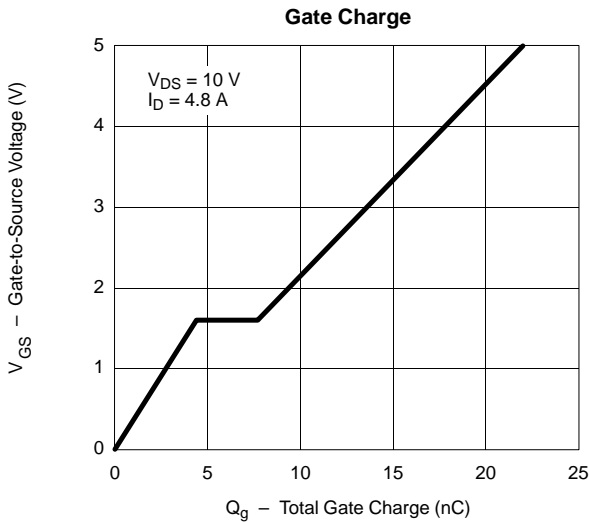
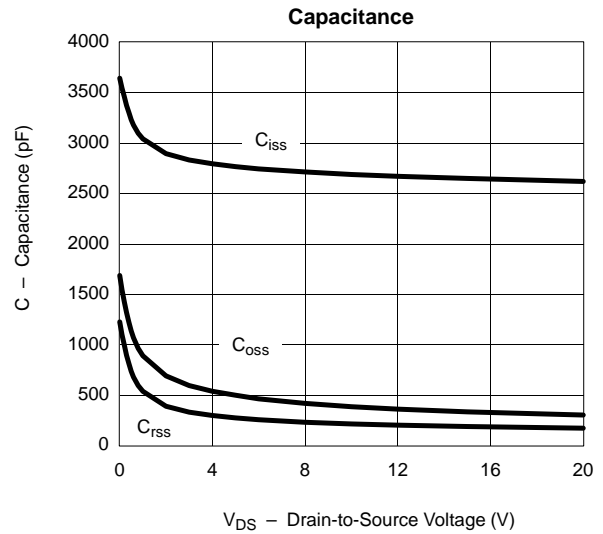
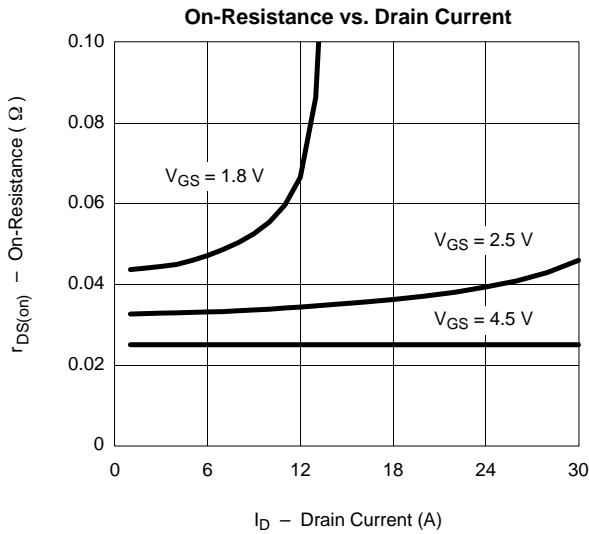
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)





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