



Si7892DP
Vishay Siliconix

N-Channel 30-V (D-S) MOSFET

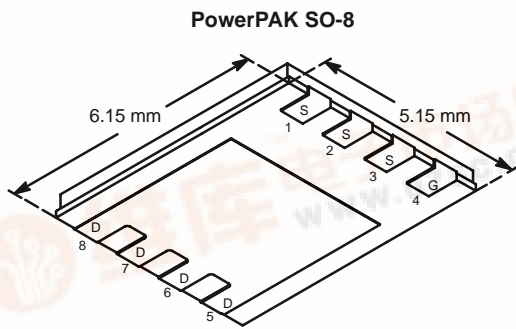
PRODUCT SUMMARY		
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)
30	0.0045 @ V _{GS} = 10 V	25
	0.006 @ V _{GS} = 4.5 V	22

FEATURES

- TrenchFET® Power MOSFET
- New Low Thermal Resistance PowerPAK® Package with Low 1.07-mm Profile
- Low Gate Charge
- 100% R_g Tested

APPLICATIONS

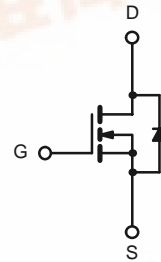
- Synchronous Rectifier



PowerPAK SO-8

Bottom View

Ordering Information: Si7892DP-T1



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _J = 150°C) ^a	T _A = 25°C	I _D	25	15	A
	T _A = 70°C		20	12	
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	60		
Continuous Source Current (Diode Conduction) ^a		I _S	4.5	1.6	
Maximum Power Dissipation ^a	T _A = 25°C	P _D	5.4	1.9	W
	T _A = 70°C		3.4	1.2	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	t ≤ 10 sec	R _{thJA}	18	23	°C/W
	Steady State		50	65	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	1.0	1.5	

Notes:
a. Surface Mounted on 1" x 1" FR4 Board.

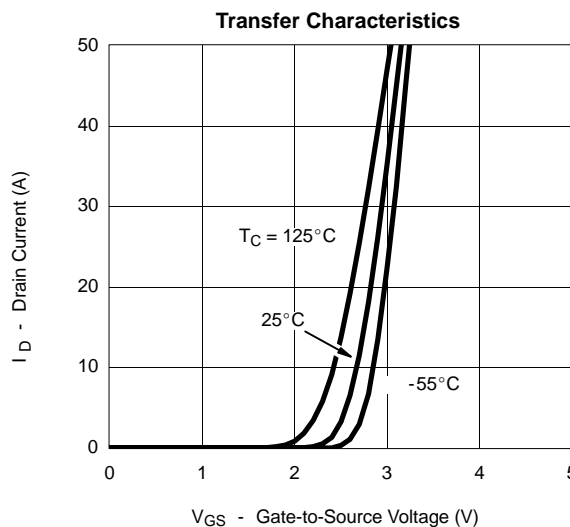
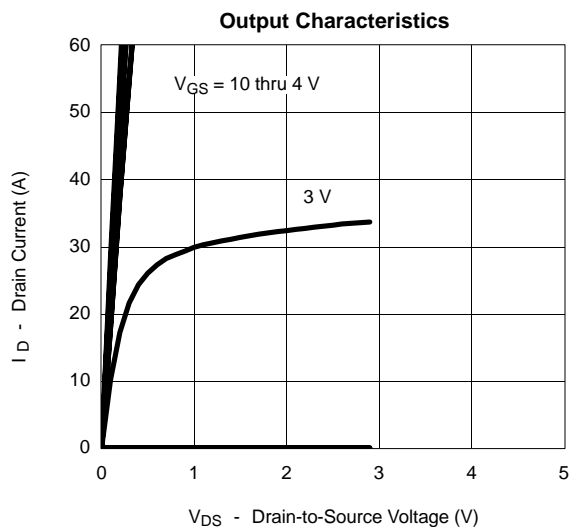


SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.0			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24 V, V _{GS} = 0 V			1	μA
		V _{DS} = 24 V, V _{GS} = 0 V, T _J = 55 °C			5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	30			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 25 A		0.0037	0.0045	Ω
		V _{GS} = 4.5 V, I _D = 22 A		0.0048	0.006	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 25 A		80		S
Diode Forward Voltage ^a	V _{SD}	I _S = 4.5 A, V _{GS} = 0 V		0.75	1.2	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 25 A		25	35	nC
Gate-Source Charge	Q _{gs}			6.7		
Gate-Drain Charge	Q _{gd}			9.7		
Gate Resistance	R _g		0.5		2.4	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 15 Ω I _D ≅ 1 A, V _{GEN} = 10 V, R _G = 6 Ω		17	30	ns
Rise Time	t _r			10	20	
Turn-Off Delay Time	t _{d(off)}			65	130	
Fall Time	t _f			35	60	
Source-Drain Reverse Recovery Time	t _{rr}		I _F = 2.9 A, di/dt = 100 A/μs		50	

Notes

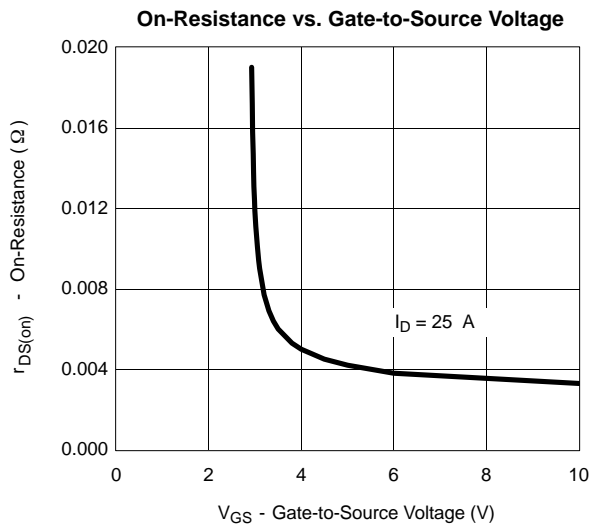
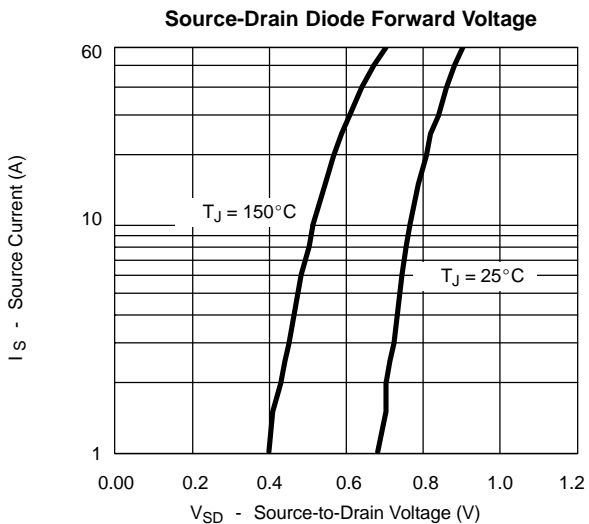
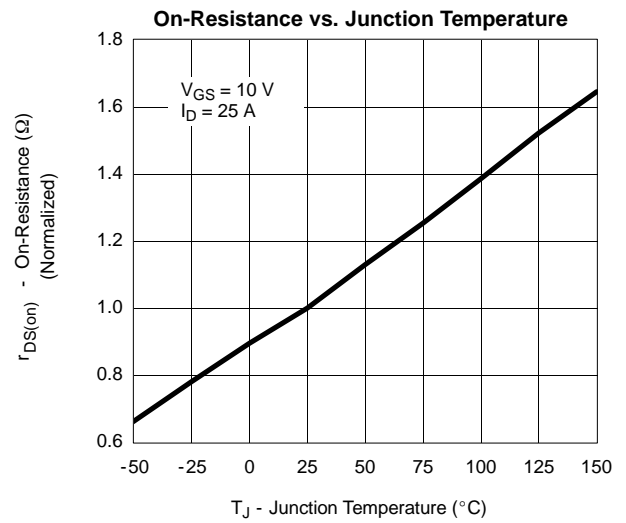
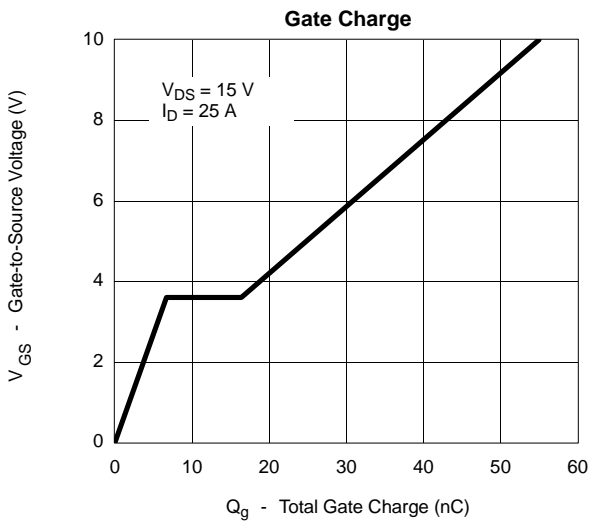
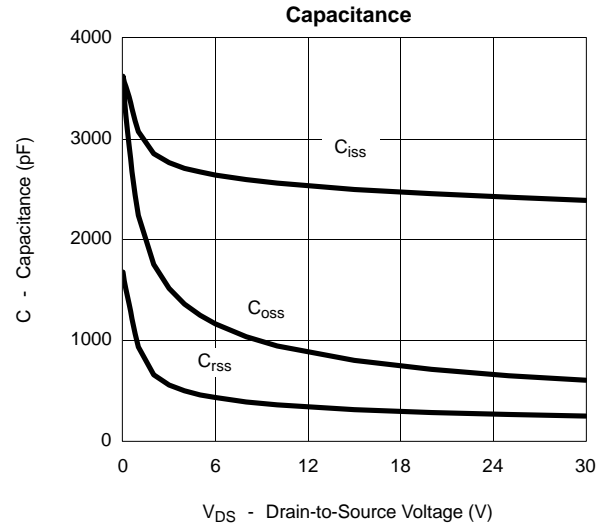
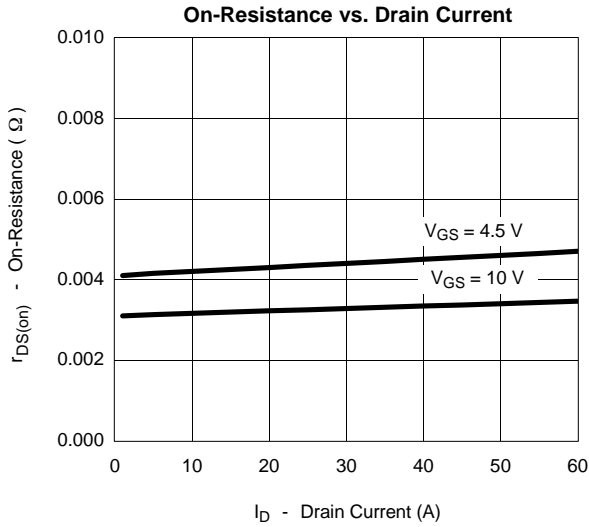
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)





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