



SiF912EDZ
Vishay Siliconix

Bi-Directional N-Channel 30-V (D-S) MOSFET

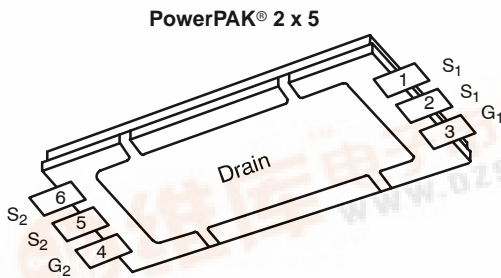
PRODUCT SUMMARY		
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)
30	0.019 @ V _{GS} = 4.5 V	10.7
	0.0195 @ V _{GS} = 4.0 V	10.5
	0.022 @ V _{GS} = 3.1 V	9.9
	0.027 @ V _{GS} = 2.5 V	9.0

FEATURES

- TrenchFET® Power MOSFET: 2.5-V Rated
- ESD Protected: 3000 V

APPLICATIONS

- Battery Protection Circuitry
- 1-Cell Li-Ion Battery Pack
 - LiB/LiP
 - Lithium-Polymer

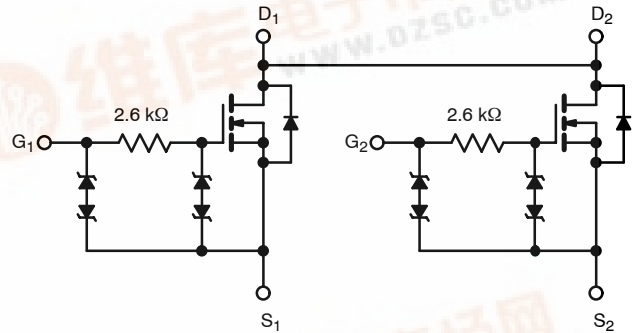


Ordering Information: SiF912EDZ-T1—E3

Marking Code

MCXYZ

MC: Part # Code
XYZ: Lot Traceability and Date Code



ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C UNLESS OTHERWISE NOTED)

Parameter	Symbol	10 secs	Steady State	Unit	
Drain-Source Voltage	V _{DS}	30		V	
Gate-Source Voltage	V _{GS}	±12			
Continuous Drain Current (T _J = 150 °C) ^a	I _D	T _A = 25 °C	10.7	7.4	A
		T _A = 85 °C	7.7	5.3	
Pulsed Drain Current (V _{GS} = 8 V)	I _{DM}	80			
Continuous Diode Current (Diode Conduction) ^a	I _S	2.9	1.3		
Maximum Power Dissipation ^a	P _D	T _A = 25 °C	3.5	1.6	W
		T _A = 85 °C	1.8	0.86	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150		°C	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R _{thJA}	t ≤ 10 sec	30	36	°C/W
		Steady State	61		
Maximum Junction-to-Case (Drain)	R _{thJC}	4.5	5.6		

Notes:
a. Surface Mounted on 1" x 1" FR4 Board.



SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

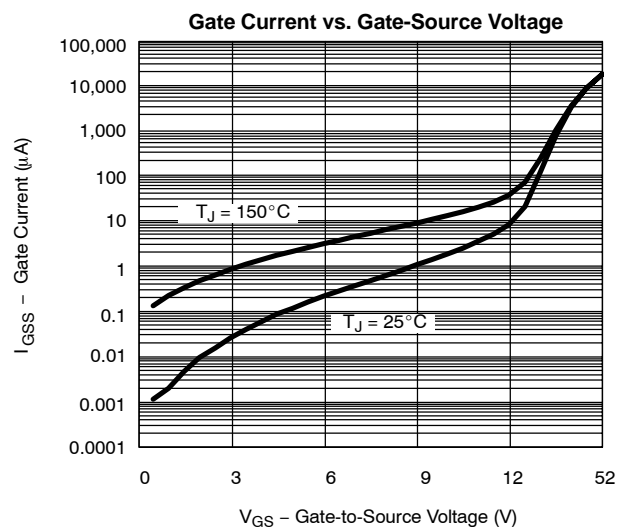
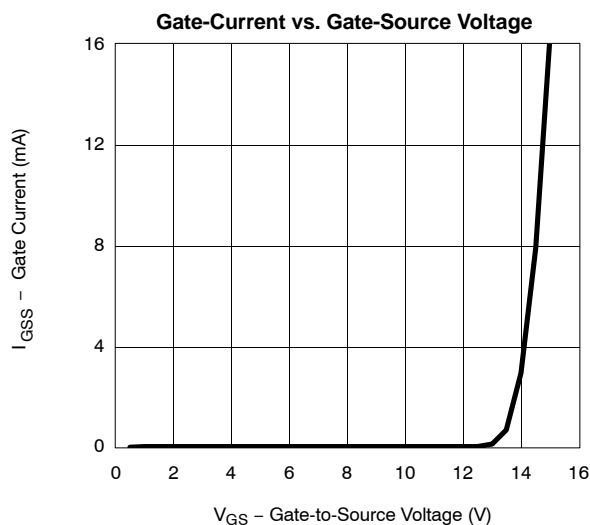
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.6		1.5	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±4.5 V			±10	μA
		V _{DS} = 0 V, V _{GS} = ±12 V			±500	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 85 °C			5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 4.5 V	40			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 4.5 V, I _D = 7.4 A		0.0155	0.019	Ω
		V _{GS} = 4.0 V, I _D = 7.3 A		0.016	0.0195	
		V _{GS} = 3.1 V, I _D = 6.8 A		0.018	0.022	
		V _{GS} = 2.5 V, I _D = 3.5 A		0.022	0.027	
Forward Transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 7.4 A		37		S
Diode Forward Voltage ^a	V _{SD}	I _S = 2.9 A, V _{GS} = 0 V		0.75	1.1	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 7.4 A		9.8	15	nC
Gate-Source Charge	Q _{gs}			2.5		
Gate-Drain Charge	Q _{gd}			2.9		
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 15 Ω I _D ≅ 1 A, V _{GEN} = 10 V, R _g = 6 Ω		0.53	0.8	μs
Rise Time	t _r			0.70	1.1	
Turn-Off Delay Time	t _{d(off)}			8.0	12	
Fall Time	t _f			3.4	5	

Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

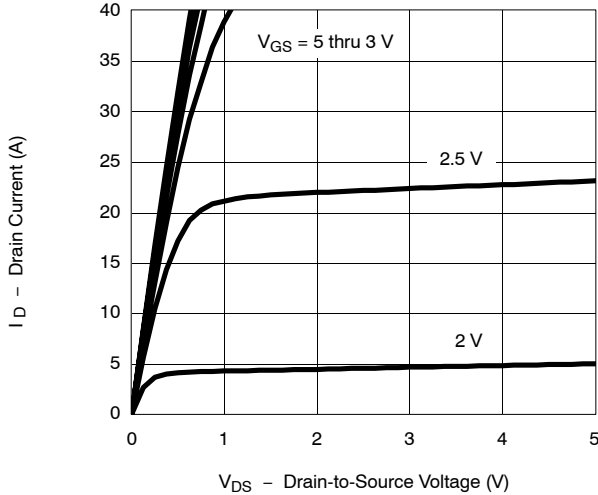
TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)



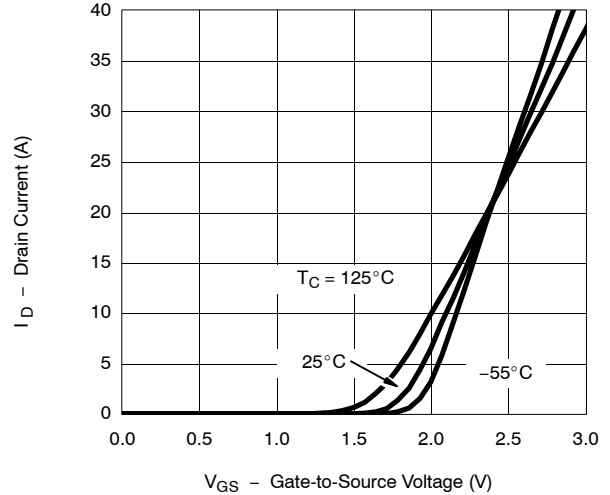


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

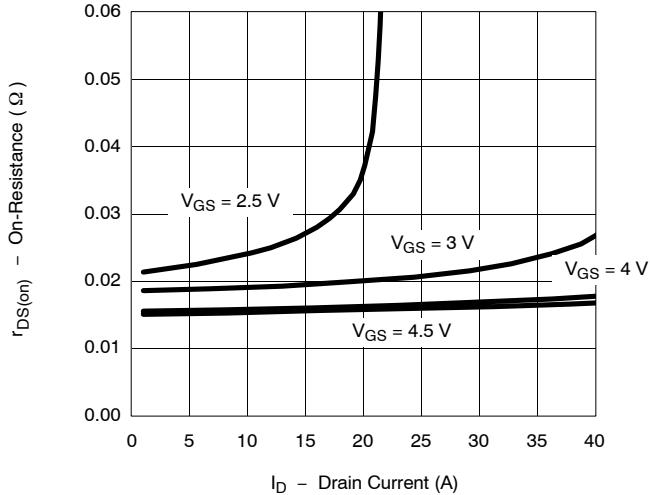
Output Characteristics



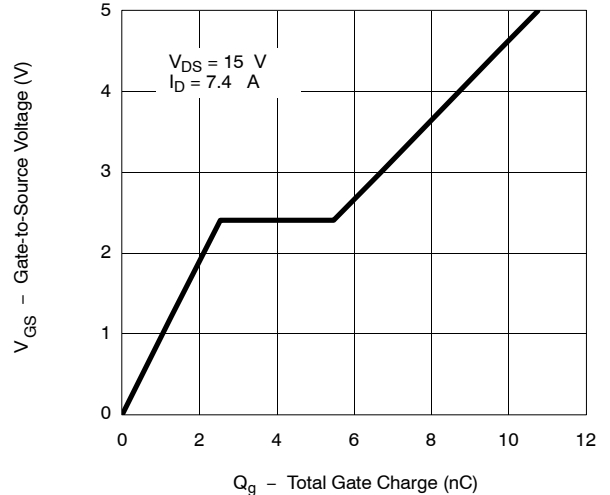
Transfer Characteristics



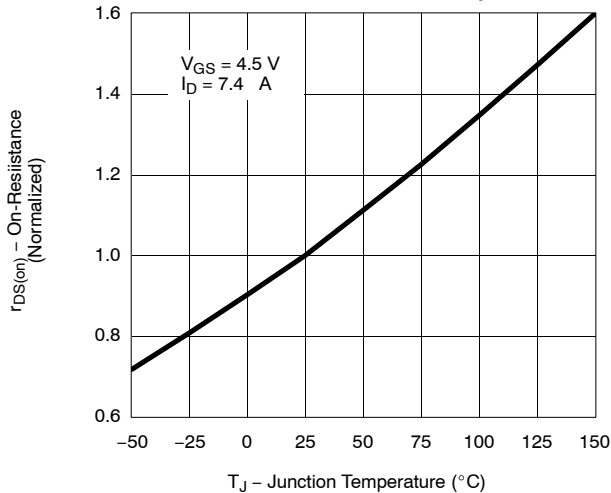
On-Resistance vs. Drain Current



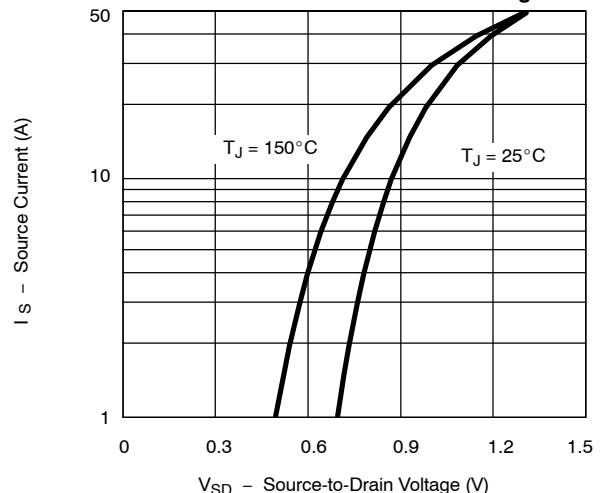
Gate Charge



On-Resistance vs. Junction Temperature

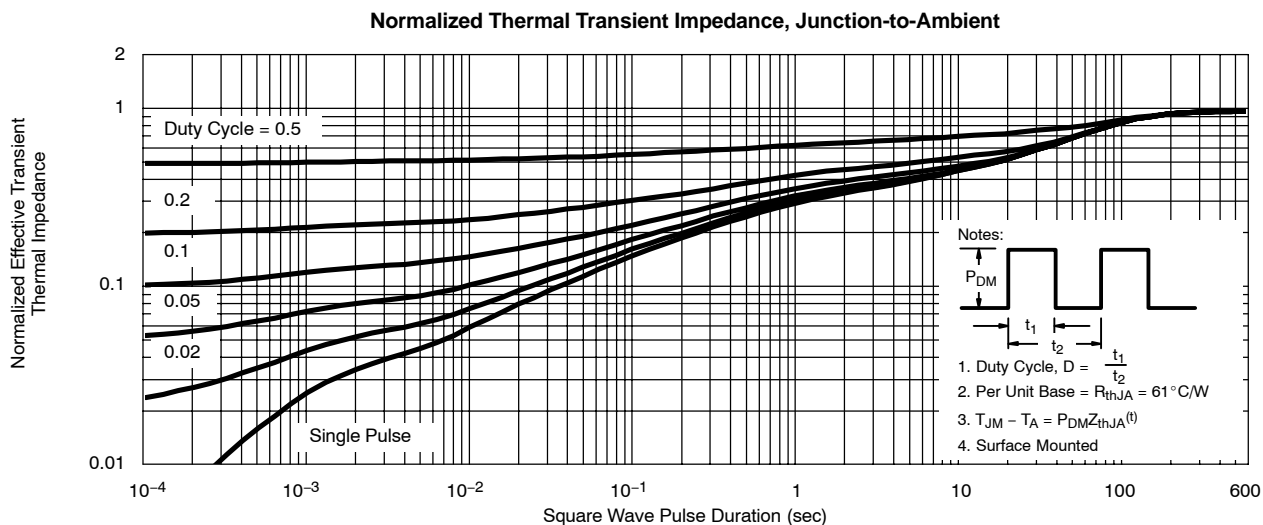
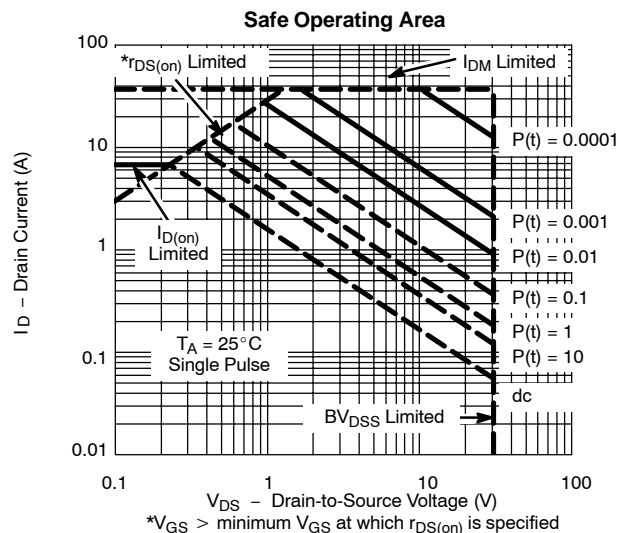
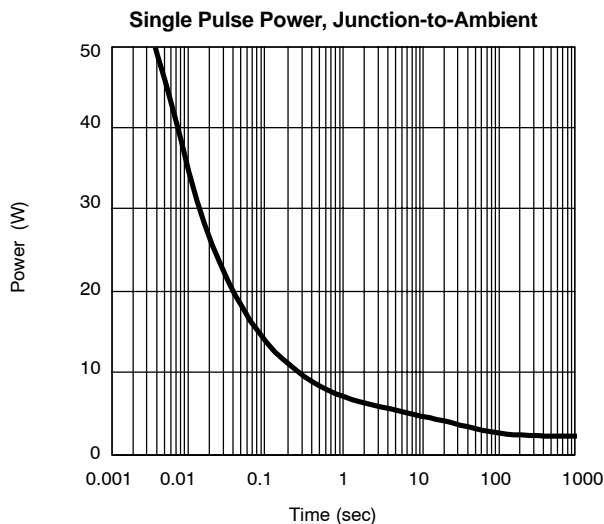
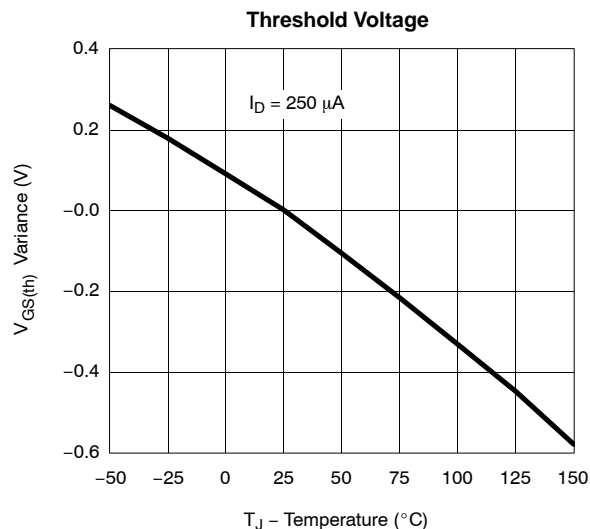
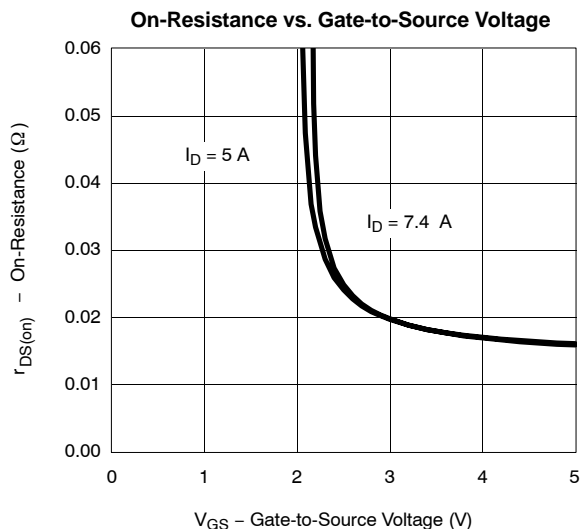


Source-Drain Diode Forward Voltage



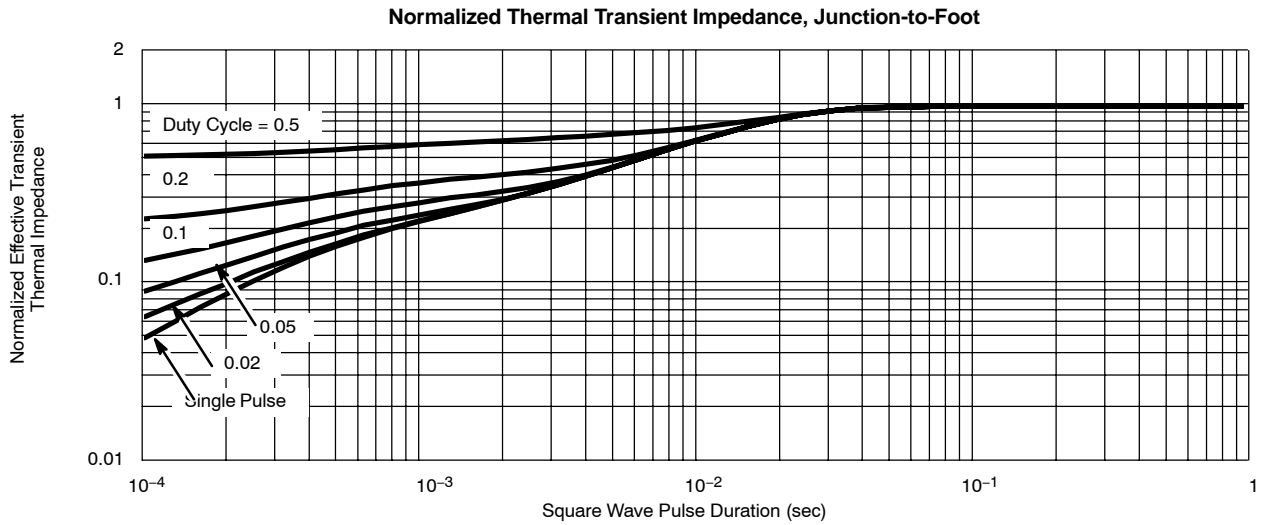


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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?72952>.