

No.4318

2SJ308
P-Channel MOS Silicon FET

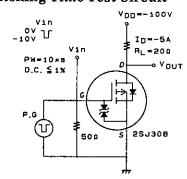
Very High-Speed Switching Applications

Features

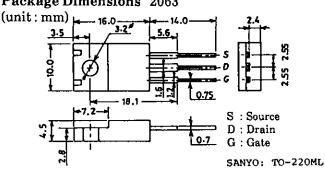
- · Low ON resistance.
- · Very high-speed switching.
- · Low-voltage drive.
- · Micaless package facilitating mounting.

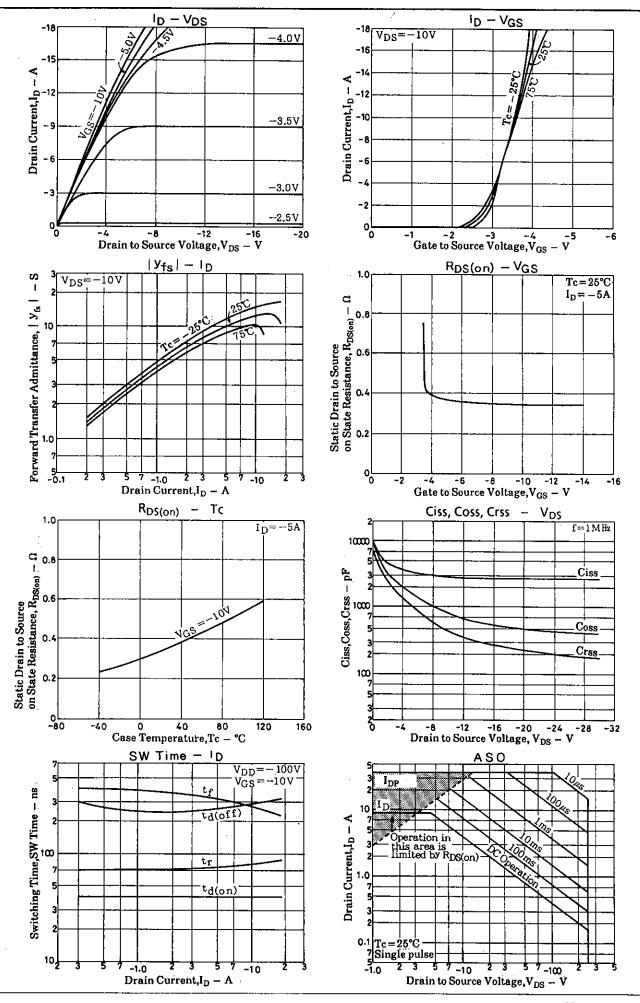
Absolute Maximum Ratings at Ta = 25°C				unit			
Drain to Source Voltage	V_{DSS}		-250 V				
Gate to Source Voltage	V_{GSS}		;	± 30	V		
Drain Current(DC)	I_D			-9	Α		
Drain Current(Pulse)	I_{DP}	PW≦10μs, duty cycle≦1%	-	- 36	Α		
Allowable Power Dissipation	$\mathbf{P_D}$			2.0	W		
		Tc = 25°C		40	W		
Channel Temperature	Tch		150 °C		$^{\circ}\mathrm{C}$		
Storage Temperature	Tstg		-55 to +150		°C .		
Electrical Characteristics at Ta =	25°C		min	typ	max	unit	
D-S Breakdown Voltage	V(BR)DSS	$I_D = -1 \text{mA}_1 V_{GS} = 0$	- 250	V P		V	
G-S Breakdown Voltage		$I_{G} = \pm 100 \mu A, V_{DS} = 0$	±30			v	
Zero Gate Voltage	IDSS	$V_{DS} = -250V_1V_{GS} = 0$			-100	$\mu { m A}$	
Drain Current	200	, 00				,	
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 25 V_1 V_{DS} = 0$			±10	μ A	
Cutoff Voltage	V _{GS(off)}	$V_{DS} = -10V, I_{D} = -1mA$	-1.5		-2.5	' V	
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10V, I_D = -5A$	6	10		S	
Static Drain to Source	$R_{DS(on)}$	$I_D = -5A, V_{GS} = -10V$		0.35	0.48	Ω	
on State Resistance	, , , , , , ,						
Input Capacitance	Ciss	$V_{DS} = -20V, f = 1MHz$	2700		рF		
Output Capacitance	Coss	$V_{DS} = -20V, f = 1MHz$	500			pF	
Reverse Transfer Capacitance	Crss	$V_{DS} = -20V, f = 1MHz$	225		рF		
Turn-ON Delay Time	${ m t_{d(on)}}$	See specified Test Circuit.		40		ns	
Rise Time	t_r	"		75		ns	
Turn-OFF Delay Time	${ m t_{d(off)}}$	"		320		ns	
Fall Time	t_f	<i>"</i>		260		ns	
Diode Forward Voltage	${ m v_{SD}}$	$I_{S} = -9A, V_{GS} = 0$		-1.0	-1.5	V	

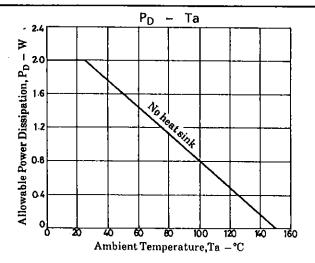
Switching Time Test Circuit

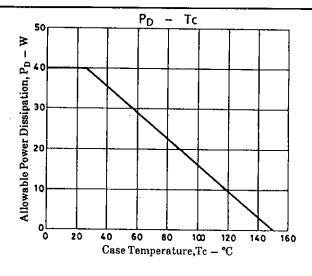


Package Dimensions 2063









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