



# SL1027P

DUAL PREAMPLIFIER WITH ALC

## Description

- The SL1027P is a monolithic integrated circuit consisting of two independent preamplifier with two built-in ALC, and it has quick stabilization during power supply input.

## Features

- High voltage gain
- Excellent channel separation
- Low noise
- Direct motor drive obtained through SEPP output stage.
- Good ALC channel balance
- Operating supply voltage range :  $V_{CC} = 5V \sim 13V$
- Load resistance :  $R_L > 680\Omega$

## Application

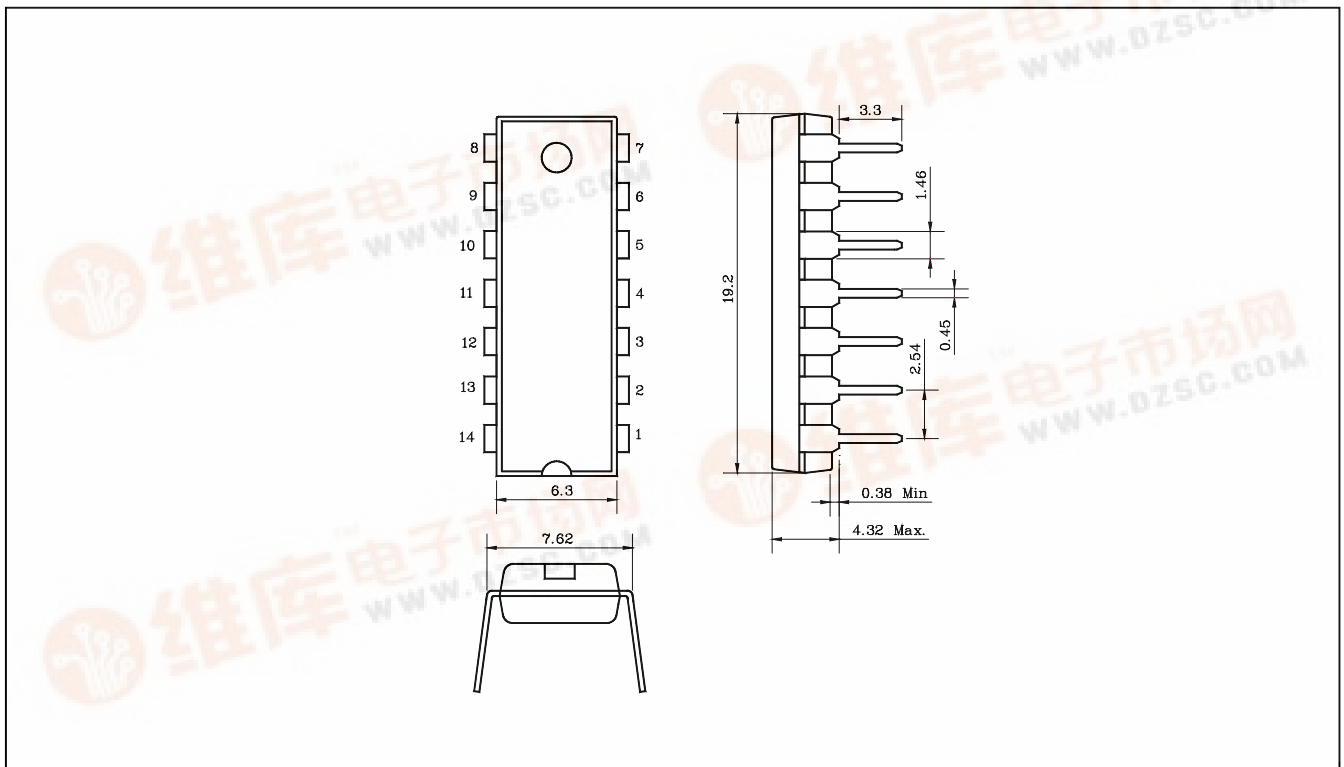
- Stereo radio cassettes

## Ordering Information

Type NO.	Marking	Package Code
SL1027P	SL1027	DIP14

## Outline Dimensions

unit : mm



## Maximum Ratings (Ta = 25°C)

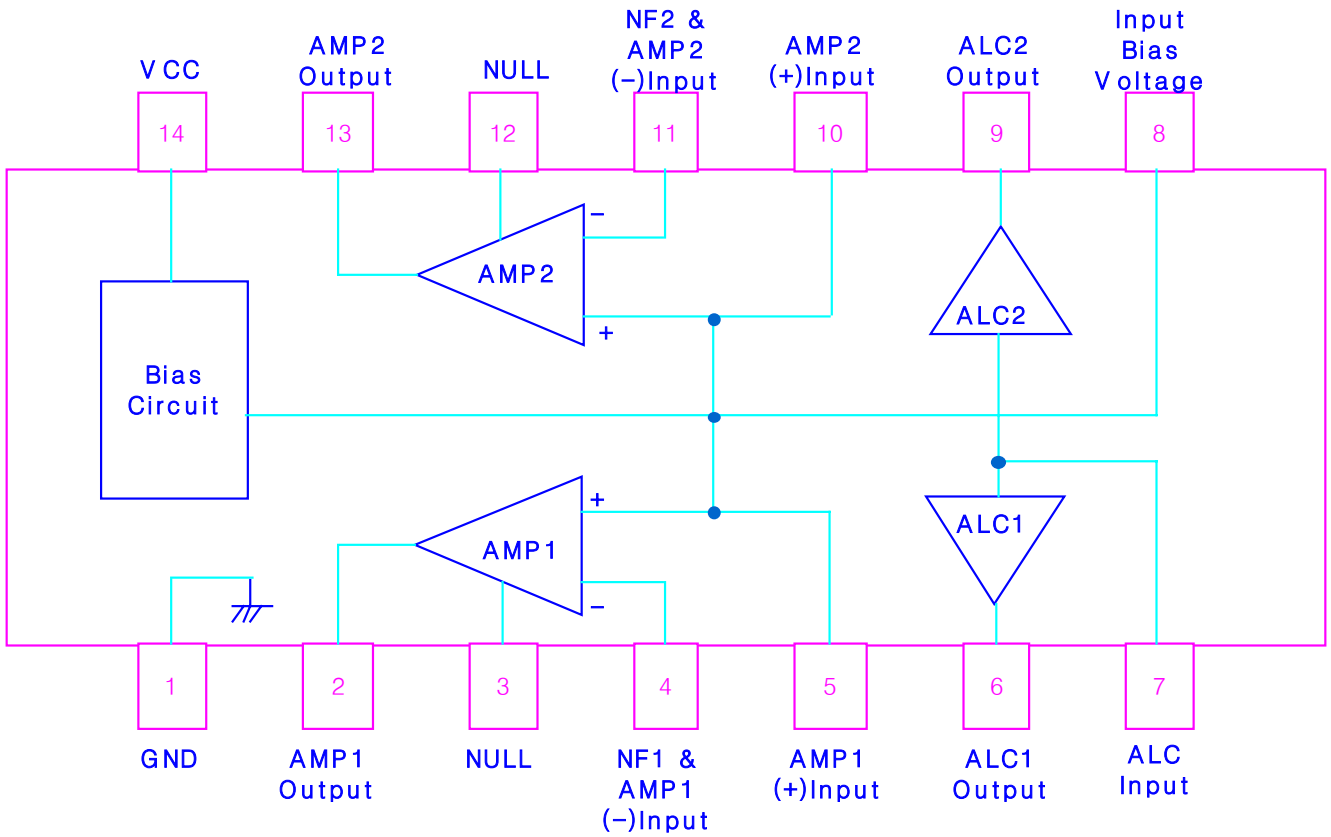
Characteristics	Symbol	Rating	Unit
Supply Voltage	V <sub>CC</sub>	14	V
Power Dissipation	P <sub>D</sub>	600	mW
Operating Temperature	T <sub>opr</sub>	- 25 ~ + 75	°C
Storage Temperature	T <sub>stg</sub>	- 40 ~ + 125	°C

## Electrical Characteristics

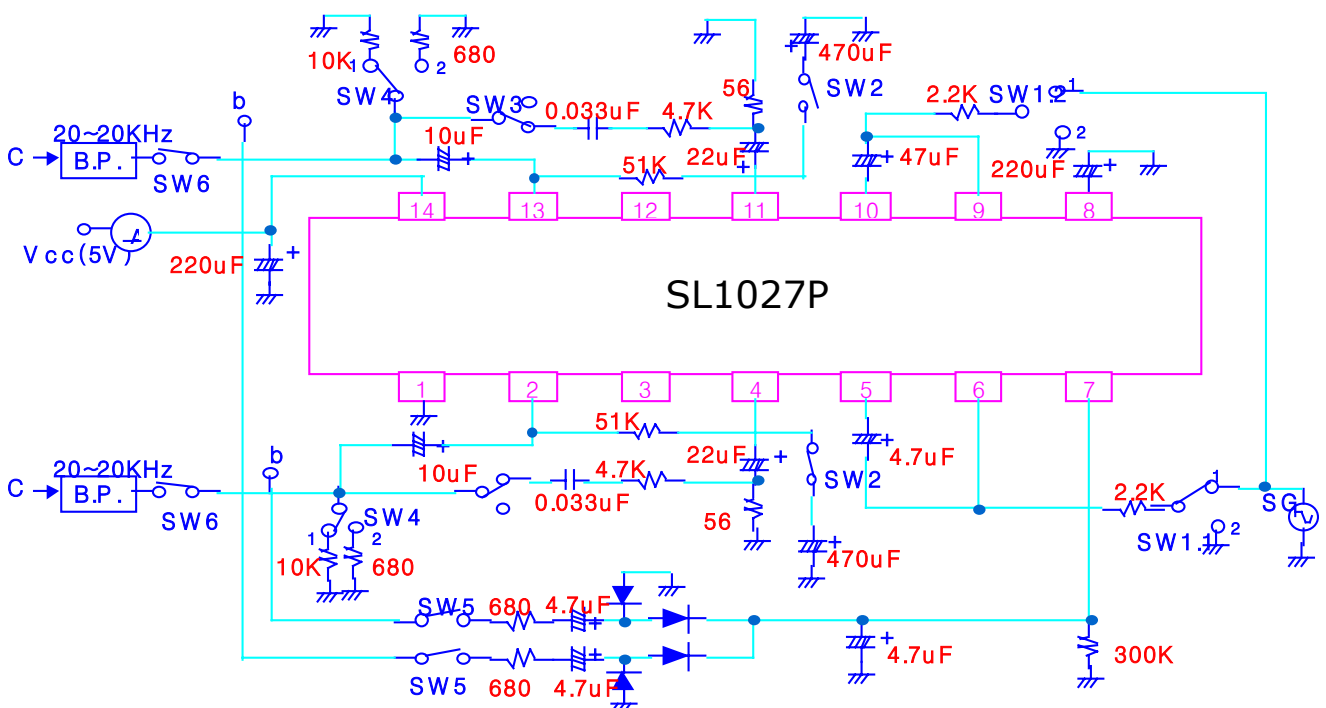
(Unless otherwise specified, Ta=25°C , V<sub>CC</sub>=5V, f=1KHz, R<sub>L</sub> = 10KΩ (PLAY), R<sub>L</sub> = 680Ω (REC))

Characteristics	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Quiescent Supply Current	I <sub>CCQ</sub>	-	-	4.5	10	mA
Open-loop Voltage Gain	G <sub>VO</sub>	-	-	85	-	dB
Closed-loop Voltage Gain	G <sub>V</sub>	PLAY	-	40	-	dB
		REC	-	58	-	dB
Maximum Output Voltage	V <sub>OM</sub>	THD = 1%, PLAY	0.9	1.2	-	V
Total Harmonic Distortion	THD	V <sub>O</sub> = 0.5V, PLAY	-	0.1	1	%
Input Resistance	R <sub>IN</sub>	-	21	30	-	KΩ
Equivalent Input Noise Voltage	V <sub>NI</sub>	B.P.F = 20Hz ~ 20KHz R <sub>g</sub> = 2.2kΩ, PLAY	-	1	2	uV
Channel Separation	CH <sub>SEP</sub>	R <sub>g</sub> =2.2kΩ V <sub>O</sub> = 0dBm,PLAY	40	50	-	dB
ALC Channel Balance	ACB	V <sub>IN</sub> =-20dBm,REC.	-	0	2	dB
ALC Width	AW	V <sub>IN</sub> =-60dBm,REC.	35	45	-	dB
ALC Distortion	AD	V <sub>IN</sub> =-20dBm,REC.	-	0.5	2	%

Block Diagram



Test Circuit



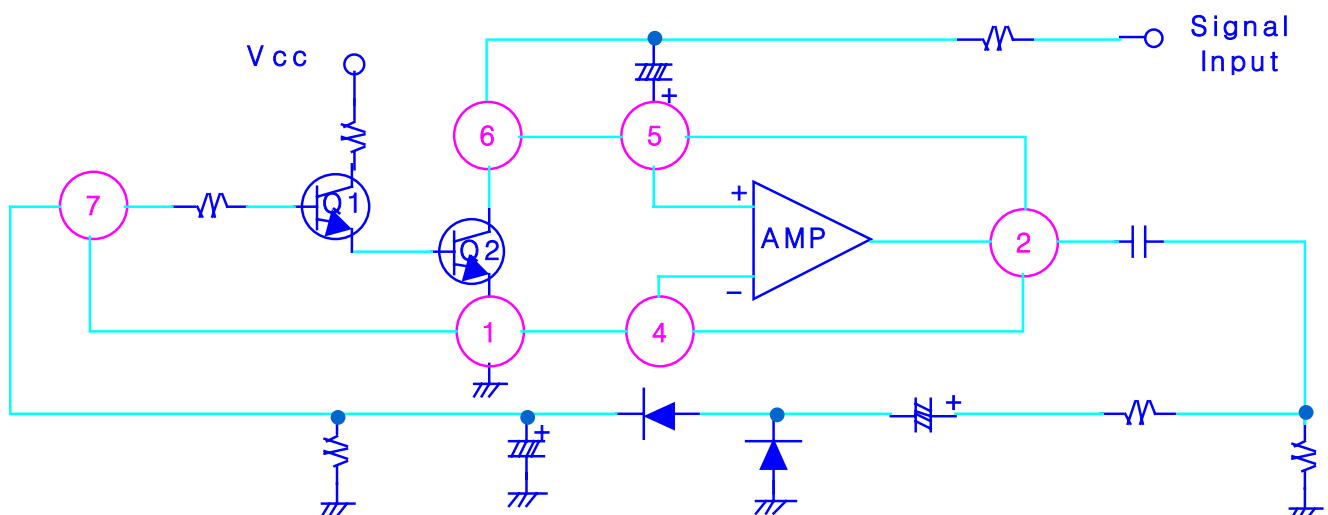
**Test Method**

Sym- bol	Switch Position						Measurement Location	Test Method
	SW 1	SW 2	SW 3	SW 4	SW 5	SW 6		
I <sub>CCQ</sub>	2	OFF	OFF	1	OFF	OFF	-	Read ammeter
G <sub>VO</sub>	1	ON	OFF	1	OFF	OFF	a, b	$G_{VO} = 20\log(V_O / V_{IN})$ [dB]
G <sub>V</sub>	1	OFF	ON	1	OFF	OFF	a, b	$G_V = 20\log(V_O / V_{IN})$ [dB]
V <sub>OM</sub>	1	OFF	ON	1	OFF	OFF	b	Measure output voltage(V <sub>O</sub> ) at THD=1%
THD	1	OFF	ON	1	OFF	OFF	b	Measure distortion factor at V <sub>O</sub> =0.5V
CH <sub>SEP</sub>	SW1-1 1→2 SW1-2 2→1	OFF	ON	1	OFF	OFF	b	Measure crosstalk of amp 1,2 at output voltage V <sub>O</sub> = 0dBm
V <sub>NI</sub>	2	OFF	ON	1	OFF	ON	c	Convert output noise voltage at 1KHz gain when R <sub>g</sub> = 2.2KΩ
AW	1	OFF	OFF	2	ON	OFF	b	Input voltage range from when input voltage V <sub>IN</sub> =-60dBm until output voltage V <sub>O</sub> goes up 3dB
ACB	1	OFF	OFF	2	ON	OFF	b	Output voltage V <sub>O</sub> level difference of amp 1,2 when input voltage V <sub>IN</sub> = -20dBm is applied.
AD	1	OFF	OFF	2	ON	OFF	b	Measure distortion factor when input voltage V <sub>IN</sub> = -20dBm is applied.

**Information of Operating**

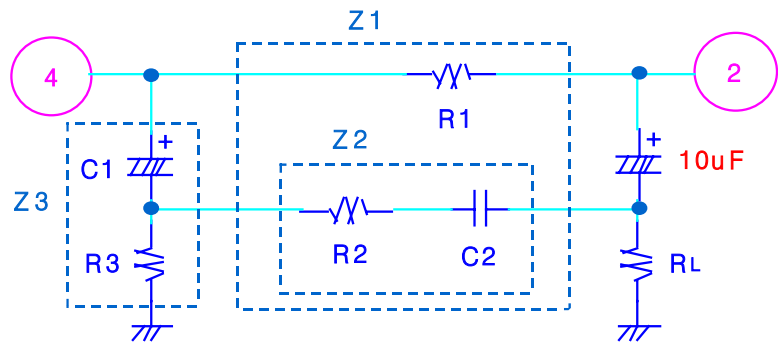
1. ALC Circuit

The ALC circuit is consist of Q1, Q2 and some external components. The output level of amplifier is rectified by external circuits and this rectified DC level is applied to the ALC input terminal(Pin 7).



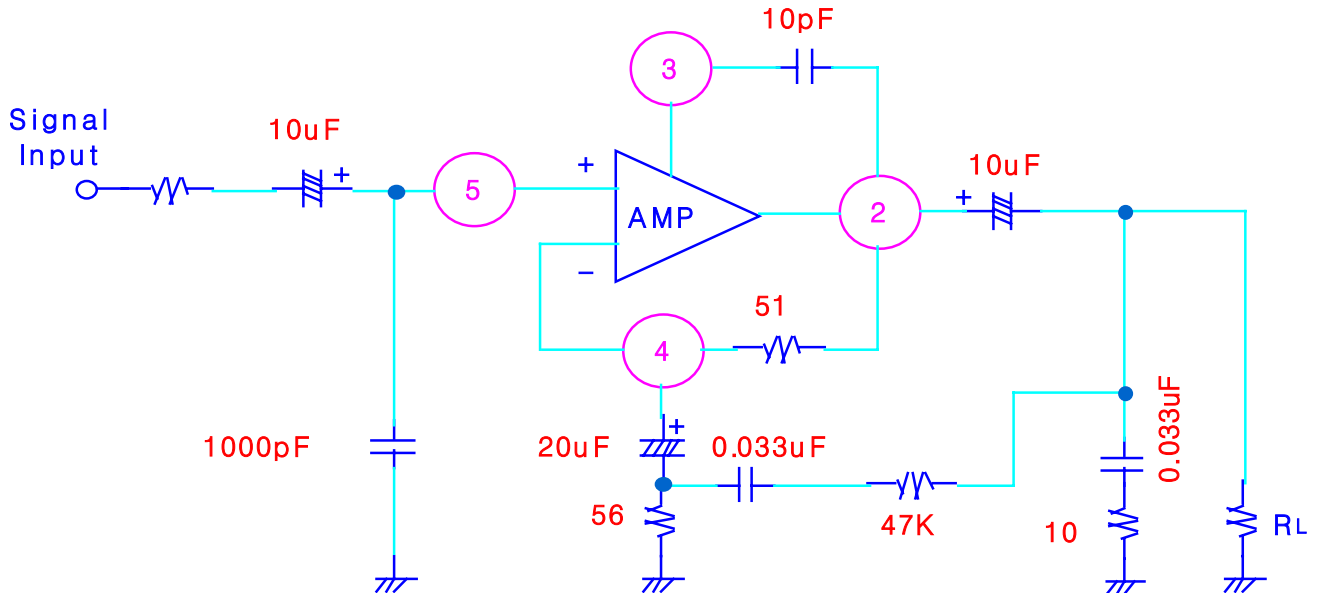
2. Closed-loop Gain (f=1kHz)

- Closed loop voltage gain  
 $G_V = 20 \log(Z1/Z3)$  (dB)
- Playback amplifier gain  
 $G_V = 20 \log(R1/R3)$  (dB)  
 in low frequency  
 $G_V = 20 \log(Z1/Z3)$  (dB)  
 in high frequency
- Recording amplifier gain  
 $G_V = 20 \log(R1/R3)$  (dB)

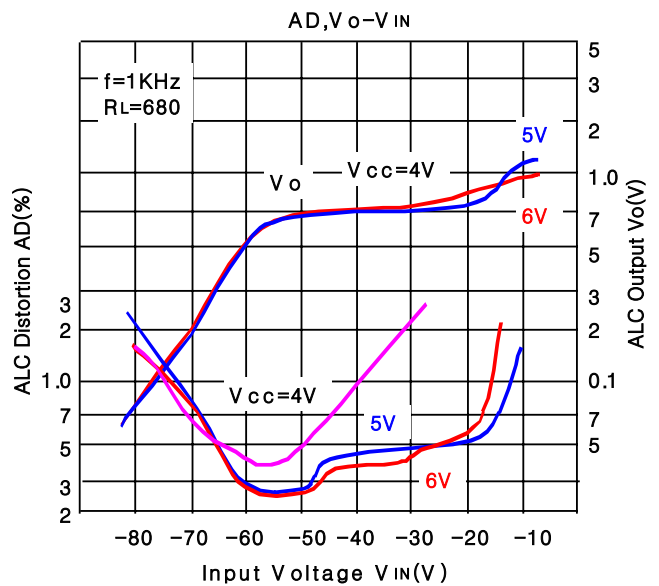
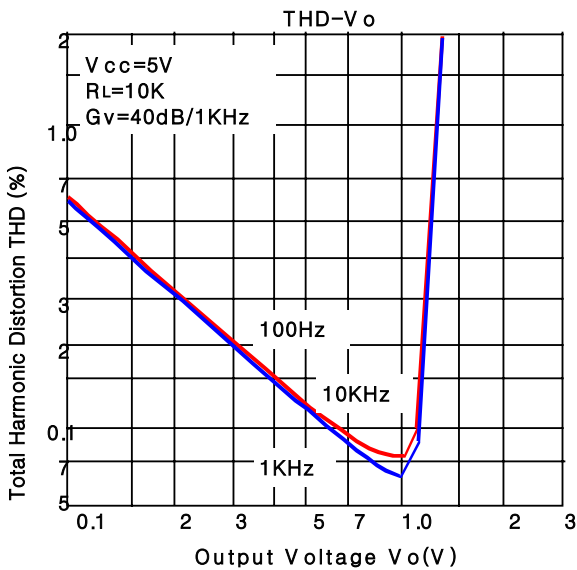
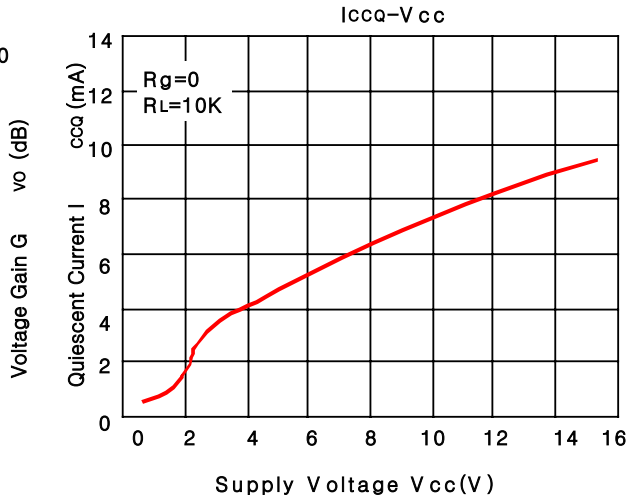
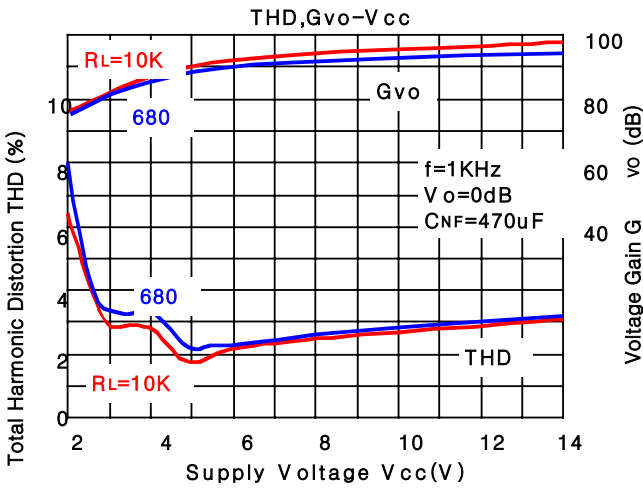
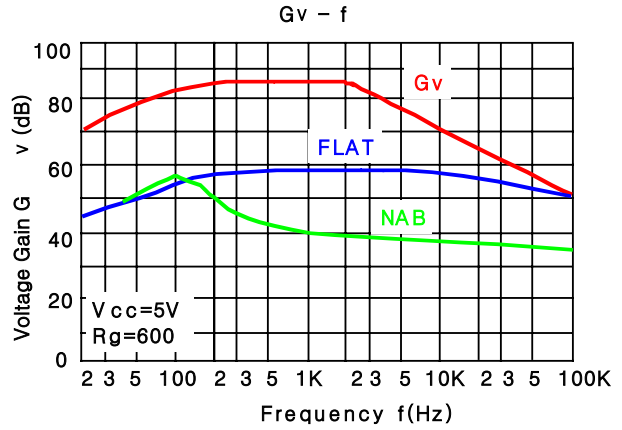
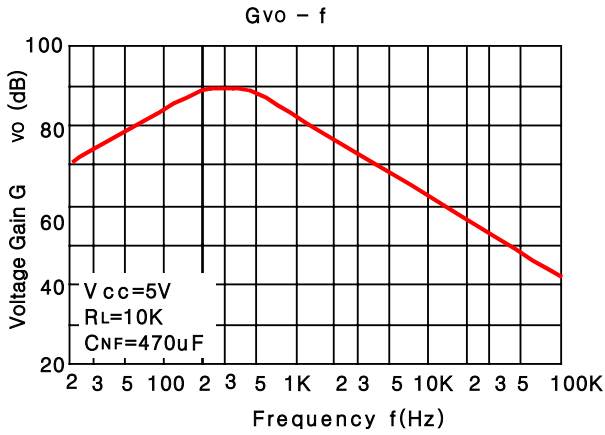


3. Oscillation

When the closed-loop gain of amplifier is lowered, oscillation will occur. So the circuit should be compensated by connecting of 10pF between pin 3 and pin 2, and 0.03  $\mu\text{F}$  in series with 10  $\Omega$  to the load end.



Typical Performance Characteristics



Application Circuit

