

SL1914

Satellite Tuner Front End LNA with AGC

Preliminary Information

DS5137

Issue no 1.6

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Features

- Single chip solution for satellite tuner LNA and AGC
- Provides for 30 dB minimum AGC
- Low DSB NF at maximum gain
- High signal handling at minimum gain
- Provides differential output drive
- ESD protection (Normal ESD handling procedures should be observed)

Applications

- Satellite receiver systems
- Data communications systems
- Master Antennae Distribution Systems

Ordering Information 6/MP1S 6/MP1T

SL1914A/KG/MP1S SL1914A/KG/MP1T

Description

The SL1914 is a wideband LNA with AGC designed primarily for application in satellite tuner front ends, offering high signal handling capability with low noise figure compatible with most common tuner input requirements.

The SL1914 is optimised to complement the SL1925, ZIF quadrature downconverter, integrating all the active RF circuitry within the tuner, in a highly compact, efficient solution, and offering a full 1-45 MS/s tuner capability.

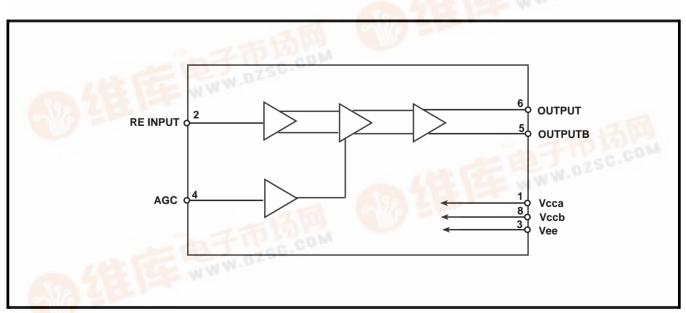


Figure 1 Block Diagram



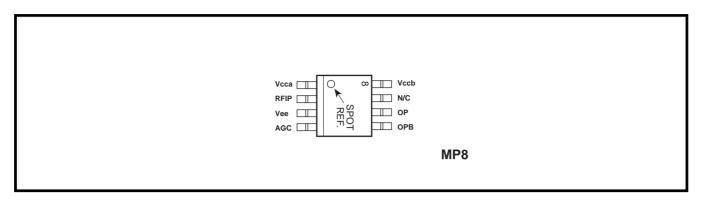


Figure 2 Pin Connections

Quick Reference Data

Characteristic		Units
Operating frequency	950 - 2150	MHz
Input DSB NF at max gain	8	dB
Maximum conversion gain	20	dB
Minimum conversion gain	-10	dB
IPIP3 _{2T}	117	dBμV
IPIP2 _{2T}	127	dBμV

Functional Description

The SL1914 is a bipolar, low noise AGC amplifier designed primarily for application in satellite tuner front ends. It contains a low noise input amplifier, an AGC stage with a minimum of 30dB of gain control and a 75 Ohm output drive. It replaces all active circuitry in conventional architectures.

The typical key performance numbers under nominal load ambient and supply conditions are contained in table headed Quick Reference Data.

Electrical Characteristics

 T_{amb} = -20 $^{\circ}$ C to +70 $^{\circ}$ C, V_{ee} = 0V, Vcc = 5V±5%, These characteristics are guaranteed by either production test or design. They apply within the specified ambient temperature and supply voltage unless otherwise stated.

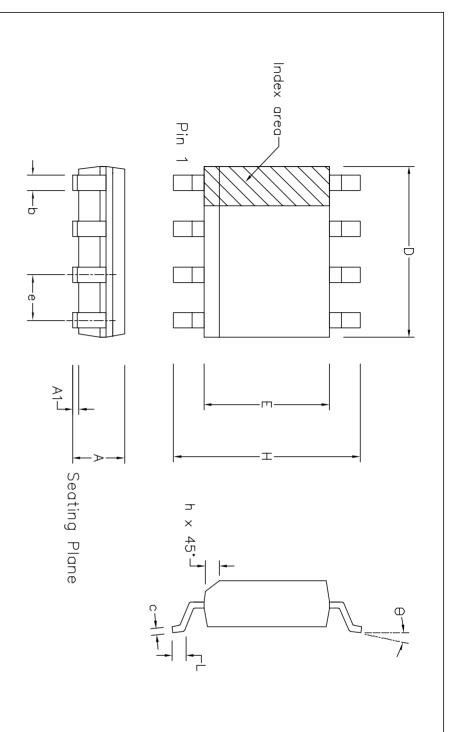
Characteristic	Pin		Value		Units	Conditions
		Min	Тур	Max		
Supply current	1, 8		110	150	mA	AGC = 4V
Operating range		950		2150	MHz	
Input impedance	2		75		Ω	
IF input return loss	2	8			dB	
Input DSB NF	2		7	9	dB	Maximum gain, AGC = 1V
Variation in NF with gain setting				-1	dB/dB	
Conversion gain						Differential power gain into 75 Ω
minimum				-10	dB	Vagc = 4V See note 1.
maximum		21			dB	Vagc = 1V
					dB	AGC monotonic from Vee to Vcc
Gain variation within				+0.5	dB	Maximum channel bandwidth of
channel						54MHz, within range 950 - 2150MHz
Input referred 1 dB gain compression	2	107			dΒμV	Minimum specified gain setting
Input referred IP3	2	117			dΒμV	Minimum specified gain setting,
input referred in 5	2	'''			αυμν	two tones at 99dBµV
Input referred IP2	2	125			dΒμV	Minimum specified gain setting,
input forcifica ii 2		123			αΒμν	two tones at 99dBμV
Variation in second and				-1	dB/dB	two tones at 990bµv
third order intermodulation intercept points with gain setting						
AGC control slope	4			4:1		
variation						
AGC control input current	4			±350	μΑ	
Output impedance	5, 6		75		Ω	
Output return loss	5, 6	6			dB	

Note 1: The AGC voltage should not exceed 4V.

Absolute Maximum Ratings

All voltages referred to Vee at 0V.

Characteristics	Min	Max	Unit	Conditions
Supply voltage	-0.3	7	V	
IFIN and IFINB input voltage		117	dΒμV	Transient condition only
All I/O port DC offset	-0.3	VCC+0.3	V	Except AGC (pin 4)
AGC input DC offset	-0.3	4	V	
Storage temperature	-55	150	°C	
Junction temperature		150	°C	
Package thermal resistance,		39.8	°C/W	
chip to case				
Package thermal resistance,		120	°C/W	
chip to ambient				
Power consumption at 5.25V		790	mW	
ESD protection	2		kV	Mil std-883 latest revision
				method 3015 class 1



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onforms to JEDEC MS-012AA Iss.	8		0.25	0.0	0.19	0.33	1.27	0.40	3.80	5.80	4.80	0.10	1.35	mm	≤in
DEC MS-		Pin Fe	0.50	∞ 2	0.25	0.51	BSC	1.27	4.00	6.20	5.00	0.25	1.75	mm	Max
-012AA		Pin Features	0.010	0°	0.008	0.013	0.050	0.016	0.150	0.228	0.189	0.004	0.053	inch	⊠. Sin
ss. C	00		0.020	∞2	0.010	0.020	BSC	0.050	0.157	0.244	0.197	0.010	0.069	inch	Max

Notes:

- 1. The chamfer on the body is optional. If it not present, a visual index feature, e.g. a dot, must be located within the cross—hatched area.
- Controlling dimension are in inches.

 Dimension D do not include mould flash, protusion or gate burrs. These shall not exceed 0.006" per side.

 Dimension E1 do not include inter—lead flash or protusion. These shall not exceed 0.010" per side.

 Dimension b does not include dambar protusion/intrusion. Allowable dambar protusion shall be 0.004"
- total in excess of b dimension.

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ISSUE	-	2	3	4			Title: Package Outline Drawing for
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