

TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

SM8LZ47

AC POWER CONTROL APPLICATIONS

• Repetitive Peak Off-State Voltage $: V_{\rm DRM} = 800 V$ • R.M.S ON-State Current $: I_{\rm T(RMS)} = 8A$

• High Commutating (dv / dt) $: (dv / dt) c = 10V / \mu s (Min.)$

• Isolation Voltage $: V_{ISOL} = 1500V AC$

MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Repetitive PeakOff-State Voltage	V_{DRM}	800	V	
R.M.S On-State Current (Full Sine Waveform)	I _{T (RMS)}	8	А	
Peak One Cycle Surge On-State	1	70 (50Hz)	A	
Current (Non-Repetitive)	ITSM	80 (60Hz)		
I ² t Limit Value	I ² t	24.5	A ² s	
Critical Rate of Rise of On-State Current (Note 1)	di / dt	50	A / µs	
Peak Gate Power Dissipation	P _{GM}	5	W	
Average Gate Power Dissipation	P _G (AV)	0.5	W	
Peak Gate Voltage	V_{FGM}	10	V	
Peak Gate Current	I _{GM}	2	Α	
Junction Temperature	Tj	-40~125	°C	
Storage Temperature Range	T _{stg}	-40~125	°C	
Isolation Voltage (AC, t = 1min.)	V _{ISOL}	1500	V	

Unit: mm 0.75 ± 0.1 2.54 ± 0.25 2.54 ± 0.25 1. T1 2. T2 3. GATE **JEDEC** JEITA **TOSHIBA** 13-10H1A

Weight: 1.7g

Note: di / dt test condition

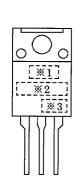
> $V_{DRM} = 400V$, $I_{TM} \le 12A$, $t_{gw} \ge 10\mu s$, $t_{gr} \le 250ns$, WWW.DZS



ELECTRICAL CHARACTERISTICS (Ta = 25°C)

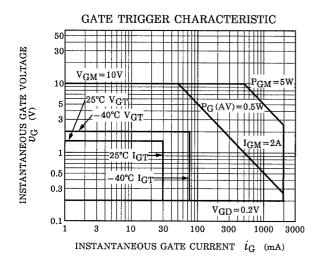
CHARACTERISTIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current		I _{DRM}	V _{DRM} = 800V		_	_	20	μA
Gate Trigger Voltage	I		V _D = 12V R _L = 20Ω	T2 (+), Gate (+)	_	_	1.5	٧
	II	V_{GT}		T2 (+), Gate (-)	_	_	1.5	
	III			T2 (-), Gate (-)	_	_	1.5	
Gate Trigger Current	I			T2 (+), Gate (+)	_	_	30	mA
	II	I _{GT}	$V_D = 12V$ $R_1 = 20\Omega$	T2 (+), Gate (-)	_	_	30	
	III			T2 (-), Gate (-)	_	_	30	
Peak On-State Voltage V _{TM}		I _{TM} = 12A		_	_	1.5	V	
Gate Non-Trigger Voltage		V_{GD}	V _D = 800V, Tc = 125°C		0.2	_	_	V
Holding Current		I _H	V _D = 12V, I _{TM} = 1A		_	_	50	mA
Thermal Resistance		R _{th (j-c)}	Junction to Case, AC		_	_	3.6	°C/W
Critical Rate of Rise of Off- State Voltage		dv / dt	V _{DRM} = 800V, T _j = 125°C Exponential Rise		_	300	_	V / µs
Critical Rate of Rise of Off- State Voltage at Commutation		(dv / dt) c	V _{DRM} = 400V, T _j = 125°C (di / dt) c = -4.5A / ms		10	_	_	V / µs

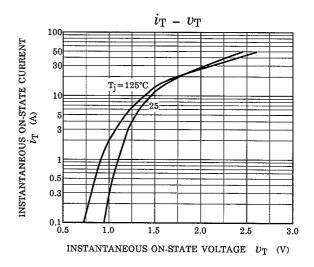
MARKING

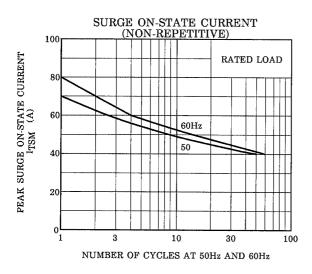


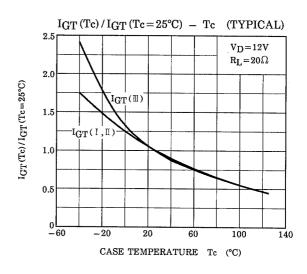
NUMBER		MARK		
* 1	TOSHIBA PRODUC	7		
* 2	TYPE	SM8LZ47	M8LZ47	
* 3		(Starting from Alphabet A) Last Decimal Digit of the Current Year	Example 8A : January 1998 8B : Febrary 1998 8L : December 1998	

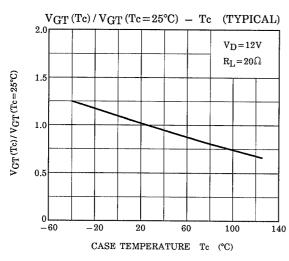
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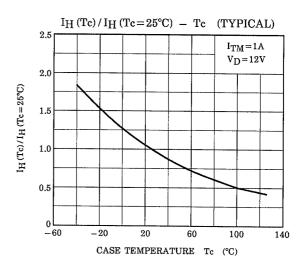




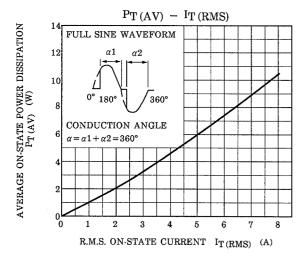


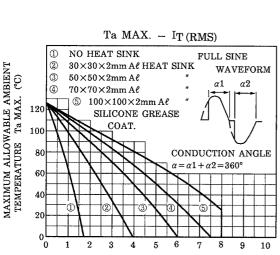






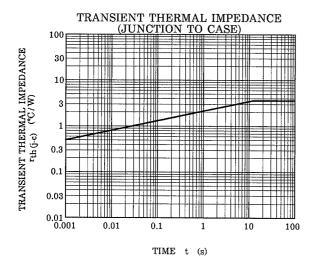
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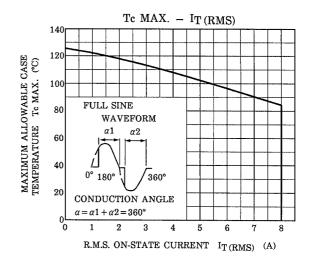


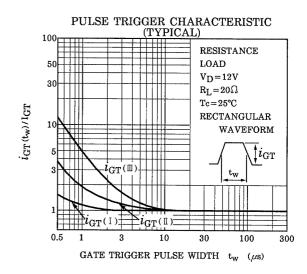


R.M.S. ON-STATE CURRENT IT (RMS) (A)

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