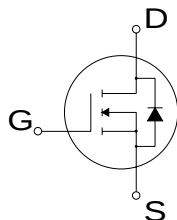
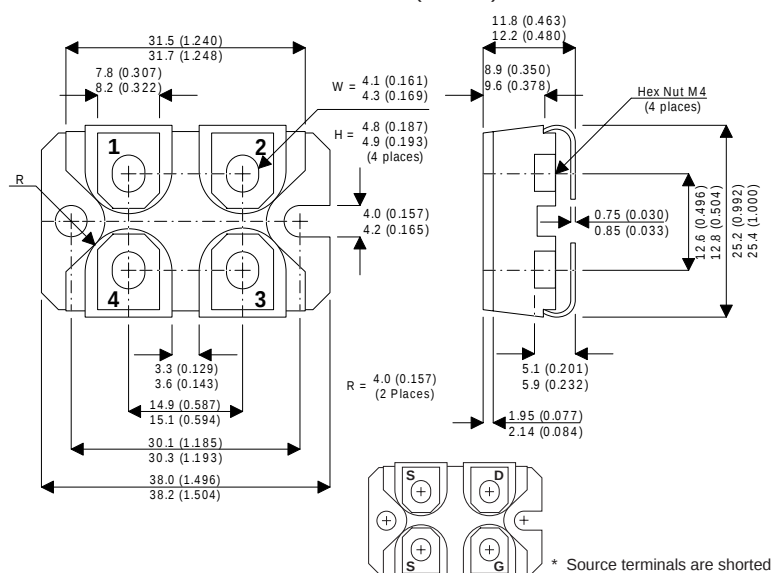


SOT-227 Package Outline.

Dimensions in mm (inches)



N-CHANNEL ENHANCEMENT MODE HIGH VOLTAGE POWER MOSFETS

V_{DSS} 1200V

$I_{D(cont)}$ 26A

$R_{DS(on)}$ 0.400Ω

- Faster Switching
- Lower Leakage
- 100% Avalanche Tested
- Popular SOT-227 Package

StarMOS is a new generation of high voltage N-Channel enhancement mode power MOSFETs. This new technology minimises the JFET effect, increases packing density and reduces the on-resistance. StarMOS also achieves faster switching speeds through optimised gate layout.

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

V_{DSS}	Drain – Source Voltage	1200	V
I_D	Continuous Drain Current	26	A
I_{DM}	Pulsed Drain Current ¹	104	A
V_{GS}	Gate – Source Voltage	±30	V
V_{GSM}	Gate – Source Voltage Transient	±40	
P_D	Total Power Dissipation @ $T_{case} = 25^{\circ}C$	700	W
	Derate Linearly	5.6	W/°C
T_J, T_{STG}	Operating and Storage Junction Temperature Range	–55 to 150	°C
T_L	Lead Temperature : 0.063" from Case for 10 Sec.	300	
I_{AR}	Avalanche Current ¹ (Repetitive and Non-Repetitive)	26	A
E_{AR}	Repetitive Avalanche Energy ¹	50	mJ
E_{AS}	Single Pulse Avalanche Energy ²	3600	

1) Repetitive Rating: Pulse Width limited by maximum junction temperature.

2) Starting $T_J = 25^{\circ}C$, $L = 10.65mH$, $R_G = 25\Omega$, Peak $I_L = 26A$

STATIC ELECTRICAL RATINGS ($T_{case} = 25^{\circ}\text{C}$ unless otherwise stated)

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain – Source Breakdown Voltage	$V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$	1200			V
I_{DSS}	Zero Gate Voltage Drain Current ($V_{GS} = 0\text{V}$)	$V_{DS} = V_{DSS}$			100	μA
		$V_{DS} = 0.8V_{DSS}$, $T_C = 125^{\circ}\text{C}$			500	
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30\text{V}$, $V_{DS} = 0\text{V}$			± 100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 5\text{mA}$	2		4	V
$I_{D(ON)}$	On State Drain Current ²	$V_{DS} > I_{D(ON)} \times R_{DS(ON)}$ Max $V_{GS} = 10\text{V}$	26			A
$R_{DS(ON)}$	Drain – Source On State Resistance ²	$V_{GS} = 10\text{V}$, $I_D = 0.5 I_D [\text{Cont.}]$			0.400	Ω

DYNAMIC CHARACTERISTICS

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}$ $V_{DS} = 25\text{V}$ $f = 1\text{MHz}$		15000		pF
C_{oss}	Output Capacitance			1240		
C_{rss}	Reverse Transfer Capacitance			640		
Q_g	Total Gate Charge ³	$V_{GS} = 10\text{V}$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [\text{Cont.}] @ 25^{\circ}\text{C}$		640		nC
Q_{gs}	Gate – Source Charge			51		
Q_{gd}	Gate – Drain (“Miller”) Charge			210		
$t_{d(on)}$	Turn-on Delay Time	$V_{GS} = 15\text{V}$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [\text{Cont.}] @ 25^{\circ}\text{C}$ $R_G = 0.6\Omega$		22		ns
t_r	Rise Time			20		
$t_{d(off)}$	Turn-off Delay Time			98		
t_f	Fall Time			18		

SOURCE – DRAIN DIODE RATINGS AND CHARACTERISTICS

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	(Body Diode)			26	A
I_{SM}	Pulsed Source Current ¹	(Body Diode)			104	
V_{SD}	Diode Forward Voltage ²	$V_{GS} = 0\text{V}$, $I_S = -I_D [\text{Cont.}]$			1.3	V
t_{rr}	Reverse Recovery Time	$I_S = -I_D [\text{Cont.}]$, $dI_S / dt = 100\text{A}/\mu\text{s}$		1400		ns
Q_{rr}	Reverse Recovery Charge	$I_S = -I_D [\text{Cont.}]$, $dI_S / dt = 100\text{A}/\mu\text{s}$		38		μC

THERMAL CHARACTERISTICS

	Characteristic	Min.	Typ.	Max.	Unit
$R_{\theta JC}$	Junction to Case			0.18	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Junction to Ambient			40	

1) Repetitive Rating: Pulse Width limited by maximum junction temperature.

2) Pulse Test: Pulse Width < 380 μs , Duty Cycle < 2%

3) See MIL–STD–750 Method 3471



CAUTION — Electrostatic Sensitive Devices. Anti-Static Procedures Must Be Followed.

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