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SDLS030

SN 5405, SN 54LS05, SN 54S05, SN 7405, SN 74LS05, SN 74S05 HEX INVERTERS WITH OPEN-COLLECTOR OUTPUTS December 1983 - Revised March 1988

- Package Option Includes Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description



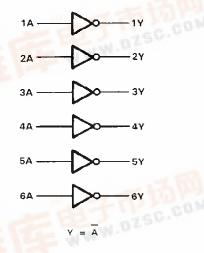
These devices contain six independent inverters. The open-collector outputs require pull-up resistors to perform correctly. They may be connected to other open-collector outputs to implement active-low wired-OR or active-high wired-AND functions. Open-collector devices are often used to generate high VOH levels.

The SN5405, SN54LS05, and SN54S05 are characterized for operation over the full military temperature range of -55 °C to 125 °C. The SN7405, SN74LS05, and SN74S05 are characterized for operation from 0 °C to 70 °C.

FUNCTION TABLE (each inverter)

INPUT	OUTPUT
A	Y
н	L
L	н

logic diagram (positive logic)



SN5405...J PACKAGE SN54LS05, SN54S05...J OR W PACKAGE SN7405...N PACKAGE SN74LS05, SN74S05...D OR N PACKAGE (TOP VIEW)

	1A 1 1Y 2 2A 3 2Y 4 3A 5 3Y 6 GND 7	14 VCC 13 6A 12 6Y 11 5A 10 5Y 9 4A 8 4Y	
	SN5405 (TOP 1A 1 2Y 2 2A 3 VCC 4 3A 5 3Y 6 4A 7	W PACKAGE VIEW) 14 1Y 13 6A 12 6Y 11 GND 10 5Y 9 5A 8 4Y	
SNO	(TOP 2 4 4 NC 5 2Y 6 NC 7 3A 8 9 10	25 FK PACKAG VIEW) 20 Y 1 20 19 18 6Y 17 NC 16 5A 15 NC 14 5Y 11 12 13 20 ¥ ₹	COM
	NC – No internal	l connection	

logic symbol[†]

1.4 (1)	1 0	(2)
ZA (3)	<u>×</u>	(4) 2Y
3A (5)		(6) 37
4A (9)		(8) 4Y
5A (11)		(10) 5Y
6A (13)		(12) GY

 † This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages.

PRODUCTION DATA documents contain information ourrent as of publication date. Products conform to provide the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

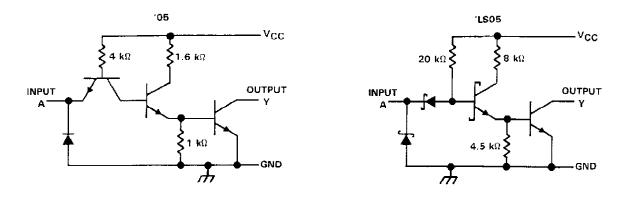
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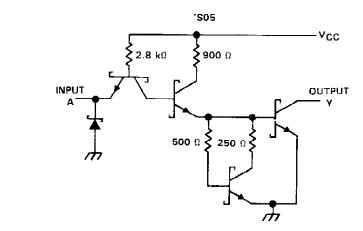


SN5405, SN54LS05, SN54S05, SN7405, SN74LS05, SN74S05 Hex inverters with Open-Collector Outputs

schematics (each inverter)

Resistor values are nominal.





absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1): '(05, 'LS05, 'S05 7 V
Input voltage: '05, 'S05	,
′LS05	
Off-state output voltage	
Operating free-air temperature range:	5 SN54′
	SN74' 0°C to 70°C
Storage temperature range	

NOTE 1: Voltage values are with respect to network ground terminal.



SN5405, SN7405 HEX INVERTERS WITH OPEN-COLLECTOR OUTPUTS

recommended operating conditions

		SN5405			SN7405			
	MIN	NØM	MAX	MIN	NOM	MAX	UNIT	
V _{CC} Supply voltage	4.5	5	5,5	4.75	5	5.25	v	
VIH High-level input voltage	2			2			V	
VIL Low-level input voltage			0.8			0.8	V	
VOH High-level output voltage			5.5			5.5	v	
IGL Low-level output current			16			16	mA	
T _A Operating free-air temperature	- 55		125	0		70	ъС	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		TEST CONDITIONS [†]			SN5405			SN7405			
PARAMETER		TEST CONDITION:	MIN	TYP [‡]	MAX	MIN	TYP‡	MAX	UNIT		
Vik	$V_{CC} = MIN$,	l₁ = −12 mA				-1.5			-1.5	V	
le	$V_{CC} = MIN,$	V _{IL} = 0.8 V,	VOH = 5.5 V	1					0.25		
юн	$V_{CC} = MIN,$	$V_{ _{1}} = 0.7 V_{1}$	V _{OH} = 5.5 V			0.25				mA	
VOL	VCC = MIN.	V _{IH} = 2 V.	IOL = 16 mA		0.2	0.4		0.2	0.4	V	
łı	V _{CC} = MAX,	$V_1 = 5.5 V$		1		1			1	mA	
чн	$V_{CC} = MAX,$	V ₁ = 2.4 V				40			40	μA	
μ	V _{CC} = MAX,	$V_1 = 0.4 V$				-1.6			-1.6	mΑ	
ICCH	$V_{CC} = MAX,$	VI = 0		1	6	12		6	12	mA	
ICCL	$V_{CC} = MAX$	V ₁ = 4.5 V			18	33		18	33	mΑ	

 † For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. ‡ All typical values are at V_{CC} \geq 5 V, T_A = 25°C

switching characteristics, V_{CC} = 5 V, T_A = 25° C (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	TEST CONDITIONS			UNIT
^t PLH	А	v	R _L = 4 kΩ,	СL = 15 рЕ	40	55	пs
^t PHL	Ċ.	,	$R_{L} = 400 \Omega$,	Cլ_ = 15 pF	8	15	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



SN54LS05, SN74LS05 HEX INVERTERS WITH OPEN-COLLECTOR OUTPUTS

recommended operating conditions

	s	SN54LS05			SN74LS05			
	MIN	NOM	MAX	MIN	NOM	MAX		
VCC Supply voltage	4.5	5	5.5	4.75	5	5.25	v	
VIH High-level input voltage	2			2			V	
VIL Low-level input voltage			0.7			0.8	v	
VOH High-level output voltage			5.5			5.5	V	
IOL Low-level output current			4			8	mA	
T _A Operating free-air temperature	- 55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS †				:	05				
				MIN	TYP‡	MAX	MIN	TYP‡	МАХ	
VIK	Vcc = MIN.	l∣ = − 18 mA				- 1.5			- 1.5	V
чон	V _{CC} = MIN,	V _{IL} = MAX,	V _{OH} = 5.5 V			0.1			0.1	mA
Ve	V _{CC} = MIN,	V _{1H} = 2 V,	0L = 4 mA		0.25	0.4		0.25	0.4	
VOL	V _{CC} = MIN,	V _{IH} ≂ 2 V,	OL = 8 mA					0.35	0.5	
- ц	V _{CC} = MAX,	V1 = 7 V				0.1			0.1	mA
Чн	VCC = MAX,	V ₁ = 2.7 V				20			20	μĄ
ΊL	V _{CC} = MAX,	V ₁ = 0.4 V				- 0.4			- 0.4	mА
іссн	V _{CC} = MAX,	V ₁ = 0	······	· · · · · ·	1.2	2.4		1.2	2.4	mA
ICCL	V _{CC} = MAX,	V = 4.5 V			3.6	6.6		3.6	6.6	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[‡] All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$.

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM {INPUT}	TO (OUTPUT)	TEST CONDITIONS	MIN	ТҮР	мах	UNIT
^t PLH	А	Y	RL=2k12, Cl=15pF		17	32	ns
^t PHL		•			15	28	nş

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



SN54S05, SN74S05 HEX INVERTERS WITH OPEN-COLLECTOR OUTPUTS

recommended operating conditions

	S	SN54S05			SN74S05			
	MIN	NOM	MAX	MIN	NOM	MAX		
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	v	
VIH High-level input voltage	2			2			v	
VIL Low-level input voltage			0.8			0.8	v	
VOH High-level output voltage			5.5			5.5	V	
IOL Low-level output current			20			20	mA	
T _A Operating free-air temperature	- 55		125	0		70	്C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER					SN54S0	5		5	UNIT	
PANAIWEICA					TYP‡	MAX	MIN	TYP [‡]	MAX	
VIK	VCC = MIN,	lj = −18 mA				- 1.2			-1.2	V
	$V_{CC} = MIN,$	$V_{IL} = 0.8 V_{2}$	V _{OH} = 5.5 V						0.25	mΑ
юн	$V_{CC} = MIN,$	$V_{ L} = 0.7 V_{,}$	V <u>о</u> н = 5.5 V			0.25				
VOL	V _{CC} = MIN,	$V_{\text{IH}} = 2 V$,	^I OL = 20 mA			0.5			0.5	V
lį	$V_{CC} = MAX.$	$V_{1} = 5.5 V$				1			1	mA
Iн	$V_{CC} = MAX,$	$V_1 = 2.7 V$				50			50	μΑ
hL	$V_{CC} = MAX,$	V _I = 0.5 V				-2			- 2	mΑ
ICCH	V _{CC} = MAX,	$V_{1} = 0$			9	19.8		9	19.8	mΑ
CCL	V _{CC} = MAX.	$V_{I} = 4.5 V$			30	54		30	54	mA

[†] Fur conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. [‡] All typical values are at V_{CC} = 5 V, T_A = 25 °C

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS		MIN	ТҮР	MAX	UNIT
1PLH	A	Y	R _L = 280 Ω,	Cլ - 15 pF	2	5	7.5	ns
^t PHL					2	4.5	7	nş
tPLH			R _L = 280 Ω,	CL = 50 pF	7.5			ns
tрнц						7		ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



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