- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

These devices contain four independent 2-input NOR buffer gates.

The SN5428, and SN54LS28 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN7428, and SN74LS28 are characterized for operation from 0°C to 70°C.

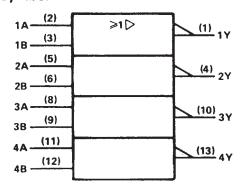
FUNCTION TABLE (each gate)

INP	UTS	ОИТРИТ
A	В	Y
Н	Х	L
Х	Н	Ł
L	L	н

positive logic

$$Y = \overline{A + B}$$
 or $Y = \overline{A \cdot B}$

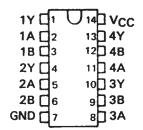
logic symbol†



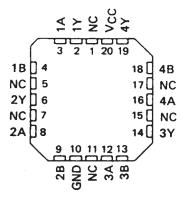
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages.

SN5428, SN54LS28...J OR W PACKAGE SN7428...N PACKAGE SN74LS28...D OR N PACKAGE (TOP VIEW)

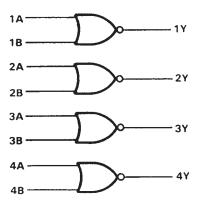


SN54LS28 . . . FK PACKAGE (TOP VIEW)

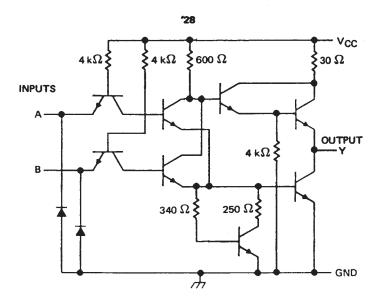


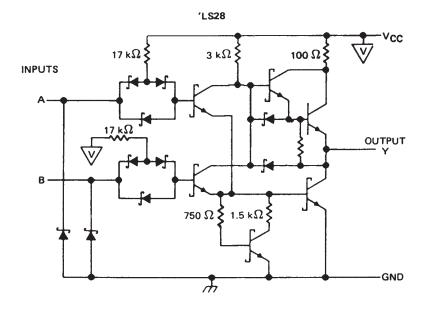
NC - No internal connection

logic diagram



schematics (each gate)





Resistor values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC} (see Note 1)	7 V
Input voltage: '28	5.5 V
'LS28	7 V
Operating free-air temperature: SN54'	–55°C to 125°C
SN74'	0°C to 70°C
Storage temperature range	–65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.



recommended operating conditions

			SN5428 SN7			SN742	8	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	٧
VIH	High-level input voltage	2			2			٧
VIL	Low-level input voltage			0.8			0.8	V
ЮН	High-level output current			- 2.4			- 2,4	mA
loL	Low-level output current			48			48	mA
TA	Operating free-air temperature	- 55		125	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER			TEST CONDITIONS †	MIN	TYP‡	MAX	UNIT
VIK	V _{CC} = MIN, II =	- 12mA				- 1.5	٧
v _{OН} .	VCC = MIN, VIL	= 0.8 V,	IOH = - 2.4 mA	2.4	3.4	-	٧
V _{OL}	V _{CC} = MIN, V _{IH}	_l = 2 V,	I _{OL} = 48 mA		0.2	0.4	٧
1 ₁	V _{CC} = MAX, V _I =	= 5.5 V				1	mA
Чн	V _{CC} = MAX, V _I =	2.4 V				40	μΑ
HL.	V _{CC} = MAX, V _I =	= 0.4 V				-1.6	mA
IOS §	V _{CC} = MAX			- 70		– 180	mΑ
¹ ссн	V _{CC} = MAX, V _I =	0 V			12	21	mA
ICCL	V _{CC} = MAX, See	Note 2			33	57	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, VCC = 5 V, TA = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST COND	DITIONS	MIN	TYP	MAX	UNIT
^t PLH			$R_L = 133 \Omega$,	C ₁ = 50 pF		6	9	ns
^t PHL		V		C[- 30 pi		8	12	ns
^t PLH	A or B	Y		C 150 - F		10	15	ns
[†] PHL		R _L = 133 Ω,	CL = 150 pF		12	18	ns	

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



[‡] All typical values are at VCC = 5 V, TA = 25°C.

[§] Not more than one output should be shorted at a time and the duration of the short circuit should not exceed one second. NOTE 2: One input at 4.5 V, all others at GND.

SN5428, SN54LS28, SN7428, SN74LS28 QUADRUPLE 2-INPUT POSITIVE-NOR BUFFERS

SDLS094 - DECEMBER 1983 - REVISED MARCH 1988

recommended operating conditions

			SN54LS28			SN74LS28			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	٧	
VIH	High-level input voltage	2			2			٧	
VIL	Low-level input voltage			0.7			0.8	V	
Іон	High-level output current			- 1.2			- 1.2	mA	
loL	Low-level output current			12			24	mA	
TA	Operating free-air temperature	- 55		125	0		70	°c	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	TEST CONDITIONS †			SN54LS28			SN74LS28			
PARAMETER			MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT	
VIK	V _{CC} = MIN,	I _I = - 18 mA				- 1.5			– 1.5	٧
Voн	V _{CC} = MIN,	VIL = MAX,	I _{OH} = - 1.2 mA	2.5	3.4		2.7	3.4		٧
V -	V _{CC} = MIN,	V _{1H} = 2 V,	I _{OL} = 12 mA		0.25	0.4		0.24	0.4	V
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	IOL = 24 mA					0.35	0.5	· ·
11	V _{CC} = MAX,	V ₁ = 7 V				0.1			0.1	mA
¹ IH	V _{CC} = MAX,	V ₁ = 2.7 V				20			20	μΑ
IIL	V _{CC} = MAX,	V ₁ = 0.4 V				- 0.4			- 0.4	mA
IOS §	V _{CC} = MAX			- 30		- 130	- 30		- 130	mA
1ссн	V _{CC} = MAX,	V ₁ = 0 V			1.8	3.6		1.8	3.6	'nΑ
CCL	V _{CC} = MAX,	See Note 2			6.9	13.8		6.9	13.8	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: One input at 4.5 V, all others at GND.

switching characteristics, VCC = 5 V, TA = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
^t PLH	A or B	V	$R_1 = 667 \Omega$, $C_L = 45 pF$		12	24	ns
^t PHL	AOIB	,	n[- 60/ 22,		12	24	กร

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



[‡] All typical values are at V_{CC} = 5 V, T_A = 25°C.

[§] Not more than one output should be shorted at a time and the duration of the short circuit should not exceed one second,

IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1999, Texas Instruments Incorporated

Copyright © Each Manufacturing Company.

All Datasheets cannot be modified without permission.

This datasheet has been download from:

www.AllDataSheet.com

100% Free DataSheet Search Site.

Free Download.

No Register.

Fast Search System.

www.AllDataSheet.com