SN74AHCT1G126 SINGLE BUS BUFFER GATE WITH 3-STATE OUTPUT SCLS380I – AUGUST 1997 – REVISED JANUARY 2003

- Operating Range of 4.5 V to 5.5 V
- Max t_{pd} of 6 ns at 5 V
- Low Power Consumption, 10-µA Max I_{CC}
- ±8-mA Output Drive at 5 V
- Inputs Are TTL-Voltage Compatible
- Latch-Up Performance Exceeds 250 mA Per JESD 17
- ESD Protection Exceeds JESD 22

 2000-V Human-Body Model (A114-A)
 - 200-V Machine Model (A115-A)
 - 1000-V Charged-Device Model (C101)

description/ordering information

The SN74AHCT1G126 is a single bus buffer gate/line driver with 3-state output. The output is disabled when the output-enable (OE) input is low. When OE is high, true data is passed from the A input to the Y output.

To ensure the high-impedance state during power up or power down, OE should be tied to GND through a pulldown resistor; the minimum value of the resistor is determined by the current-sourcing capability of the driver.

ORDERING INFORMATION

TA	PACKAGI	<u>=</u> †	ORDERABLE PART NUMBER	TOP-SIDE MARKING‡
	SOT (SOT-23) – DBV	Reel of 3000	SN74AHCT1G126DBVR	B26
4000 1- 0500	SOT (SOT-23) - DBV	Reel of 250	SN74AHCT1G126DBVT	B20_
–40°C to 85°C		Reel of 3000	SN74AHCT1G126DCKR	DN
	SOT (SC-70) – DCK	Reel of 250	SN74AHCT1G126DCKT	BN_

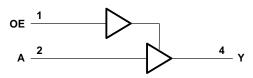
[†] Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

[‡] The actual top-side marking has one additional character that designates the assembly/test site.

FUNCTION TABLE

INPU	JTS	OUTPUT
OE	Α	Y
н	Н	Н
н	L	L
L	Х	Z

logic diagram (positive logic)



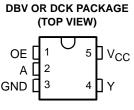


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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage range, V_{CC} Input voltage range, V_I (see Note 1) Output voltage range, V_O (see Note 1) Input clamp current, I_{IK} ($V_I < 0$) Output clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{CC}$) Continuous output current, I_O ($V_O = 0$ to V_{CC}) Continuous current through V_{CC} or GND Package thermal impedance, θ_{JA} (see Note 2): DBV package DCK package	$\begin{array}{c} -0.5 \ \text{V to } 7 \ \text{V} \\ -0.5 \ \text{V to } \ \text{V}_{\text{CC}} + 0.5 \ \text{V} \\ -20 \ \text{mA} \\ \pm 20 \ \text{mA} \\ \pm 25 \ \text{mA} \\ \pm 50 \ \text{mA} \\ 206^{\circ}\text{C/W} \end{array}$
Storage temperature range, T _{stg} CCK package	252°C/W

⁺ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

2. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions (see Note 3)

		MIN	MAX	UNIT
VCC	Supply voltage	4.5	5.5	V
VIH	High-level input voltage	2		V
VIL	Low-level input voltage		0.8	V
VI	Input voltage	0	5.5	V
Vo	Output voltage	0	VCC	V
ЮН	High-level output current		-8	mA
IOL	Low-level output current		8	mA
$\Delta t / \Delta v$	Input transition rise or fall rate		20	ns/V
Т _А	Operating free-air temperature	-40	85	°C

NOTE 3: All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report, Implications of Slow or Floating CMOS Inputs, literature number SCBA004.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	Vee	Т	_ = 25°C	;	MIN	МАХ	UNIT
FARAMETER	TEST CONDITIONS	Vcc	MIN	TYP	MAX	IVIIIN	IVIAA	UNIT
Vou	I _{OH} = -50 μA	4.5 V	4.4	4.5		4.4		V
VOH	I _{OH} = -8 mA	4.5 V	3.94			3.8		v
Ve	I _{OL} = 50 μA	4.5 V			0.1		0.1	V
VOL	I _{OL} = 8 mA	4.5 V			0.36		0.44	v
lj	$V_{I} = 5.5 V \text{ or GND}$	0 V to 5.5 V			±0.1		±1	μΑ
loz	$V_{O} = V_{CC}$ or GND	5.5 V			±0.25		±2.5	μΑ
ICC	$V_{I} = V_{CC} \text{ or } GND, \qquad I_{O} = 0$	5.5 V			1		10	μΑ
∆I _{CC} ‡	One input at 3.4 V, Other input at V_{CC} or GND	5.5 V			1.35		1.5	mA
Ci	$V_{I} = V_{CC}$ or GND	5 V		4	10		10	pF
Co	$V_{O} = V_{CC}$ or GND	5 V		10				pF

[‡]This is the increase in supply current for each input at one of the specified TTL voltage levels, rather than 0 V or V_{CC}.



SN74AHCT1G126 SINGLE BUS BUFFER GATE WITH 3-STATE OUTPUT

SCLS380I - AUGUST 1997 - REVISED JANUARY 2003

switching characteristics over recommended operating free-air temperature range, V_{CC} = 5 V \pm 0.5 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM	TO LOAD		Т	λ = 25°C	;	MIN	МАХ	UNIT						
PARAMETER	(INPUT) (OUTPUT)		CAPACITANCE	MIN	TYP	MAX	WIIN	IVIAA	UNIT						
^t PLH	А	Y	C _I = 15 pF		3.8	5.5	1	6.5	ns						
^t PHL	~	Ι			3.8	5.5	1	6.5	115						
^t PZH	05	Y	C _L = 15 pF		3.6	5.1	1	6	ns						
^t PZL	OE	1		·							3.6	5.1	1	6	115
^t PHZ	OE	Y	C _L = 15 pF		4.6	6.8	1	8							
^t PLZ		'			4.6	6.8	1	8	ns						
^t PLH	А	Y	C _I = 50 pF		5.3	7.5	1	8.5							
^t PHL	A	Ŷ	T	T	CL = 50 pF		5.3	7.5	1	8.5	ns				
^t PZH	05	Y	CL = 50 pF		5.1	7.1	1	8							
^t PZL	OE	ř			5.1	7.1	1	8	ns						
^t PHZ	OE	Y	$C_{\rm b} = 50 \rm pE$		6.1	8.8	1	10							
^t PLZ		r	C _L = 50 pF		6.1	8.8	1	10	ns						

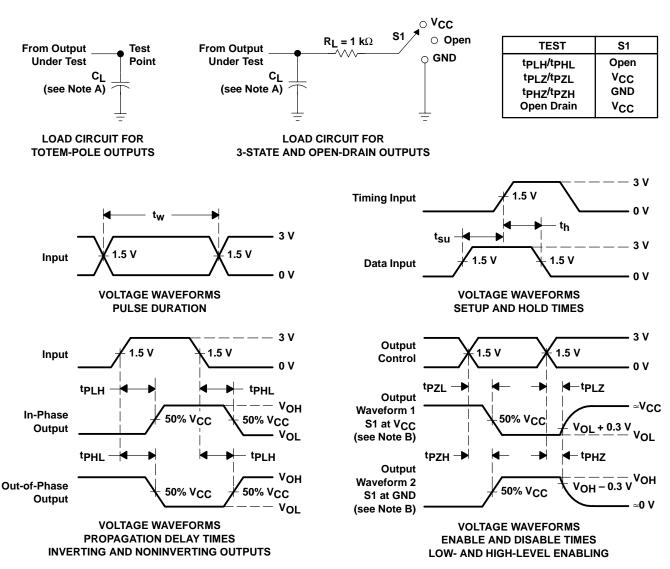
operating characteristics, V_{CC} = 5 V, T_A = 25° C

	PARAMETER			TYP	UNIT
Cpd	Power dissipation capacitance	No load,	f = 1 MHz	14	pF



SN74AHCT1G126 SINGLE BUS BUFFER GATE WITH 3-STATE OUTPUT

SCLS380I - AUGUST 1997 - REVISED JANUARY 2003



PARAMETER MEASUREMENT INFORMATION

NOTES: A. CL includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, Z_O = 50 Ω , t_f \leq 3 ns, t_f \leq 3 ns.
- D. The outputs are measured one at a time with one input transition per measurement.
- E. All parameters and waveforms are not applicable to all devices.

Figure 1. Load Circuit and Voltage Waveforms



TEXAS INSTRUMENTS

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
74AHCT1G126DBVRE4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
74AHCT1G126DBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
74AHCT1G126DBVTE4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
74AHCT1G126DBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
74AHCT1G126DCKRE4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
74AHCT1G126DCKRG4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
74AHCT1G126DCKTE4	ACTIVE	SC70	DCK	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
74AHCT1G126DCKTG4	ACTIVE	SC70	DCK	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AHCT1G126DBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AHCT1G126DBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AHCT1G126DCKR	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74AHCT1G126DCKT	ACTIVE	SC70	DCK	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

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Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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TAPE AND REEL INFORMATION





QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74AHCT1G126DBVR	SOT-23	DBV	5	3000	180.0	9.2	3.23	3.17	1.37	4.0	8.0	Q3
SN74AHCT1G126DBVT	SOT-23	DBV	5	250	180.0	9.2	3.23	3.17	1.37	4.0	8.0	Q3
SN74AHCT1G126DCKR	SC70	DCK	5	3000	180.0	9.2	2.24	2.34	1.22	4.0	8.0	Q3
SN74AHCT1G126DCKT	SC70	DCK	5	250	180.0	9.2	2.24	2.34	1.22	4.0	8.0	Q3
SN74AHCT1G126DCKT	SC70	DCK	5	250	180.0	9.2	2.24	2.34	1.22	4.0	8.0	Q3



PACKAGE MATERIALS INFORMATION

11-Mar-2008



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74AHCT1G126DBVR	SOT-23	DBV	5	3000	202.0	201.0	28.0
SN74AHCT1G126DBVT	SOT-23	DBV	5	250	202.0	201.0	28.0
SN74AHCT1G126DCKR	SC70	DCK	5	3000	202.0	201.0	28.0
SN74AHCT1G126DCKT	SC70	DCK	5	250	202.0	201.0	28.0
SN74AHCT1G126DCKT	SC70	DCK	5	250	201.0	192.0	26.0

DBV (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.

D. Falls within JEDEC MO-178 Variation AA.



DCK (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE



- NOTES: A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
 - D. Falls within JEDEC MO-203 variation AA.



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