SDFS023A - D2932, MARCH 1987 - REVISED OCTOBER 1993

8-Line to 1-Line Multiplexers Can Perform as:

Boolean Function Generators Parallel-to-Serial Converters Data Source Selectors

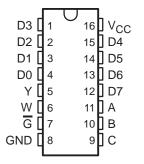
 Package Options Include Plastic Small-Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

description

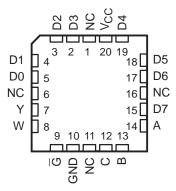
These monolithic data selectors/multiplexers provide full binary decoding to select one of eight data sources. The strobe (\overline{G}) input must be at a low logic level to enable the data selection/multiplexing function. A high level at the strobe terminal forces the W output high and the Y output low.

The SN54F151B is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74F151B is characterized for operation from 0°C to 70°C.

SN54F151B . . . J PACKAGE SN74F151B . . . D OR N PACKAGE (TOP VIEW)



SN54F151B . . . FK PACKAGE (TOP VIEW)



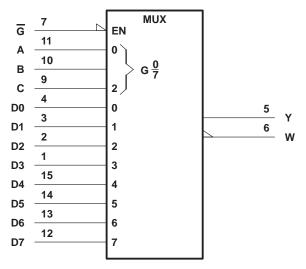
NC - No internal connection

FUNCTION TABLE

	IN	OUT	PUTS				
	SELECT	•	STROBE	0011 013			
С	В	Α	G	Υ	W		
Х	Χ	Χ	Н	L	Н		
L	L	L	L	D0	D0		
L	L	Н	L	D1	D1		
L	Н	L	L	D2	D2		
L	Н	Н	L	D3	D3		
Н	L	L	L	D4	D4		
Н	L	Н	L	D5	D5		
Н	Н	L	L	D6	D6		
Н	Н	Н	L	D7	D7		

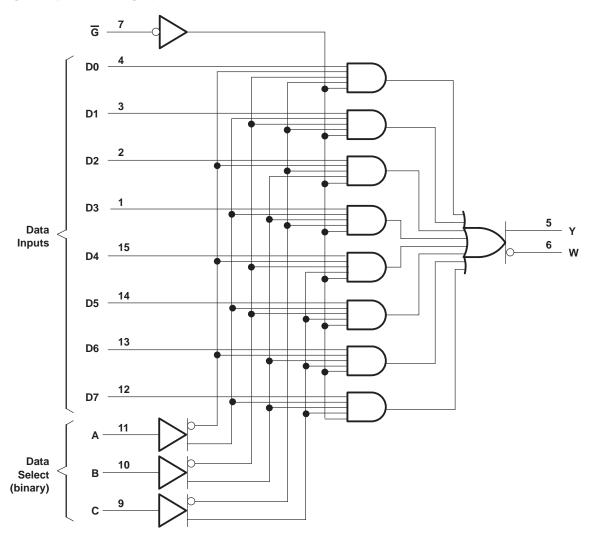
D0, D1, . . . D7 = the level of the respective D input.

logic symbol†



 $[\]dagger$ This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for the D, J, and N packages.

logic diagram (positive logic)



Pin numbers shown are for the D, J, and N packages.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V _{CC}		 0.5 V to 7 V
Input voltage range (see Note 1)		 1.2 V to 7 V
Input current range		 30 mA to 5 mA
Voltage range applied to any output in the	ne high state	 0.5 V to V _{CC}
Current into any output in the low state:	SN54F151B	 40 mA
	SN74F151B	 48 mA
Operating free-air temperature range:	SN54F151B	 –55°C to 125°C
	SN74F151B	 0°C to 70°C
Storage temperature range		 65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input voltage rating may be exceeded provided that the input current rating is observed.



SDFS023A - D2932, MARCH 1987 - REVISED OCTOBER 1993

recommended operating conditions

		SN54F151B			SI	UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.8			0.8	V
liK	Input clamp current			-18			-18	mA
IOH	High-level output current			- 1			-1	mA
lOL	Low-level output current			20			24	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS			N54F151	В	SI	В	UNIT	
PARAMETER	"	TEST CONDITIONS			MAX	MIN	TYP [†]	MAX	UNIT
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.2			-1.2	V
Vari	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -1 \text{ mA}$	2.5	3.4		2.5	3.4		V
VOH	$V_{CC} = 4.75 \text{ V},$	$I_{OH} = -1 \text{ mA}$				2.7			V
V _{OL}	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 20 \text{ mA}$		0.3	0.5		0.3	0.5	V
lį	V _{CC} = 5.5 V,	V _I = 7 V			0.1			0.1	mA
lіН	V _{CC} = 5.5 V,	V _I = 2.7 V			20			20	μΑ
I _{IL}	$V_{CC} = 5.5 V$,	V _I = 0.5 V			- 0.6			- 0.6	mA
los [‡]	V _{CC} = 5.5 V,	V _O = 0	-60		-150	-60		-150	mA
Icc	$V_{CC} = 5.5 \text{ V},$	V _I = 4.5 V		13.5	21		13.5	21	mA

 $^{^{\}dagger}$ All typical values are at V_{CC} = 5 V, T_A = 25°C.

switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	I		V_{CC} = 5 V, C_{L} = 50 pF, R_{L} = 500 Ω, T_{A} = 25°C			V_{CC} = 4.5 V to 5.5 V, C_L = 50 pF, R_L = 500 Ω , T_A = MIN to MAX§			
			·	'F151B		SN54F	151B	SN74F151B		
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	A, B, or C	W	3.8	5.2	9	2	11.5	3.5	9.5	ns
^t PHL	A, B, Of C	٧٧	2.9	4.3	7.5	2.6	8	2.7	7.5	115
^t PLH	A, B, or C	Y	4.5	6	10.5	4	13.5	4	12	ns
^t PHL	A, B, OI C		4	5.6	9	3.6	9.5	3.6	9	115
^t PLH	G	W	3	4.1	6.1	3	7.5	3	7	ns
^t PHL	9	VV	2.8	3.5	6	2.5	6.5	2.5	6	115
^t PLH	G	Y	4.4	5.3	9.5	3.8	12	3.8	10.5	ns
^t PHL	9	·	3.5	4.5	7	3	8	3	7.5	115
^t PLH	Data	W	2.7	3.6	6.5	1.8	7.5	2.3	7	ne
^t PHL	(any D)	VV	1.2	1.9	4	1	6	1	5	ns
t _{PLH}	Data	Y	2.9	3.7	6.5	2.4	8.5	2.5	7.5	ne
^t PHL	(any D)	l Y	3.3	4.2	7	2.1	9	2.6	7.5	ns

§ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. NOTE 2: Load circuits and waveforms are shown in Section 1.



[‡] Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.



TAPE AND REEL INFORMATION





Α	0	Dimension designed to accommodate the component width
В	0	Dimension designed to accommodate the component length
		Dimension designed to accommodate the component thickness
٧	٧	Overall width of the carrier tape
ГР	1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Devic	e		Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74F15	1BDR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
SN74F151	BNSR	SO	NS	16	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1





*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74F151BDR	SOIC	D	16	2500	333.2	345.9	28.6
SN74F151BNSR	SO	NS	16	2000	346.0	346.0	33.0

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