### SN54F157A, SN74F157A QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

- Buffered Inputs and Outputs
- Package Options Include Plastic Small-Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

### description

The 'F157A is a quadruple 2-input data selector/multiplexer featuring a common strobe  $(\overline{G})$  input. When the strobe is high, all outputs are low. When the strobe is low, a 4-bit word is selected from one of two sources and is routed to the four outputs. The 'F157A provides true data.

The SN54F157A is characterized for operation over the full military temperature range of  $-55^{\circ}$ C to 125°C. The SN74F157A is characterized for operation from 0°C to 70°C.

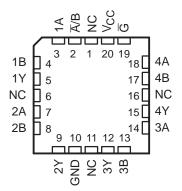
FU	NC	ΓΙΟΝ	TAE	

	INPU	OUTPUT		
G	A/B	Α	В	Y
Н	Х	Х	Х	L
L	L	L	Х	L
L	L	Н	Х	н
L	Н	Х	L	L
L	Н	Х	Н	Н

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SN54F157A J PACKAGE SN74F157A D OR N PACKAGE (TOP VIEW)									
Ā/B	1	U <sub>16</sub>	V <sub>cc</sub>						
1A [ 1B [	2 3	15 14	]						
1Y [	4	13	] 4B						
2A [ 2B [	5 6	12 11	] 4Y ] 3A						
2Y [	7	10	3B						
GND [	8	9	] 3Y						

SN54F157A . . . FK PACKAGE (TOP VIEW)



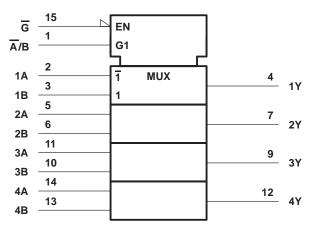
NC - No internal connection



### SN54F157A, SN74F157A QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

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### logic symbol<sup>†</sup>



<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for the D, J, and N packages.

#### 2 1A 1Y 3 1B 5 2A 2Y 6 2B 11 3A 9 3Y 10 3B 14 **4A** <u>12</u> 4Y 13 4B 15 G 1 Ā/B

logic diagram (positive logic)

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>‡</sup>

Supply voltage range, V <sub>CC</sub>	$\ldots$ -0.5 V to 7 V
Input voltage range (see Note 1)	$\ldots$ –1.2 V to 7 V
Input current range	-30 mA to 5 mA
Voltage range applied to any output in the high state	$\dots$ -0.5 V to V <sub>CC</sub>
Current into any output in the low state	40 mÅ
Operating free-air temperature range: SN54F157A	-55°C to 125°C
SN74F157A	0°C to 70°C
Storage temperature range	-65°C to 150°C

<sup>‡</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input voltage rating may be exceeded provided that the input current rating is observed.

### recommended operating conditions

		SN54F157A			SI	UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.8			0.8	V
IIК	Input clamp current			-18			-18	mA
IOH	High-level output current			- 1			- 1	mA
IOL	Low-level output current			20			20	mA
ТА	Operating free-air temperature	-55		125	0		70	°C



## SN54F157A, SN74F157A QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

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# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS			SN54F157A			SN74F157A			
PARAMETER		STCONDITIONS	MIN	TYP†	MAX	MIN	TYP†	MAX	UNIT	
VIK	V <sub>CC</sub> = 4.5 V,	lj = -18 mA			-1.2			-1.2	V	
Veu	$V_{CC} = 4.5 V,$	I <sub>OH</sub> = – 1 mA	2.5	3.4		2.5	3.4		V	
VOH	V <sub>CC</sub> = 4.75 V,	I <sub>OH</sub> = – 1 mA				2.7			v	
V <sub>OL</sub>	$V_{CC} = 4.5 V,$	I <sub>OL</sub> = 20 mA		0.3	0.5		0.3	0.5	V	
lj	$V_{CC} = 5.5 V,$	$V_{I} = 7 V$			0.1			0.1	mA	
Ιн	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> = 2.7 V			20			20	μΑ	
۱ <sub>IL</sub>	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> = 0.5 V			- 0.6			- 0.6	mA	
los‡	V <sub>CC</sub> = 5.5 V,	$V_{O} = 0$	-60		-150	-60		-150	mA	
ICC	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> = 4.5 V		15	23		15.5	23	mA	

<sup>†</sup> All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C.

<sup>‡</sup>Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

#### switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C <sub>I</sub> R	CC = 5 V _ = 50 pl _ = 500 9 _ = 25°C	<b>F,</b> Ω,	C R	L = 50 p L = 500 9			UNIT
				′F157A		SN54F157A		SN74F	157A	
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
<sup>t</sup> PLH	<u> </u>	V	3.2	6.6	10	3.2	12	3.2	11	
<sup>t</sup> PHL	Ā/B	T	2.2	4.6	7	2.2	9	2.2	8	ns
<sup>t</sup> PLH	G	V	4.2	6.6	9.5	4.2	13	4.2	11	
<sup>t</sup> PHL	G	T	1.7	4.1	6.5	1.7	7.5	1.7	7	ns
<sup>t</sup> PLH	A or B	V	1.7	4.1	6	1.7	7.5	1.7	6.5	ns
<sup>t</sup> PHL	AUD	ľ	1.7	3.6	5.5	1	7.5	1.2	7	115

§ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

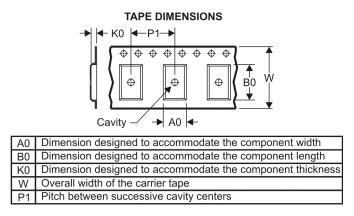
NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



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### TAPE AND REEL INFORMATION





### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*A	I dimensions are nominal												
	Device		Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
	SN74F157ADR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
	SN74F157ANSR	SO	NS	16	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1



# PACKAGE MATERIALS INFORMATION

19-Mar-2008



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74F157ADR	SOIC	D	16	2500	333.2	345.9	28.6
SN74F157ANSR	SO	NS	16	2000	346.0	346.0	33.0

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