SLLS085B - JANUARY 1977 - REVISED MAY 1995

- Meets or Exceeds the Requirements of ANSI EIA/TIA-422-B and ITU Recommendation V.11
- Single 5-V Supply
- Balanced-Line Operation
- TTL Compatible
- High Output Impedance in Power-Off Condition
- High-Current Active-Pullup Outputs
- Short-Circuit Protection
- Dual Channels
- Input Clamp Diodes

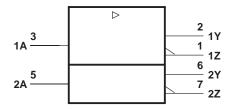
† The PS package is only available left-end taped and reeled, i.e., order SN75158PSLE.

description

The SN75158 is a dual differential line driver designed to satisfy the requirements set by the ANSI EIA/TIA-422-B and ITU V.11 interface specifications. The outputs provide complementary signals with high-current capability for driving balanced lines, such as twisted pair, at normal line impedance without high power dissipation. The output stages are TTL totem-pole outputs providing a high-impedance state in the power-off condition.

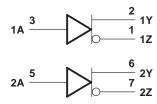
The SN75158 is characterized for operation from 0°C to 70°C.

logic symbol‡



[‡] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram (positive logic)

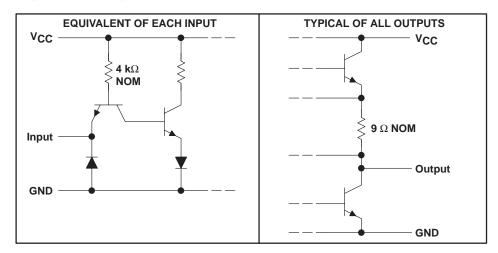




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schematics of inputs and outputs



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC} (see Note 1)	
Input voltage, V _I	5.5 V
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range, T _A	0°C to 70°C
Storage temperature range, T _{stg}	65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: All voltage values, except differential output voltage VOD, are with respect to network ground terminal. VOD is at the Youtput with respect to the Z output.

DISSIPATION RATING TABLE

PACKAGE	$\begin{aligned} & \textbf{T}_{\pmb{A}} \leq \textbf{25}^{\circ}\textbf{C} \\ & \textbf{POWER RATING} \end{aligned}$	DERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING
D	725 mW	5.8 mW/°C	464 mW
Р	1000 mW	8.0 mW/°C	640 mW
PS	450 mW	3.6 mW/°C	288 mW

recommended operating conditions

	MIN	NOM	MAX	UNIT
Supply voltage, V _{CC}	4.75	5	5.25	V
High-level input voltage, VIH	2			V
Low-level input voltage, V _{IL}			0.8	V
High-level output current, I _{OH}			-40	mA
Low-level output current, I _{OL}			40	mA
Operating free-air temperature, T _A	0		70	°C



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electrical characteristics over operating free-air temperature range (unless otherwise noted)

	PARAMETER	TEST C	ONDITIONS†	MIN	TYP‡	MAX	UNIT
VIK	Input clamp voltage	V _{CC} = MIN,	I _I = -12 mA		-0.9	-1.5	V
Vон	High-level output voltage	V _{CC} = MIN, V _{IH} = 2 V,	$V_{IL} = 0.8 \text{ V},$ $I_{OH} = -40 \text{ mA}$	2.4	3		٧
VOL	Low-level output voltage	V _{CC} = MIN, V _{IH} = 2 V,	$V_{IL} = 0.8 \text{ V},$ $I_{OL} = 40 \text{ mA}$		0.2	0.4	V
V _{OD1}	Differential output voltage	$V_{CC} = MAX$,	IO = 0		3.5	2×V _{OD2}	V
IV _{OD2} I	Differential output voltage	$V_{CC} = MIN$		2	3		V
ΔV_{OD}	Change in magnitude of differential output voltage§	$V_{CC} = MIN$]		±0.02	±0.4	V
\/		$V_{CC} = MAX$	$R_L = 100 \Omega$,		1.8	3	V
Voc	Common-mode output voltage¶	V _{CC} = MIN	See Figure 1		1.5	3	V
ΔVOC	Change in magnitude of common-mode output voltage§	V _{CC} = MIN or MAX			±0.02	±0.4	V
			V _O = 6 V		0.1	100	
I _O	Output current with power off	$V_{CC} = 0$	$V_0 = -0.25 \text{ V}$		-0.1	-100	μΑ
			$V_0 = -0.25 \text{ to } 6 \text{ V}$			±100	
II	Input current at maximum input voltage	$V_{CC} = MAX$,	V _I = 5.5 V			1	mA
lн	High-level input current	$V_{CC} = MAX$,	V _I = 2.4 V			40	μА
I _I L	Low-level input current	$V_{CC} = MAX$,	V _I = 0.4 V		-1	-1.6	mA
los	Short-circuit output current#	$V_{CC} = MAX$		-40	-90	-150	mA
ICC	Supply current (both drivers)	$V_{CC} = MAX,$ $T_A = 25^{\circ}C,$	Inputs grounded, No load		37	50	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
^t PLH	Propagation delay time, low-to-high-level output	See Figure 2, Termination A		16	25	ns
^t PHL	Propagation delay time, high-to-low-level output	See Figure 2, Terrilliation A		10	20	ns
^t PLH	Propagation delay time, low-to-high-level output	See Figure 2, Termination B		13	20	ns
tPHL	Propagation delay time, high-to-low-level output	See Figure 2, Termination B		9	15	ns
tTLH	Transition time, low-to-high-level output	Con Figure 2. Termination A		4	20	ns
tTLH	Transition time, high-to-low-level output	See Figure 2, Termination A 4 20		20	ns	
	Overshoot factor	See Figure 2, Termination C			10%	

[‡] All typical values are at V_{CC} = 5 V and T_A = 25°C except for V_{OC} , for which V_{CC} is as stated under test conditions. ΔV_{OD} and $\Delta |V_{OC}|$ are the changes in magnitudes of V_{OD} and V_{OC} , respectively, that occur when the input is changed from a high level to a low

[¶] In ANSI Standard EIA/TIA-422-B, VOC, which is the average of the two output voltages with respect to ground, is called output offset voltage, Vos.

[#]Only one output should be shorted at a time, and duration of the short circuit should not exceed one second.

PARAMETER MEASUREMENT INFORMATION

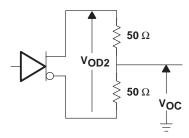
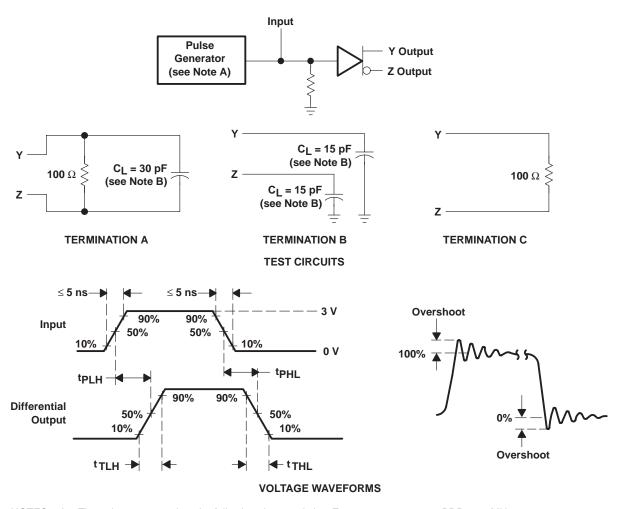


Figure 1. Differential and Common-Mode Output Voltages



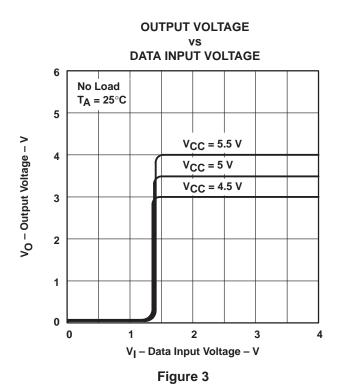
NOTES: A. The pulse generator has the following characteristics: Z_O = 50 Ω , t_W = 25 ns, PRR \leq 10 MHz.

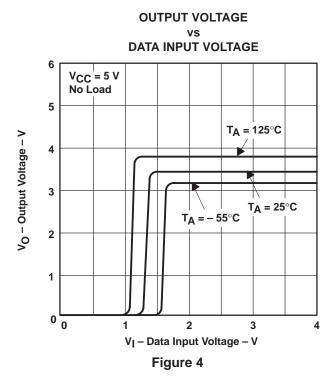
B. C_I includes probe and jig capacitance.

Figure 2. Test Circuit and Voltage Waveforms

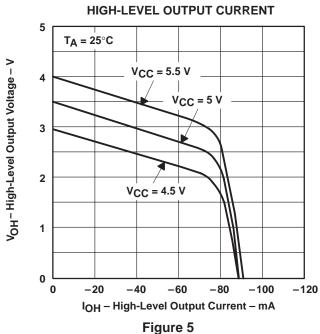


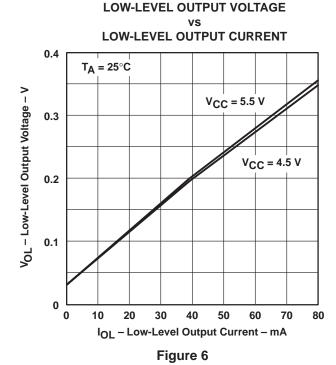
TYPICAL CHARACTERISTICS



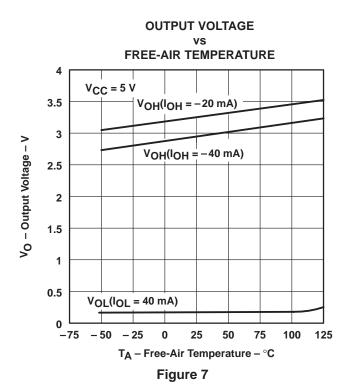


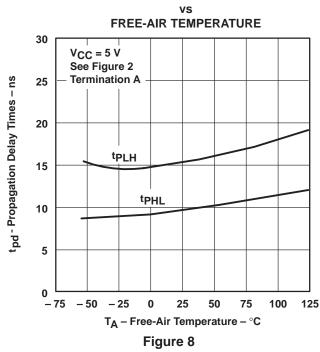




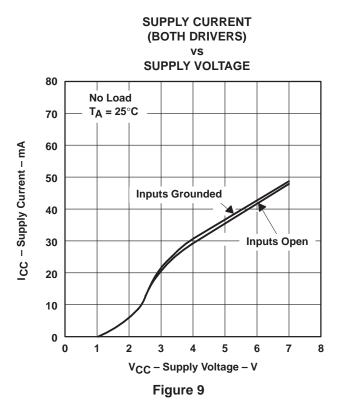


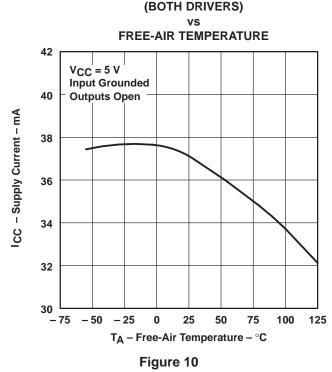
TYPICAL CHARACTERISTICS





PROPAGATION DELAY TIMES





SUPPLY CURRENT

TYPICAL CHARACTERISTICS

SUPPLY CURRENT (BOTH DRIVERS)

FREQUENCY

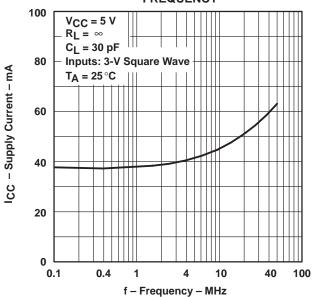


Figure 11

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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
SN75158D	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN75158DE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN75158DG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN75158DR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN75158DRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN75158DRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN75158P	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN75158PE4	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN75158PSLE	OBSOLETE	SO	PS	8		TBD	Call TI	Call TI
SN75158PSR	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN75158PSRE4	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN75158PSRG4	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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PACKAGE OPTION ADDENDUM

4-Jun-2007

no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by Customer on an annual basis.



TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN75158DR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
SN75158PSR	SO	PS	8	2000	330.0	16.4	8.2	6.6	2.5	12.0	16.0	Q1





*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN75158DR	SOIC	D	8	2500	340.5	338.1	20.6
SN75158PSR	SO	PS	8	2000	346.0	346.0	33.0

D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AA.





NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001

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