



# SRC1207M

NPN Silicon Transistor

## Descriptions

- Switching application
- Interface circuit and driver circuit application

## Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

## Ordering Information

Type NO.	Marking	Package Code
SRC1207M	1207	TO-92M

## Outline Dimensions

unit : mm

Dimensions:

- Top width:  $4.0 \pm 0.1$
- Top height:  $3.0 \pm 0.1$
- Pin 1 offset: 0.44 REF
- Pin 2 offset: 0.52 REF
- Pin 3 offset: 1.27 Typ.
- Pin 1-2 distance:  $2.54 \pm 0.1$
- Pin 2-3 distance:  $2.54 \pm 0.1$
- Pin 1-3 distance:  $5.08 \pm 0.2$
- Pin 1 diameter:  $0.7$  Typ.
- Pin 2 diameter:  $0.7$  Typ.
- Pin 3 diameter:  $0.7$  Typ.
- Pin 1 length:  $3.0 \pm 0.1$
- Pin 2 length:  $3.0 \pm 0.1$
- Pin 3 length:  $3.8$  Min.
- Pin 1-2-3 height:  $0.42$  Typ.

**• Equivalent Circuit**

Pin Connections:

1. Emitter
2. Collector
3. Base

R <sub>1</sub>	R <sub>2</sub>
10KΩ	47KΩ



**Absolute maximum ratings**

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit
Out Voltage	$V_O$	50	V
Input Voltage	$V_I$	30	V
Out Current	$I_O$	100	mA
Power Dissipation	$P_D$	400	mW
Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{STG}$	-55 ~ 150	°C

**Electrical Characteristics**

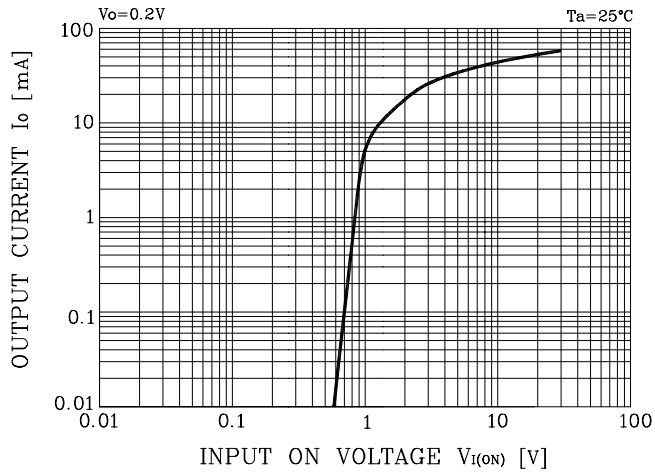
(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output Cut-off Current	$I_{O(OFF)}$	$V_O=50V, V_I=0$	-	-	500	nA
DC Current Gain	$G_I$	$V_O=5V, I_O=10mA$	80	150	-	-
Output Voltage	$V_{O(ON)}$	$I_O=10mA, I_I=0.5mA$	-	0.1	0.3	V
Input Voltage (ON)	$V_{I(ON)}$	$V_O=0.2V, I_O=5mA$	-	-	1.8	V
Input Voltage (OFF)	$V_{I(OFF)}$	$V_O=5V, I_O=0.1mA$	0.5	-	-	V
Transition Frequency	$f_T^*$	$V_O=10V, I_O=5mA$	-	200	-	MHz
Input Current	$I_I$	$V_I=5V$	-	-	0.88	mA

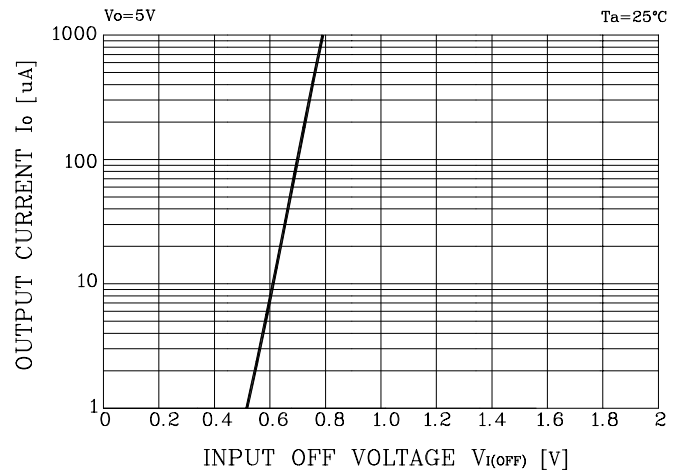
\* : Characteristic of Transistor Only

## Electrical Characteristic Curves

**Fig. 1  $I_o - V_{I(ON)}$**



**Fig. 2  $I_o - V_{I(OFF)}$**



**Fig. 3  $G_I - I_o$**

