



SHANGHAI SUNRISE ELECTRONICS CO., LTD.

SS32 THRU SS36

SURFACE MOUNT SCHOTTKY
BARRIER RECTIFIER

TECHNICAL
SPECIFICATION

VOLTAGE: 20 TO 60V CURRENT: 3.0A

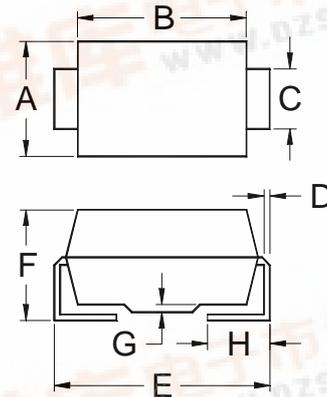
FEATURES

- Ideal for surface mount pick and place application
- Low profile package
- Low power loss, high efficiency
- High current capability, low V_F
- High surge capability
- High temperature soldering guaranteed: 260°C/10sec/at terminal

MECHANICAL DATA

- Terminal: Plated leads solderable per MIL-STD 202E, method 208C
- Case: Molded with UL-94 Class V-0 recognized flame retardant epoxy
- Polarity: Color band denotes cathode

SMC/DO-214AB



	A	B	C	D
MAX.	.245(6.22)	.280(7.11)	.124(3.15)	.012(0.305)
MIN.	.220(5.59)	.260(6.60)	.108(2.75)	.006(0.152)
	E	F	G	H
MAX.	.320(8.13)	.096(2.44)	.008(0.203)	.060(1.52)
MIN.	.305(7.75)	.084(2.13)	.004(0.102)	.030(0.76)

Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Single-phase, half-wave, 60Hz, resistive or inductive load rating at 25°C, unless otherwise stated, for capacitive load, derate current by 20%)

RATINGS	SYMBOL	SS32	SS33	SS34	SS35	SS36	UNITS
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	20	30	40	50	60	V
Maximum RMS Voltage	V_{RMS}	14	21	28	35	42	V
Maximum DC Blocking Voltage	V_{DC}	20	30	40	50	60	V
Maximum Average Forward Rectified Current ($T_L=100^\circ\text{C}$)	$I_{F(AV)}$	3.0					A
Peak Forward Surge Current (8.3ms single half sine-wave superimposed on rated load)	I_{FSM}	100					A
Maximum Instantaneous Forward Voltage (at rated forward current)	V_F	0.5		0.7			V
Maximum DC Reverse Current (at rated DC blocking voltage)	I_R	$T_a=25^\circ\text{C}$		0.5			mA
		$T_a=100^\circ\text{C}$		20.0			mA
Typical Junction Capacitance (Note 1)	C_J	300					pF
Typical Thermal Resistance (Note 2)	$R_\theta(ja)$	15					$^\circ\text{C/W}$
Storage and Operation Junction Temperature	T_{STG}, T_J	-65 to +150					$^\circ\text{C}$

Note:

1. Measured at 1.0 MHz and applied voltage of 4.0V_{dc}

2. Thermal resistance from junction to terminal mounted on 5x5mm copper pad area

<http://www.sse-diode.com>

