Unit: mm



TOSHIBA Field Effect Transistor Silicon P Channel MOS Type

# SSM3J02T

# Power Management Switch High Speed Switching Applications

- Component package suitable for high-density mounting
- Small Package
- Low ON Resistance :  $R_{on} = 0.5 \Omega$  (max) (@VGS = -4 V)

:  $R_{on} = 0.7 \Omega \text{ (max) } (@V_{GS} = -2.5 \text{ V})$ 

Low-voltage operation possible

#### Maximum Ratings (Ta = 25°C)

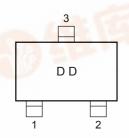
Characteristic		Symbol	Rating	Unit	
Drain-Source voltage		$V_{DS}$	-30	V	
Gate-Source voltage		V <sub>GSS</sub>	±10	V	
	DC	I <sub>D</sub>	-1.5	А	
Drain current	Pulse	I <sub>DP</sub> (Note2)	-3.0		
Drain power dissipation (Ta = 25°C)		P <sub>D</sub> (Note1)	1250	mW	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

Note1: Mounted on FR4 board

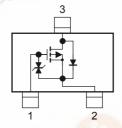
 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu pad: } 645 \text{ mm2}, \text{ t} = 10 \text{ s})$ 

Note2: The pulse width limited by max channel temperature.

### **Equivalent Circuit**

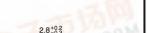


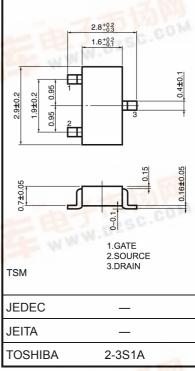
Marking



# **Handling Precaution**

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.





Weight: 10 mg (typ.) WWW.BZSC.COM



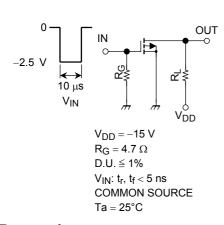
#### **Electrical Characteristics (Ta = 25°C)**

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0$	_	_	±1	μΑ
Drain-Source breakdown voltage		V <sub>(BR)DSS</sub>	$I_D = -1 \text{ mA}, V_{GS} = 0$	-30	_	_	V
Drain Cut-off current		I <sub>DSS</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = 0$	_	_	-1	μА
Gate threshold voltage		V <sub>th</sub>	$V_{DS} = -3 \text{ V}, I_D = -0.1 \text{ mA}$	-0.6	_	-1.1	V
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = -3 \text{ V}, I_D = -0.3 \text{ A}$ (Note3)	0.6	_	_	S
Drain-Source ON resistance		R <sub>DS (ON)</sub>	$I_D = -0.3 \text{ A}, V_{GS} = -4 \text{ V}$ (Note3)	_	0.4	0.5	Ω
			$I_D = -0.3 \text{ A}, V_{GS} = -2.5 \text{ V}$ (Note3)	_	0.55	0.7	
Input capacitance		C <sub>iss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	150	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	21	_	pF
Output capacitance		Coss	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	61	_	pF
Switching time	Turn-on time	t <sub>on</sub>	$V_{DD} = -15 \text{ V}, I_D = -0.3 \text{ A},$	_	55	_	ns
	Turn-off time	t <sub>off</sub>	$V_{GS} = 0 \text{ to } -2.5 \text{ V}, R_G = 4.7 \Omega$	_	52	_	

Note3: Pulse test

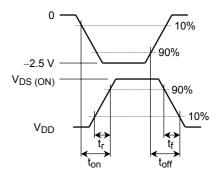
### **Switching Time Test Circuit**

(a) Test circuit



(b)  $V_{IN}$   $V_{GS}$ 

(c) V<sub>OUT</sub>



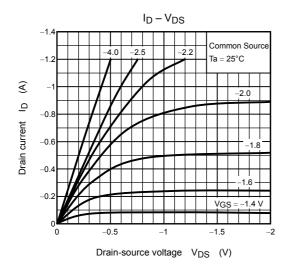
#### **Precaution**

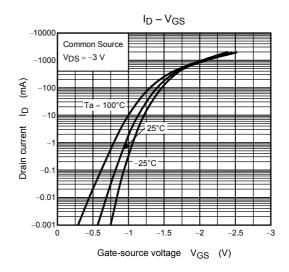
 $V_{th} \ can \ be \ expressed \ as \ voltage \ between \ gate \ and \ source \ when \ low \ operating \ current \ value \ is \ I_D = -100 \ \mu A$  for this product. For normal switching operation,  $V_{GS}$  (on) requires higher voltage than  $V_{th}$  and  $V_{GS}$  (off) requires lower voltage than  $V_{th}$ .

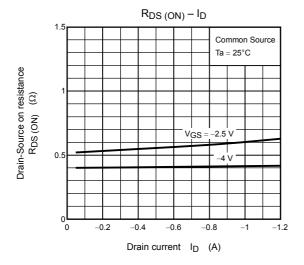
(relationship can be established as follows:  $V_{GS (off)} < V_{th} < V_{GS (on)}$ )

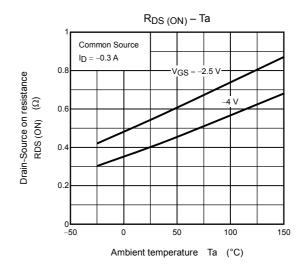
Please take this into consideration for using the device.

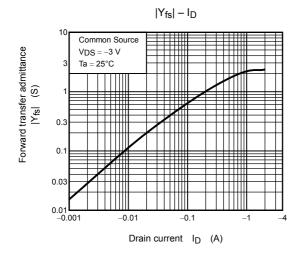
 $V_{\rm GS}$  recommended voltage of  $-2.5~{
m V}$  or higher to turn on this product.

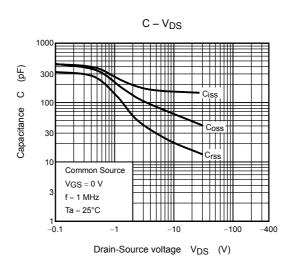




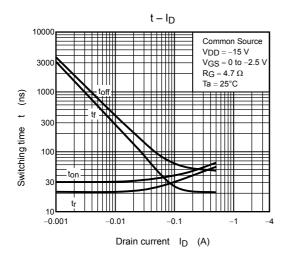


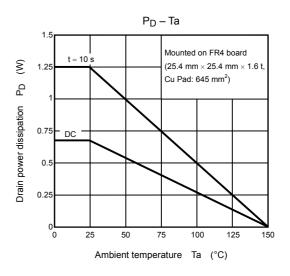


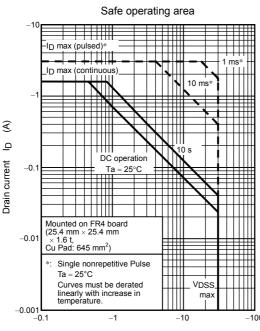


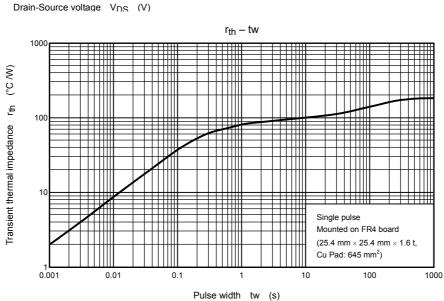


3









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