Unit: mm

CategoryTOSHIBA Field Effect Transistor Silicon N Channel MOS Type/Category

SSM3K12T

DC-DC Converter

High Speed Switching Applications

• Small Package

• Low ON-resistance : $R_{on} = 95 \text{ m}\Omega \text{ (max) (@VGS} = 10 \text{ V)}$

: $R_{on} = 145 \text{ m}\Omega \text{ (max) (@V_{GS} = 4.5 V)}$

• High speed : $t_{on} = 21 \text{ ns}$

 $t_{off} = 16 \text{ ns}$

Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-Source voltage		V_{DS}	30	V	
Gate-Source voltage		V _{GSS}	±20	V	
Drain current	DC	Ι _D	3.0	Α	
	Pulse	I _{DP} (Note 2)	6.0		
Drain power dissipation (Ta = 25°C)		P _D (Note 1)	0.7	W	
		t = 10 s	1.25		
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	−55~150	°C	

Note 1: Mounted on FR4 board

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu pad: } 645 \text{ mm}^2)$

Note 2: The pulse width limited by max channel temperature.

2.8±03 1.6±03 1.6±03 1. GATE 2. SOURCE 3. DRAIN JEDEC JEITA TOSHIBA 2-3S1A

Weight: 10 mg (typ.)

Handling Precaution

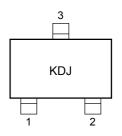
When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

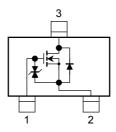
The Channel-to-Ambient thermal resistance R_{th} (ch-a) and the drain power dissipation P_D vary according to the board material, board area, board thickness and pad area, and are also affected by the environment in which the product is used. When using this device, please take heat dissipation fully into account.



Marking

Equivalent Circuit





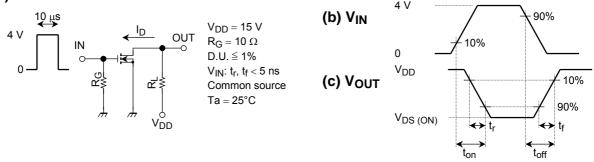
Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$	_	_	±1	μΑ	
Drain-Source breakdown voltage		V (BR) DSS	$I_D = 1 \text{ mA}, V_{GS} = 0$	30	_	_	V	
Drain Cut-off current		I _{DSS}	V _{DS} = 30 V, V _{GS} = 0	_	_	1	μΑ	
Gate threshold voltage		V _{th}	$V_{DS} = 5 \text{ V}, I_D = 0.1 \text{ mA}$	1.1	_	1.8	V	
Forward transfer admittance		Y _{fs}	$V_{DS} = 5 \text{ V}, I_D = 1.5 \text{ A}$ (Note 3)	1.8	3.2	_	S	
Drain-Source ON resistance		R _{DS} (ON)	$I_D = 1.5 \text{ A}, V_{GS} = 10 \text{ V}$ (Note 3)	_	78	95	mΩ	
			$I_D = 1.5 \text{ A}, V_{GS} = 4.5 \text{ V}$ (Note 3)	_	117	145		
			$I_D = 1.5 \text{ A}, V_{GS} = 4.0 \text{ V}$ (Note 3)	_	135	175		
Total gate charge		Qg	$V_{DD} = 24 \text{ V}, I_D = 3 \text{ A}, V_{GS} = 4 \text{ V}$	_	2.6	_	nC	
Input capacitance		C _{iss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	120	_	pF	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	20	_	pF	
Output capacitance		Coss	V _{DS} = 15 V, V _{GS} = 0, f = 1 MHz	_	68	_	pF	
Switching time	Rise time	t _r		_	13	_		
	Turn-on time	t _{on}	V _{DD} = 15 V, I _D = 1.5 A		21		- ns	
	Fall time	t _f	$V_{GS} = 0~4~V, R_{G} = 10~\Omega$		3.6	_		
	Turn-off time	t _{off}			16			

Note 3: Pulse test

Switching Time Test Circuit

(a) Test circuit



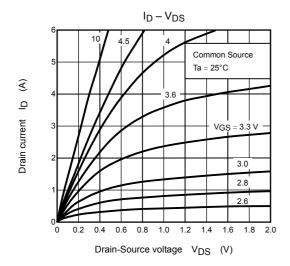
Precaution

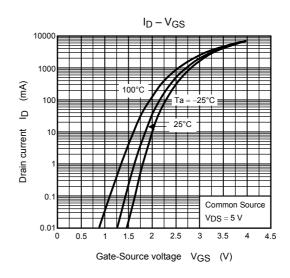
 V_{th} can be expressed as voltage between gate and source when low operating current value is $ID = 100~\mu A$ for this product. For normal switching operation, V_{GS} (on) requires higher voltage than V_{th} and V_{GS} (off) requires lower voltage than V_{th} .

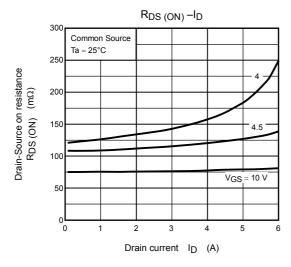
(relationship can be established as follows: V_{GS} (off) $< V_{th} < V_{GS}$ (on))

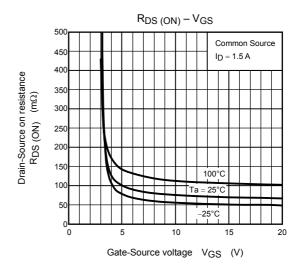
Please take this into consideration for using the device.

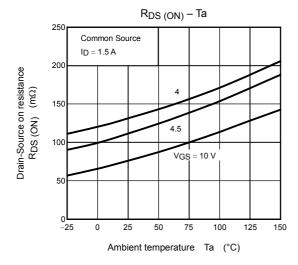
VGS recommended voltage of 4 V or higher to turn on this product.

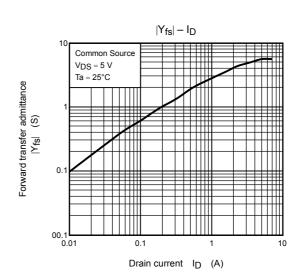




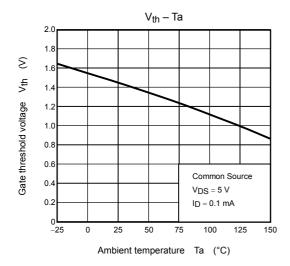


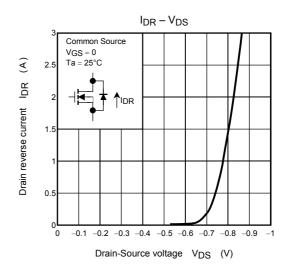


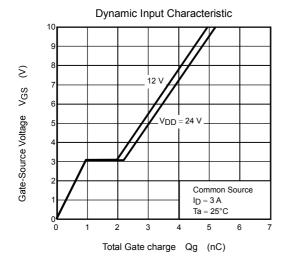


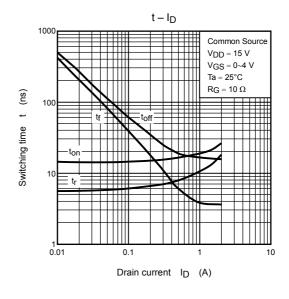


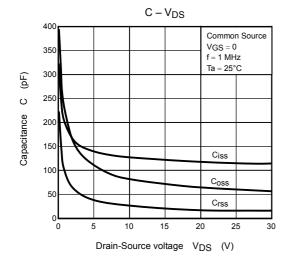
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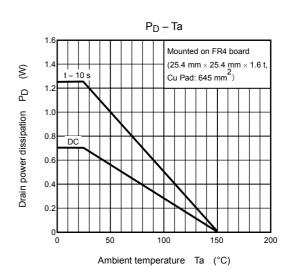


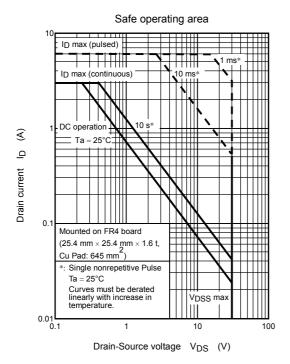


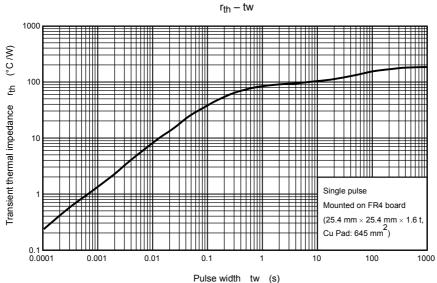












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