

STL72

MEDIUM VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

Figure 1: Package

- MEDIUM VOLTAGE CAPABILITY
- n LOW SPREAD OF DYNAMIC PARAMETERS
- n MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- n VERY HIGH SWITCHING SPEED

APPLICATIONS

ELECTRONIC BALLASTS FOR FLUORESCENT LIGHTING

DESCRIPTION

The device is manufactured using high voltage Multi-Epitaxial Planar technology for high switching speeds and medium voltage capability.

It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The STL series is designed for use in Compact Fluorescent Lamps.

TO-92

Figure 2: Internal Schematic Diagram

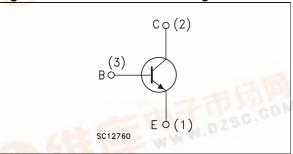


Table 1: Order Codes

Part Number	Marking	Package	Packaging
STL72	L72 L or (#) L72 H	TO-92	Bulk

See:note on page 2



Table 2: Absolute Maximum Ratings

Symbol	Parameter	Value	Unit	
V _{CES}	Collector-Emitter Voltage (V _{BE} = 0)	700	V	
V_{CEO}	Collector-Emitter Voltage (I _B = 0)	400	V	
V _{EBO}	Emitter-Base Voltage (I _C = 0)	9	V	
I _C	Collector Current	1	Α	
I _{CM}	Collector Peak Current (t _p < 5ms)	2	Α	
I _B	Base Current	0.5	Α	
I _{BM}	Base Peak Current (t _p < 5ms)	1	Α	
P _{tot}	Total Dissipation at T _C = 25 °C	1	W	
T _{stg}	Storage Temperature	-65 to 150	°C	
T_J	Max. Operating Junction Temperature	150	°C	

Table 3: Thermal Data

R _{thj-amb}	Thermal Resistance Junction-Ambient	Max	120	°C/W

Table 4: Electrical Characteristics (T_{case} = 25 °C unless otherwise specified)

Symbol	Parameter	Test C	onditions	Min.	Тур.	Max.	Unit
I _{CEV}	Collector Cut-off Current	V _{CE} = 700 V				1	mA
	(V _{BE} = -1.5 V)	V _{CE} = 700 V	T _j = 125 °C			5	mA
I _{EBO}	Emitter-Cut-off Current	V _{EB} = 9 V				1	mA
	$(I_C = 0)$						
V _{CEO(sus)} *	Collector-Emitter Sustaining Voltage	I _C = 1 mA	L = 25 mH	400			V
	$(I_B = 0)$						
V _{CE(sat)} *	Collector-Emitter	I _C = 0.2 A	I _B = 40 mA		0.15	0.4	V
	Saturation Voltage	I _C = 0.4 A	I _B = 80 mA		0.25	0.5	V
V _{BE(sat)} *	Base-Emitter Saturation Voltage	I _C = 0.4 A	I _B = 80 mA		0.95	1.1	V
h _{FE}	DC Current Gain #	I _C = 0.4 A	V _{CE} = 5 V				
		Group L		10		16	
		Group H		15		23	
		I _C = 1 A	V _{CE} = 10 V	5		15	
	INDUCTIVE LOAD	I _C = 0.25	V _{Clamp} = 300 V				
t _f	Fall Time	$I_{B1} = -I_{B2} = 50 \text{ mA}$	L = 3mH		0.3		μs
		(see figure 3)					

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^{*} Pulsed: Pulsed duration = 300 µs, duty cycle ≤ 1.5 %.
The product is pre-selected in DC current gain (Group L and Group H). STMicroelectronics reserves the right to ship either groups according to production availability. Please contact your nearest STMicroelectronics sales office for delivery datails.

Figure 3: Inductive Load Switching Test Circuit

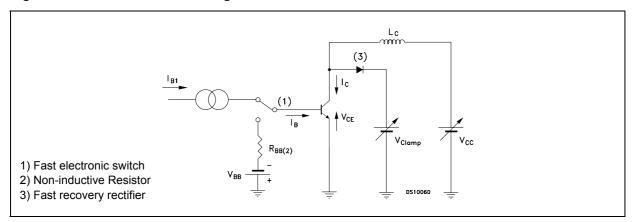
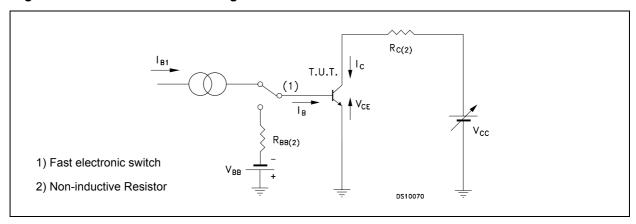


Figure 4: Restistive Load Switching Test Circuit



TO-92 BULK SHIPMENT MECHANICAL DATA

DIM.	mm.				
	MIN.	ТҮР	MAX.		
А	4.32		4.95		
b	0.36		0.51		
D	4.45		4.95		
E	3.30		3.94		
е	2.41		2.67		
e1	1.14		1.40		
L	12.70		15.49		
R	2.16		2.41		
S1	0.92		1.52		
W	0.41		0.56		
V		5 ^O			

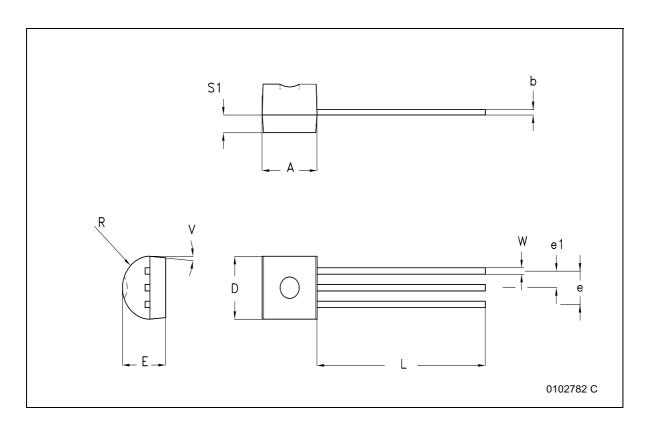


Figure 5: Revision History

Release Date	Version	Change Designator
01-Apr-2005	1	Initial release
12-Jul-2005	2	New hfe range values

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