

**SUD494N**

Silicon epitaxial planar Diode

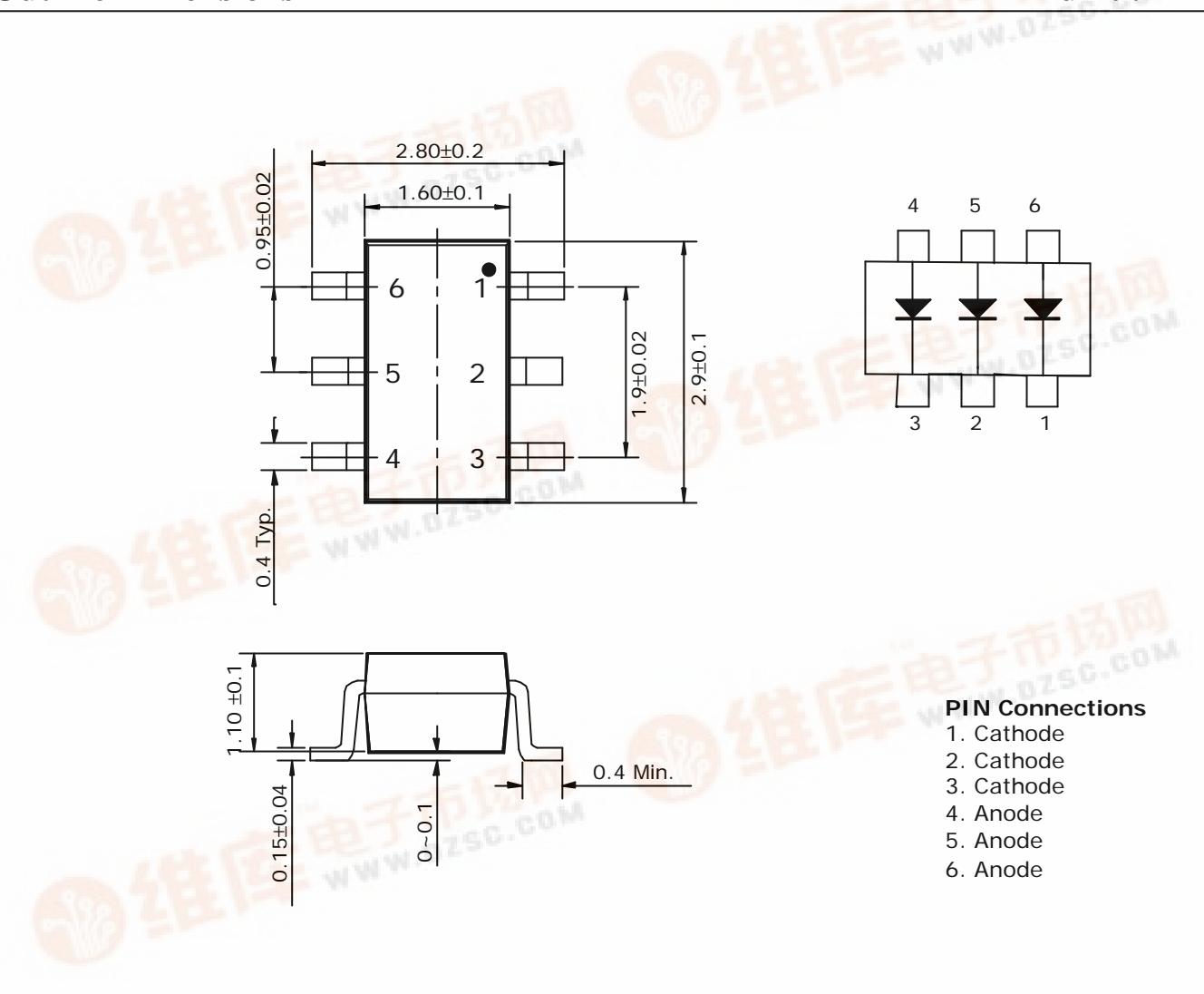
## Features

- Ultra high speed
- Fast reverse recovery time :  $t_{rr} = 1.6\text{ns}(\text{Typ.})$
- Small total capacitance :  $C_T = 2.2\text{pF}(\text{Typ.})$
- Three SDS914 chips in SOT-26 package

## Ordering Information

Type NO.	Marking	Package Code
SUD494N	EX	SOT-26

## Outline Dimensions



**Absolute maximum ratings**

Ta=25°C

Characteristic	Symbol	Ratings	Unit
Maximum(peak) reverse voltage	V <sub>RM</sub>	85	V
Reverse voltage	V <sub>R</sub>	80	V
Maximum(peak) forward current	I <sub>FM</sub>	300	mA
Average forward current	I <sub>O</sub>	100	mA
Surge current(10ms)	I <sub>FSM</sub>	2	A
Power dissipation	P <sub>D</sub>	150	mW
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 ~ 150	°C

**Electrical Characteristics**

Ta=25°C

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Forward voltage	V <sub>F(1)</sub>	I <sub>F</sub> =1mA	-	0.6	-	V
	V <sub>F(2)</sub>	I <sub>F</sub> =10mA	-	0.7	-	
	V <sub>F(3)</sub>	I <sub>F</sub> =100mA	-	0.9	1.2	
Reverse current	I <sub>R</sub>	V <sub>R</sub> =80V	-	-	0.5	µA
Total capacitance	C <sub>T</sub>	V <sub>R</sub> =0, f=1MHz	-	2.2	4.0	pF
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> =10mA	-	1.6	4.0	ns

## Electrical Characteristic Curves

Fig. 1  $I_F$ - $V_F$

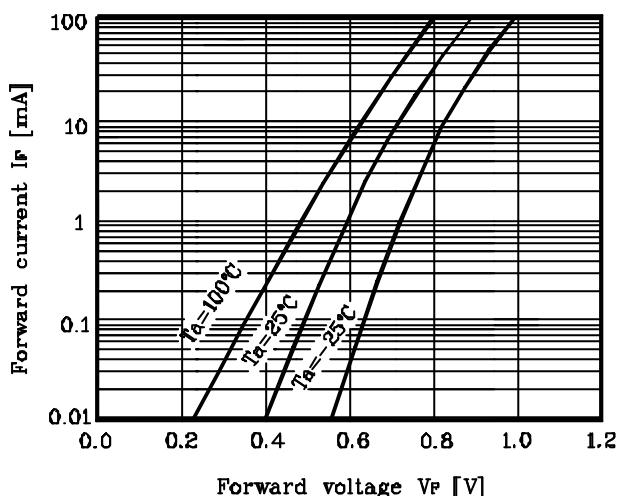


Fig. 2  $I_R$ - $V_R$

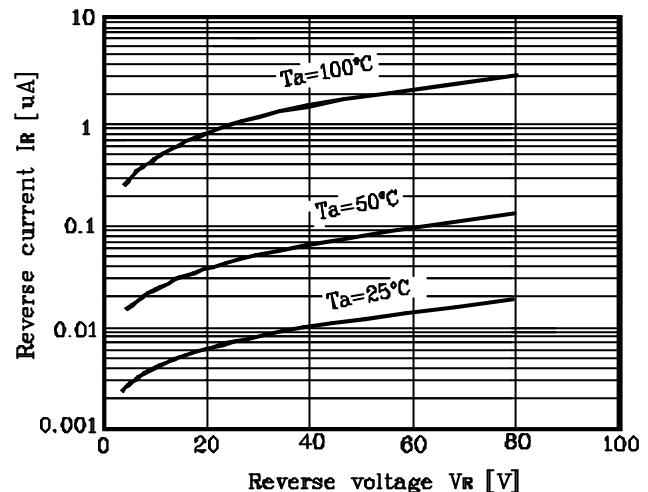


Fig. 3  $C_T$ - $V_R$

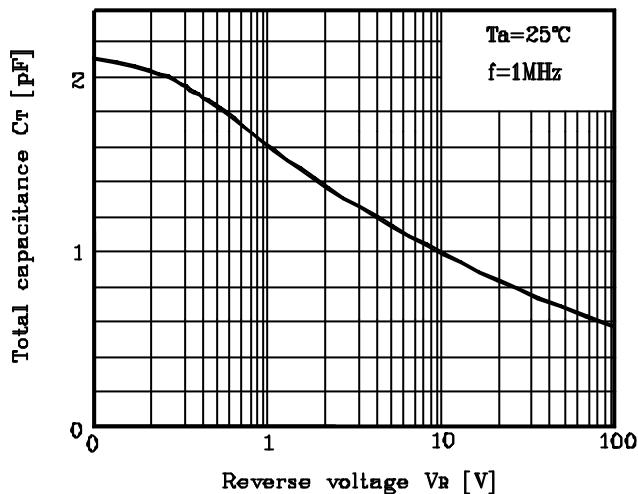


Fig. 4 trr- $I_F$

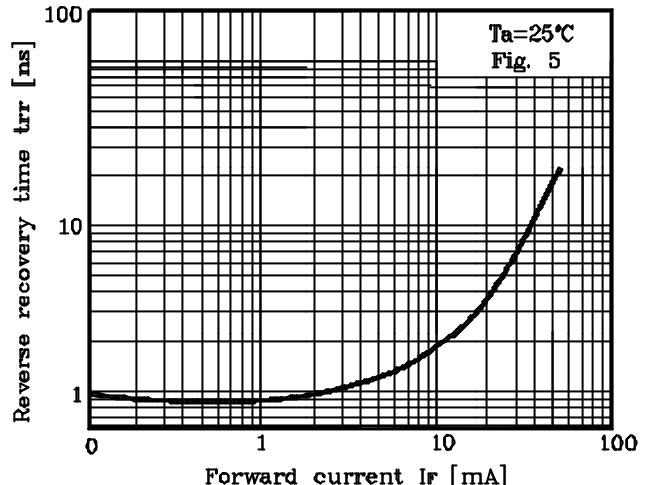


Fig. 5 Reverse recovery time(trr) test circuit

