AUK Semiconductor

SUT487J

Epitaxial Planar Type PNP Silicon Transistor

Description

- General purpose transistor

Feature

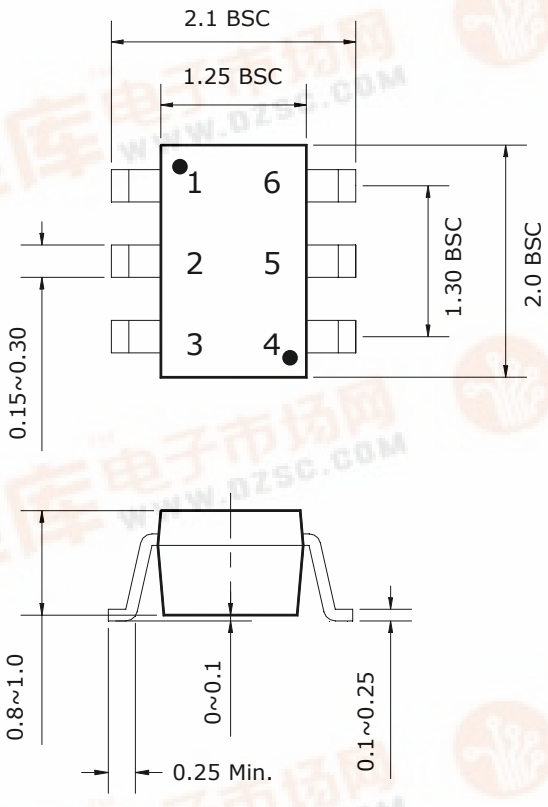
- Two 2SA1980 chips in SOT-363 package

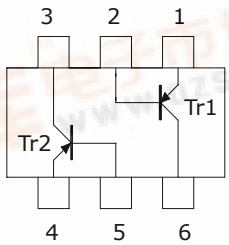
Ordering Information

Type NO.	Marking	Package Code
SUT487J	XX	SOT-363

Outline Dimensions

unit : mm





PIN Connections

1. Emitter 1
2. Base 1
3. Collector 2
4. Emitter 2
5. Base 2
6. Collector 1

Absolute maximum ratings (Tr1, Tr2 : PNP)

Ta=25°C

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	V_{CBO}	-50	V
Collector-Emitter voltage	V_{CEO}	-50	V
Emitter-base voltage	V_{EBO}	-5	V
Collector current	I_C	-150	mA
Collector dissipation	P_C	150	mW
Junction temperature	T_j	150	°C
Storage temperature range	T_{stg}	-55~150	°C

Electrical Characteristics (Tr1,Tr2 : PNP)

Ta=25°C

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-Base breakdown voltage	BV_{CBO}	I_C=-100μA, I_E=0	-50	-	-	V
Collector-Emitter breakdown voltage	BV_{CEO}	I_C=-1mA, I_B=0	-50	-	-	V
Emitter-Base breakdown voltage	BV_{EBO}	I_E=-10μA, I_C=0	-5	-	-	V
Collector cut-off current	I_{CBO}	V_{CB}=-50V, I_E=0	-	-	-0.1	μA
Emitter cut-off current	I_{EBO}	V_{EB}=-5V, I_C=0	-	-	-0.1	μA
DC current gain	h_{FE}	V_{CE}=-6V, I_C=-2mA	120	-	400	-
Collector-Emitter saturation voltage	$V_{CE(sat)}$	I_C=-100mA, I_B=-10mA	-	-	-0.3	V
Transition frequency	f_T	V_{CE}=-10V, I_C=-1mA, f=100MHz	80	-	-	MHz
Collector output capacitance	C_{ob}	V_{CB}=-10V, I_E=0, f=1MHz	-	4	7	pF

Electrical Characteristic Curves

Tr1, Tr2 : PNP

Fig. 1 I_C -V_{BE}

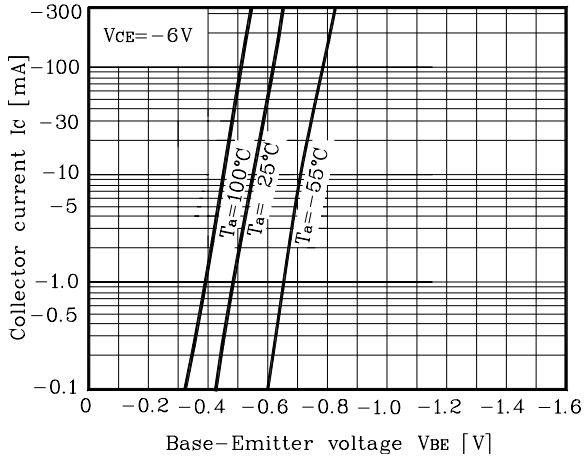


Fig. 2 I_C -V_{CE}

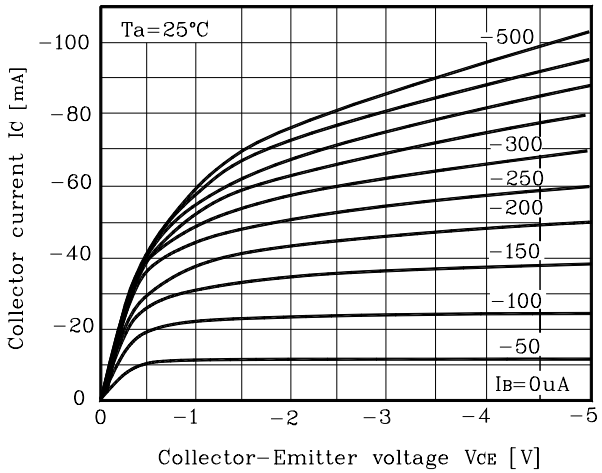


Fig. 3 h_{FE}-I_C

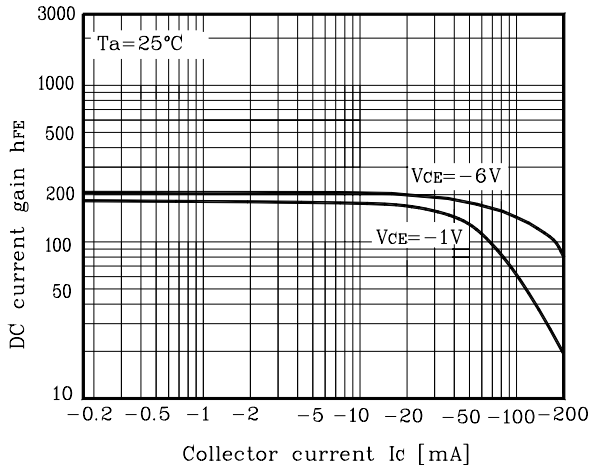
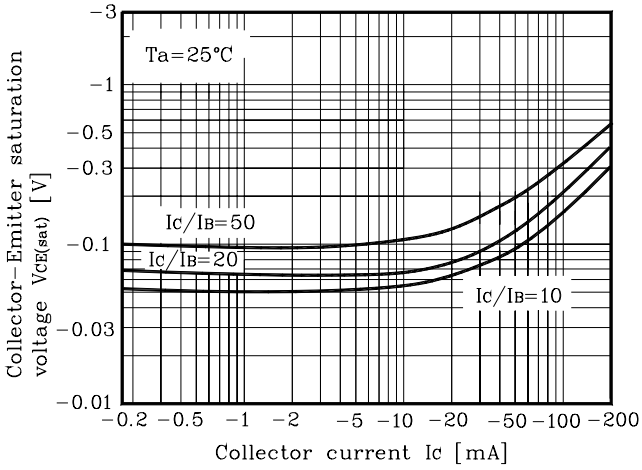


Fig. 4 $V_{CE(sat)}$-I_C



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