HITACHI

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For Messrs :

Date : June 10,'99

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CUSTOMER'S ACCEPTANCE SPECIFICATIONS SX21V001-Z4A

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Accepted by: Proposed by:Acyan						
solavs	Hitachi I td Sh. 3284PS 2	701 - SX21V001-74A - 3	age 1-1/1			

3284PS 2701 - SX21V001-Z4A - 3

No.



	REC	ORD OF REVISION
Date	Sheet No.	Summary
May 25,'99	3284PS 2712- SX21V001-Z4A-2 Page 12-2/2	12.2 REVISION Added Rev.B
June 10,'99	3284PS 2705- SX21V001-Z4A-3 Page 5-2/3	5.2.2 ELECTRICAL CHARACTERISTICS Revised resistance betw een terminal X1-X2 : $350-850\Omega \rightarrow 400-800\Omega$ Y1-Y2 : $350-750\Omega \rightarrow 220-700\Omega$
splays, tachi, Ltd.	ate June 10, '99 Sh No	1 3284PS 2702 - SX21V001-74A - 3 IPadel 2-1/1

3. MECHANICAL DATA

(1) Part Name	SX21V001-Z4A
(2) Module Size	203.0(W) mm \times 142.5(H) mm \times 8.0 max (D) mm
(3) Display Size	167.02(W) mm \times 125.26(H) mm Diagonal size 21cm (8.2")
(4) Dot Pitch	0.087(W) mm $ imes$ 0.261(H) mm
(5) Number of Dots	640 \times 3 (R,G,B)(W) \times 480 (H) dots
(6) Duty	1/240
(7) LCD	Film type (negative type)
(8) View ing Direction	12 O'clock
(9) Backlight	Cold Cathode Fluorescent Lamp (CFL) \times 1
(10) Weight	(310) g typ
(11) Pow er Supply Voltage	3.3V only
(12) Touch panel	Resistance type

Displays, Hitachi, Ltd. Date June 10, '99 No	1 3284PS 2703 - SX21V001-Z4A - 3	Page	3-1/1
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4. ABSOLUTE MAXIMUM RATINGS

4. 1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS (LCM)

4. 1 ELECTRICAL ABSOLUTE MAX	VSS=0V:Standard				
ПЕМ	SYMBOL	MIN	MAX	UNIT	COMMENT
Pow er Supply for Logic	VDD-VSS	0	6.0	V	
Contrast Adjustment Voltage	VCON-VSS	0	VDD	V	
Input Voltage	Vi	-0.3	VDD+0.3	V	Note 1
Input Current	li	0	1	А	
Static Electricity	-	-	-	-	Note 2

DISP OFF, FLM, CL1, CL2, UD0~UD7, LD0~LD7 Note 1

Note 2 Make certains you are grounded when handling LCM

4. 2 ELECTRICAL ABSOLUTE MAXIMUM RATINGS (TOUCH PANEL)

ПЕМ	SPECIFICATION	NOTE
Voltage	(7VDC) (MAX)	
Current	(25mA) (max)	

4. 3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

	OPERATING		STC	DRAGE		
ПЕМ	MIN	MAX	MIN	MAX	COMMENT	
Ambient Temperature	5°C	40°C	-20°C	60°C	Note 2, 3	
Humidity	Note 1		Note 1		Without condensation	
Vibration	-	2.45 m/s ² (0.25G)	-	11.76 m/s ² (1.2G) Note 5	Note 4	
		29.4 m/s ² (3G)	-	490 m/s ² (50G) Note 5	XYZ directions 11ms	
Corrosive Gas Not Acceptable		Not A	cceptable			

Note 1 Ta \leq 40°C : 85%RH max.

Ta>40°C : Absolute humidity must be low er than the humidity of 85%RH at 40°C.

Note 2 Ta at -20°C ----- <48h, at 60°C ----- <168h

- Note 3 Background color changes slightly depending on ambient temperature. This phenomenon is reversible.
- Note 4 5Hz~100Hz (Except resonance frequency)
- Note 5 This module should be operated normally after finish the test.

Note 6 When LCM is operated at 5°C, the life time of CFL will be reduced. Need to make sure of value of IL and characteristics of inverter. Also the response time at 5°C will be slow er.

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5. ELECTRICAL CHARACTERISTICS

5. 1 FLECTRICAL CHARACTERISTICS OF LCD

5. 1 ELECTRICAL CHARAC		VSS=0V	1			
ПЕМ	SYMBOL	CONDITION	MIN	ΤΥΡ	MAX	UNIT
Pow er Supply Voltage	VDD	VDD-VSS=3.3V	3.15	3.30	3.45	V
Contrast Adjustment Voltage (Note 1)	VCON	-	0.8	-	2.8	V
Input Voltage for Logic	Vi	"H" level	0.8VDD	-	VDD	v
Circuits (Note 2)	VI	"L" level	0	-	0.2VDD	v
Pow er Supply Current (Note 3)	IDD	VDD-VSS=3.3V	-	30	46	mA
Input Leak Current	lcon(Note5)	Vcon=0.8~2.8V	-	-	(20)	μA
input Leak Current	lin (Note2)	Vin=VDDorVSS	-	-	±1.0	ματ
		Ta= 5°C,	0.8	-	-	
Contrast Adjustment Voltage	Vcon	Ta=25°C,	-	(1.8)	-	V
(Note 6)		Ta=40°C,	-	-	2.8	
Frame Frequency (Note 7)	fFLM	-	100	120	150	Hz

(Note 1) In proportion as the VCON voltage decrease the brightness will increase.

(Note 2) DISP OFF, FLM, CL1, CL2, UD0~UD7, LD0~LD7

(Note 3) fFLM=120Hz, Ta=25°C, Display pattern: Checker pattern.

- (Note 4) Rush Current of Pow er ON : $2A(PK) \times 100\mu s$
- (Note 5) VCON
- (Note 6) The Contrast Adjustment Voltage fluctuates about $\pm 0.3V$ by each module. (only TYP values)
- (Note 7) Need to make sure of flickering and rippling of display when setting the Frame Frequency in your set.

(Note 8) Absolute maximum ratings voltage of CFL cable for this module is as follow s. VCFL side : 2kV

VSS side : 300V

This inverter design shall not exceed the rated voltage.

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5. 2 ELECTRICAL CHARACTERISTICS OF TOUCH PANEL

5.2.1 OPERATING CONDITION

ПЕМ	SPECIFICATION		
Operating Voltage	5VDC		
Operating Current	10~25mA		

5.2.2 ELECTRICAL CHARACTERISTICS

ПЕМ		SPECIFICATION	NOTE
Resistance	X1-X2	400~800Ω	
betw een terminal	Y1-Y2	220~700Ω	
Insulance Resistance	X-Y	$10M\Omega$ min	Operating Voltage
Lincority	Х	1.5% max	Can Nata 1
Linearity	Y	1.5% max	See Note 1
Chattering		10msec max	

5.2.3 MECHANICAL CHARACTERISTICS

ПЕМ	SPECIFICATION	NOTE
Pen input pressure	0.05N~0.5N	
Surface hardness	2H min	JIS K 5400

5.2.4 OPTICAL CHARACTERISTICS

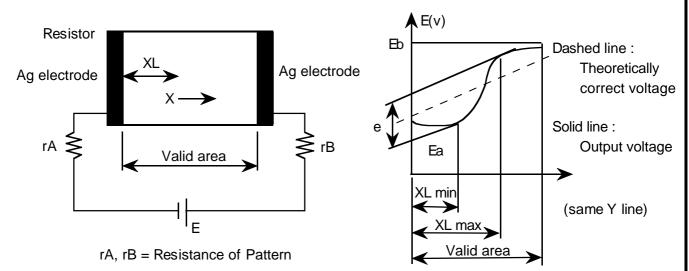
ΠΕΜ	SPECIFICATION	NOTE
Transparency	81% typ	

Displays, Hitachi, Ltd.	Data	June 10, '99	Sh.	3284PS 2705 - SX21V001-Z4A - 3	Page	5-2/3
Hitachi, Ltd.	Dale	buile 10, 55	No.		i aye	0 2/0

Note 1 : Test Methode and Conditions

The difference ("e") between the theoretical output voltage and the actual output voltage when pressure is applied to any point within the valid area must be as indicated below.

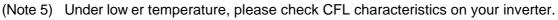
e \leq applied voltage \times 0.03 (=±0.015)



5. 3 ELECTRICAL CHARACTERISTICS OF BACKLIGHT

ПЕМ	SYMBOL	MIN	ΤΥΡ	MAX	UNIT	NOTE
Lamp Voltage	VL	-	(500)	-	Vrms	Ta=25°C
Frequency	fL	-	(60)	-	kHz	
Lamp Current (1Lamp) (Note6)	IL	(1.2) (Note 2)	(1.8)	(2.5) (Note 2)	mA	Ta=25°C
Starting discharge Voltage	VS (Note 2)	(1400)	-	-	Vrms	Ta=5°C

- (Note 1) Please design your lamp driving circuit (inverter) according to the above specifications, and inform Hitachi of it.
- (Note 2) Starting discharge voltage is increased when LCM is operating at low er temperature. Please check the characteristics of your inverter before applying to your set.
- (Note 3) Average life time of CFL will be decreased when LCM is operating at low er temperature.
- (Note 4) Under low er driving frequency of an inverter, a certain backlight system (CFL & CFL reflection sheet) may generate a sound noise. Before designing the inverter, please consider the driving frequency and the noise.



(Note 6)

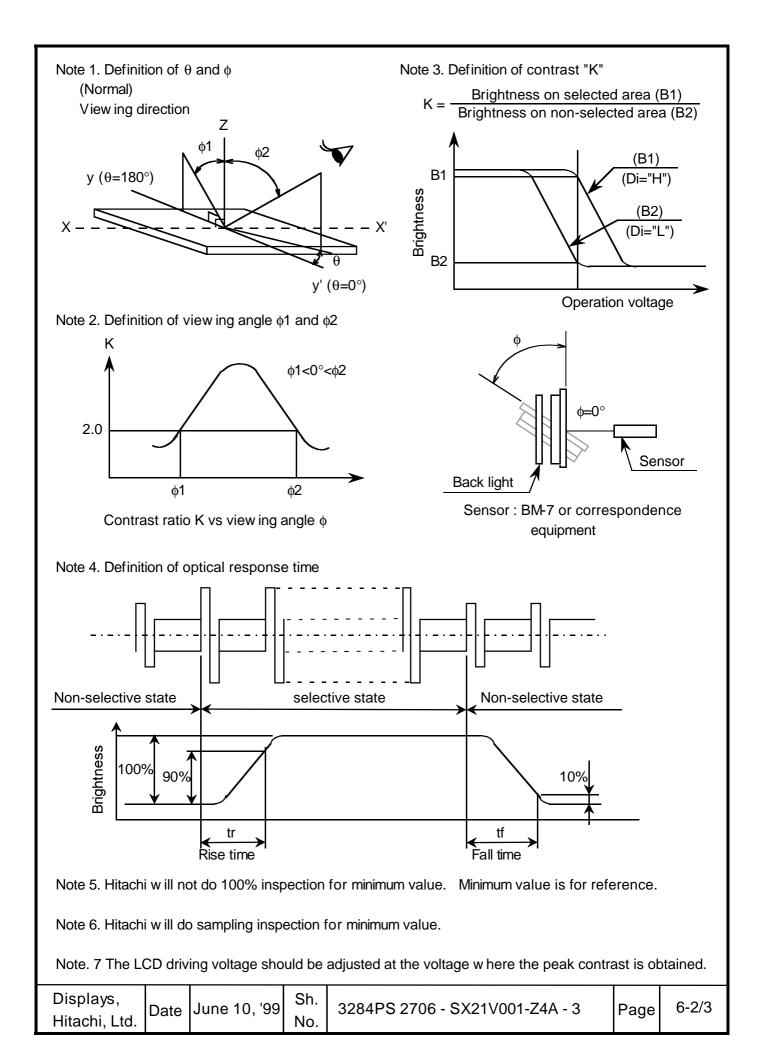
	H.V		IN	GND		
Displays, Hitachi, Ltd.	Date	June 10, '99	Sh. No.	3284PS 2705 - SX21V001-Z4A - 3	Page	5-3/3

6.1 OPTICAL CHA	RACTER	ISTICS OF	LCD	Ta=25°C (Backlight On)							
ПЕМ		SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	NOTE			
View ing area		φ2-φ1	θ=0°, K <u>≥</u> 2.0	-	(40)	-	deg	1),2)			
Contrast ratio		к	φ=0° , θ=0°	-	(50)	-	-	3),5),6)			
Response time (rise)		tr	φ=0° , θ=0°	-	(190)	-	ms	4)			
Response time (f	all)	tf	φ=0° , θ=0°	-	(160)	-	ms	4)			
Color tone	Red	х		-	(0.54)	-	-				
(Primary Color)		у		-	(0.33)	-	-				
	Green	х		-	(0.31)	-	-				
	Green	у		-	(0.51)	-	-	7)			
	Dhuo	х	φ = 0°, θ = 0°	-	(0.17)	-	-	7)			
	Blue	у		-	(0.17)	-	-				
	\A/bita	х		-	(0.29)	-	-				
	White	у		-	(0.31)	-	-				

(Measurement condition : Hitachi standard)

Note 1)~7) : See next page.

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6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

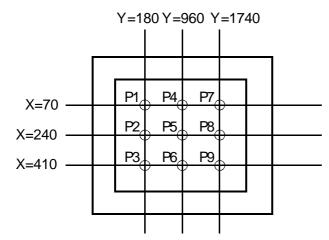
ПЕМ	MIN	TYP	MAX	UNIT	NOTE
Brightness	-	(58)	-	cd/m ²	IL=1.8mA Note 1),2)
Rise Time	-	5	-	Minute	IL=1.8mA Brightness 80%
Brightness Uniformity	-	-	±30	%	Undermentioned Note 1),4)

Measurement condition : Hitachi standard)

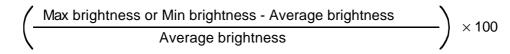
CFL : INITIAL, Ta=25°C

Display data should be all "ON" The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained, when set pattern is all "Q".

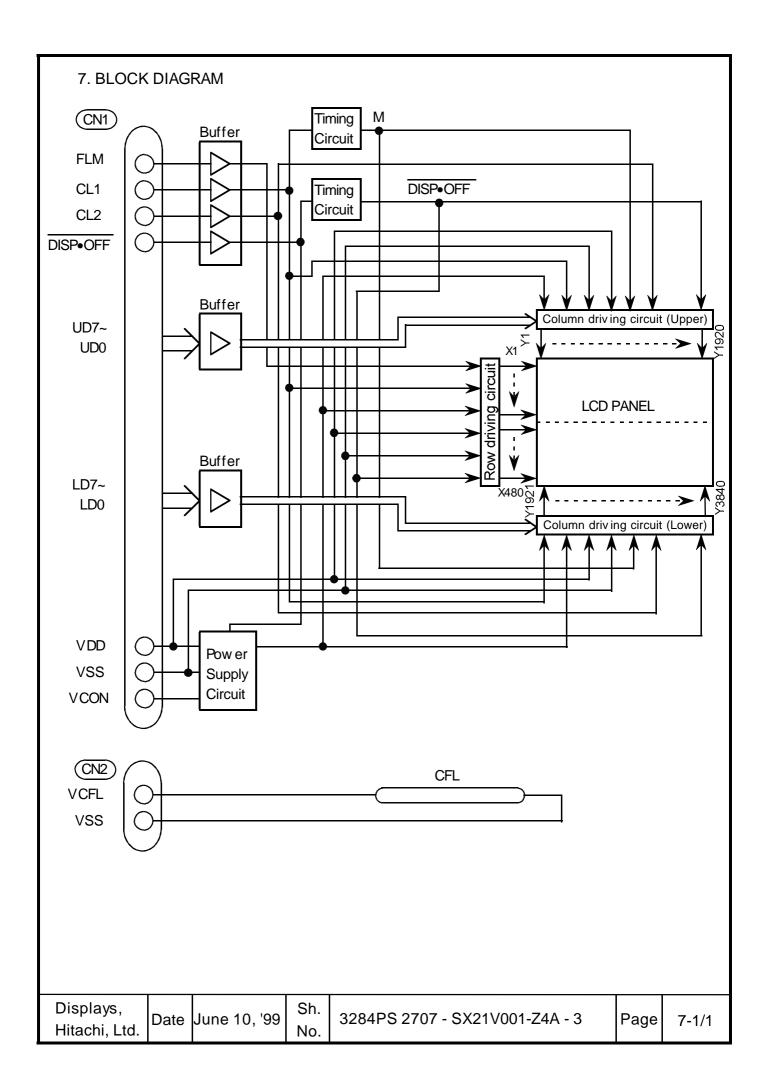
- (Note 1) Measurement after 10 minutes from CFL operating. Average value of 9 points (Note 3).
- (Note 2) Brightness control : 100%
- (Note 3) Measurement of the following 9 places on the display.

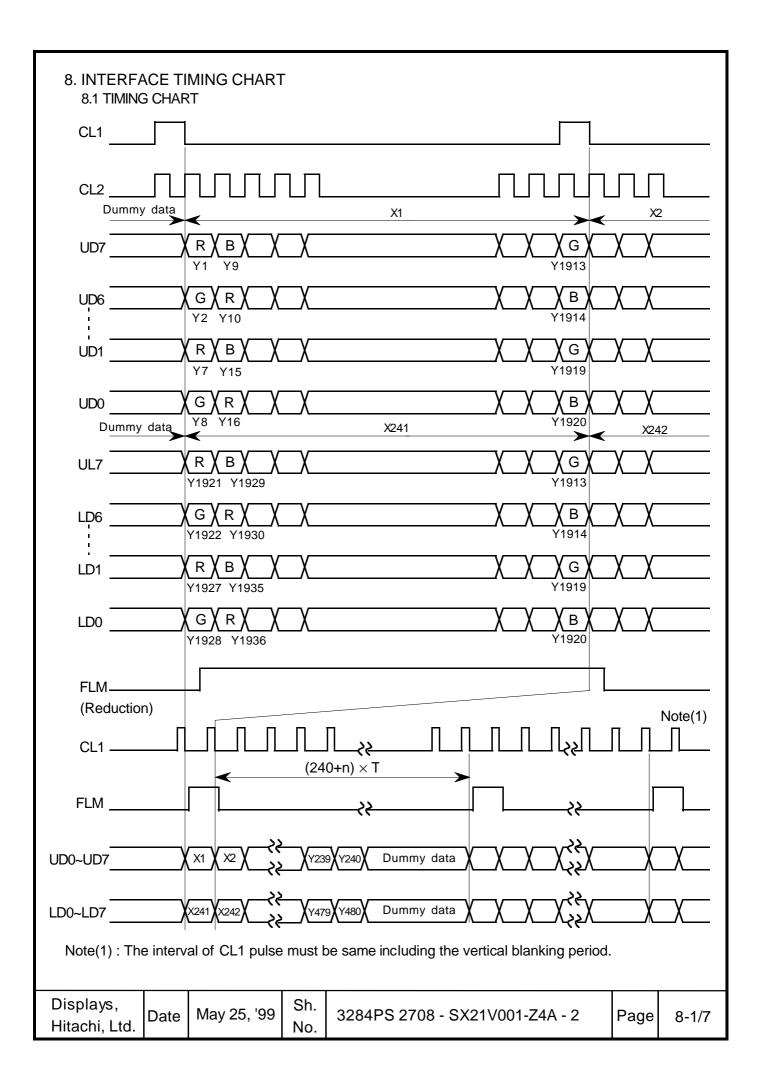


(Note 4) Definition of the brightness tolerance.



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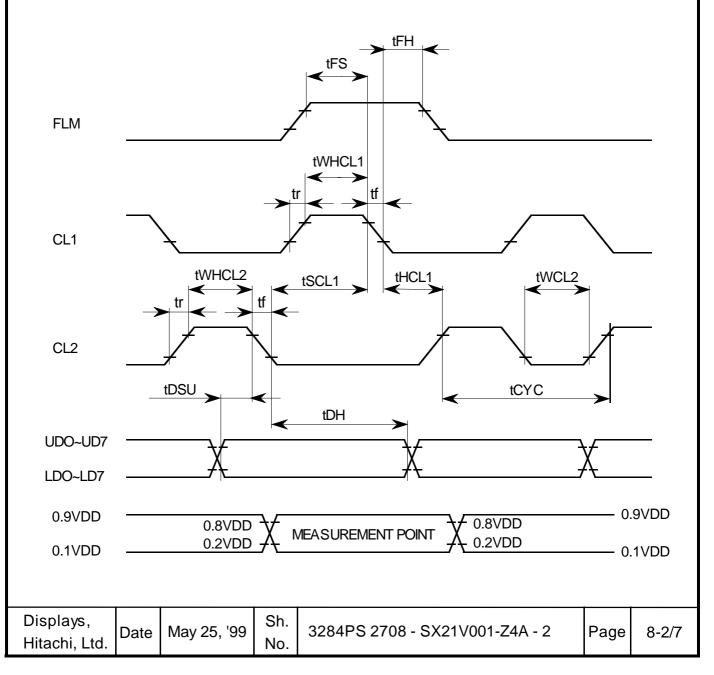


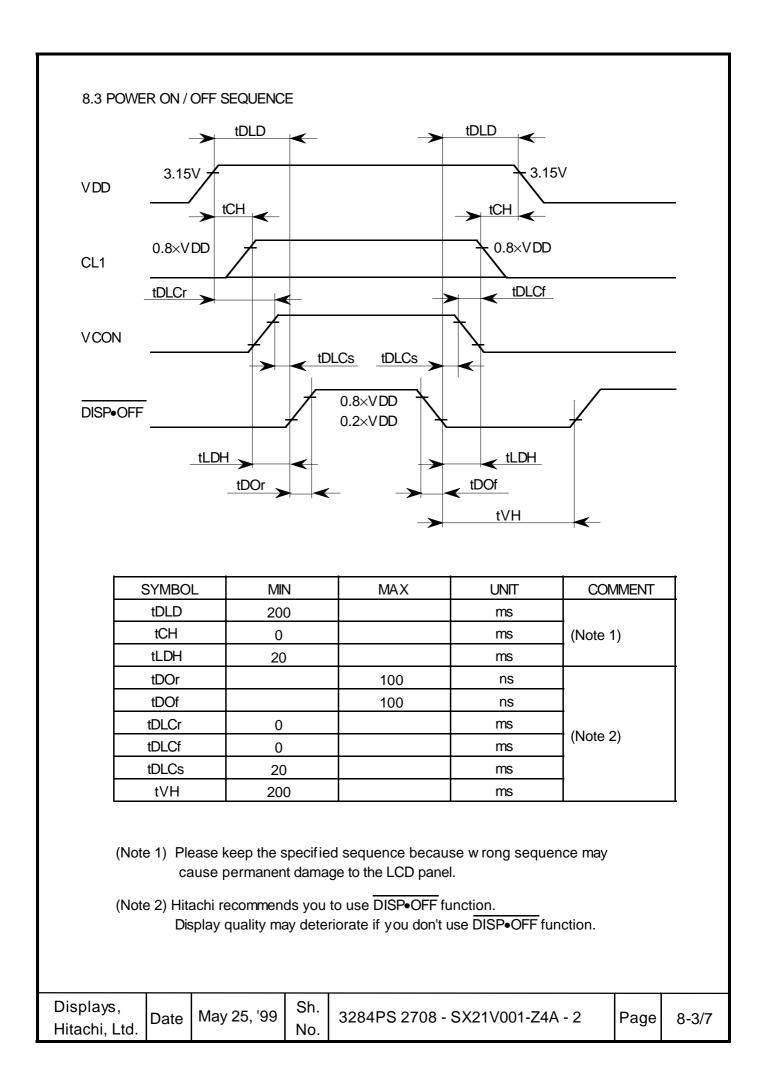


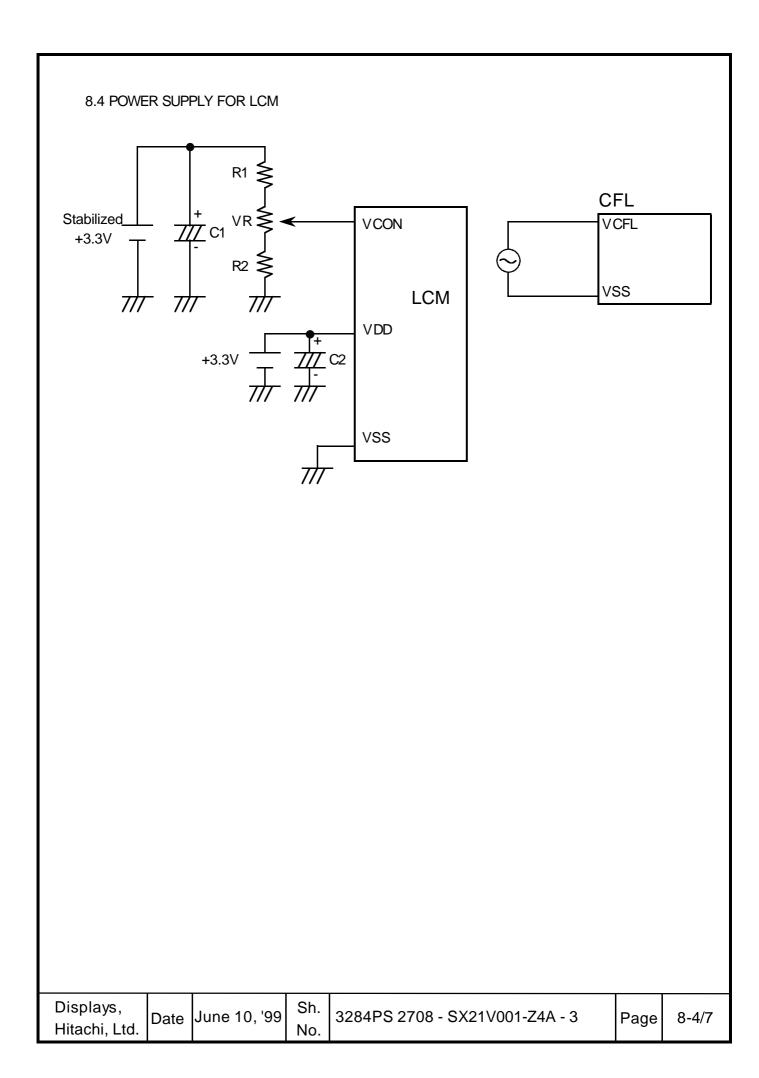
8.2 INTERFACE TIMING SPECIFICATION

VD	D=3.3±0.15	∕, VSS=0V,	Vcon=0.8~2	2.8V, Ta=+5	°C~+40°C
ПЕМ	SYMBOL	MIN	TYP	MAX	UNIT
CL1 pulse width "H"	tWHCL1	65			ns
Clock cycle time	tCYC	40			ns
CL2 pulse w idth	tWCL2	15			ns
Clock set up time	tSCL1	20			ns
Clock hold time	tHCL1	50			ns
Clock rise fall time	tr, tf			30	ns
Data set up time	tDSU	10			ns
Data hold time	tDH	10			ns
"FLM" set up time	tFS	100			ns
"FLM" hold time	tFH	50			ns









8.5 INPUT DATA ALLOCATION TABLE

X 1 R G B	Data	a Signal	U D 7	U D 6	U D 5	U D 4	U D 3	U D 2	U D 1	U D 0	U D 7	U D 6	U D 5	U D 4	 U D 4	U D 3	U D 2	U D 1	U D 0
X 1 R G B		Y	1	2	3	4	5	6	7	8	9	10	11	12	 9	9	9	9	1 9
Image: Constraint of the state of	х														-		-	-	2 0
3 R G B R		1	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		2	R		В		G	В			В	R	G	В	 G	В	R	G	В
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	Ш	3	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	AN	4	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	Υ Α	5	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	PPEF	1	1	1	1	1	1			1	1		1	1	1		1	I	1 1 1
240 R G B R		238	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
241 R G B R		239	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
1 1		240	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
1 243 R G B		241	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
HV 244 R G B		242	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
470 R G B R		243	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
470 R G B R	NE	244	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
470 R G B R	P/	245	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
470 R G B R	WER	1	1	1	1	1	1	1			1		1	1		1	1	1	1
480 R G B B D D D D D D D D D D D D D D D D D D <thd< th=""> <thd< th=""> <thd< th=""></thd<></thd<></thd<>	LC LC	478	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
X 1 <th1< th=""> <th1< th=""> <th1< th=""></th1<></th1<></th1<>		479	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
X 9		480	R	G	В	R	G	В	R	G	В	R	G	В	 G	В	R	G	В
Y 1 2 3 4 5 6 7 8 9 0 1 2 6 7 8 9 0	х																		3 8
		Y																	4 0
Data Signal D D D D D D D D D D D D D D D D D D D	Data	a Signal	D	D	D	D	D	D	D	D	D	D	D	D	 D	D	D	D	L D 0

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8.6 INTERNAL PIN CONNECTION

CN1 MOLEX 54104-3511

PIN No.	SIGNAL	LEVEL	FUNCTION
1	LD0	H/L	Display Data (Low er Column)
2	LD1	H/L	Display Data (Low er Column)
3	LD2	H/L	Display Data (Low er Column)
4	LD3	H/L	Display Data (Low er Column)
5	VSS		GND
6	LD4	H/L	Display Data (Low er Column)
7	LD5	H/L	Display Data (Low er Column)
8	LD6	H/L	Display Data (Low er Column)
9	LD7	H/L	Display Data (Low er Column)
10	VSS	-	GND
11	FLM	Н	First Line Marker
12	DISPOFF	H/L	H:ON/L:OFF
13	CL1	H→L	Data Latch
14	VSS	-	GND
15	CL2	H→L	Data Shift
16	VSS	-	GND
17	VDD	-	Pow er Supply for Logic
18	VDD	-	Pow er Supply for Logic
19	VCON	-	Contrast Adjust
20	VSS	-	GND
21	VDD	-	GND
22	UD0	H/L	Display Data (Upper Column)
23	UD1	H/L	Display Data (Upper Column)
24	UD2	H/L	Display Data (Upper Column)
25	UD3	H/L	Display Data (Upper Column)
26	VSS	-	GND
27	UD4	H/L	Display Data (Upper Column)
28	UD5	H/L	Display Data (Upper Column)
29	UD6	H/L	Display Data (Upper Column)
30	UD7	H/L	Display Data (Upper Column)
31	VSS	-	GND
32	DIG Y2	-	Touch panel Y2
33	DIG X2	-	Touch panel X2
34	DIG Y1	-	Touch panel Y1
35	DIG X1	-	Touch panel X1

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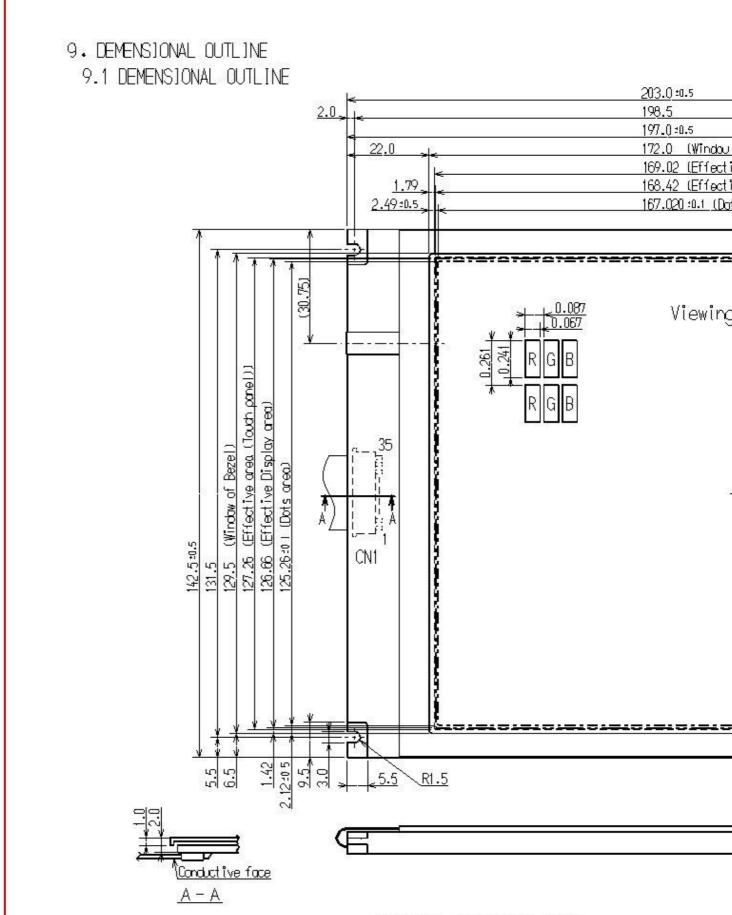
CN2 JST : BHSR-02VS-1 (Suitable Connector : (1) SM02B-BHSS-1-TB or or

(1) housing : BHSMR-02VS-1

contact pin : SBHSM-002T-P0.5

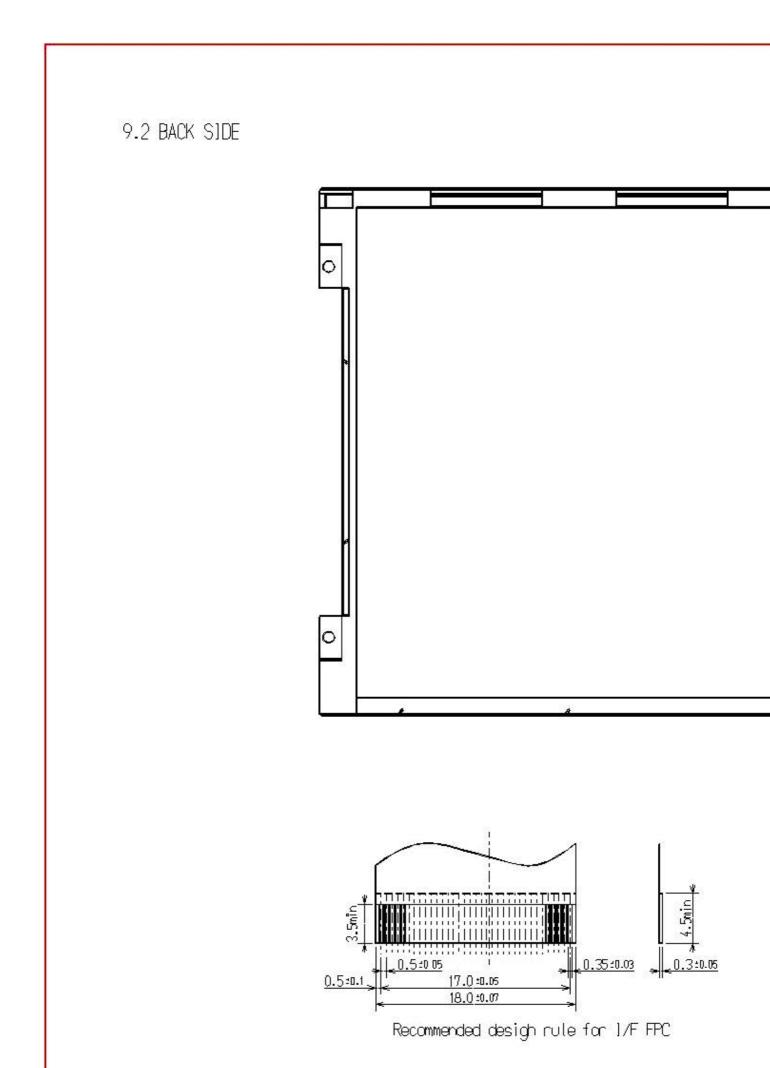
PIN No.	SIGNAL	LEVEL	FUNCTION
1	VCFL	A C	Pow er Supply for CFL
2	VSS	-	GND for CFL

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Mounting screw side. M2.5"

Note(1) Measurement when adding 9.8×10⁴ Pa (1.0kgf/cm²) at the measuring point



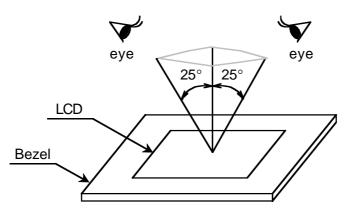
10. APPEARANCE STANDARD

10.1 APPEARANCE INSPECTION CONDITION

Visual inspection should be done under the follow ing condition.

- (1) The inspection should be done in a dark room.
- (2) The CFL should be lighted with the prescribed inverter.
- (3) The distance betw een eyes of an inspector and the LCD Module is 25cm.
- (4) The view ing zone is show n the figure.

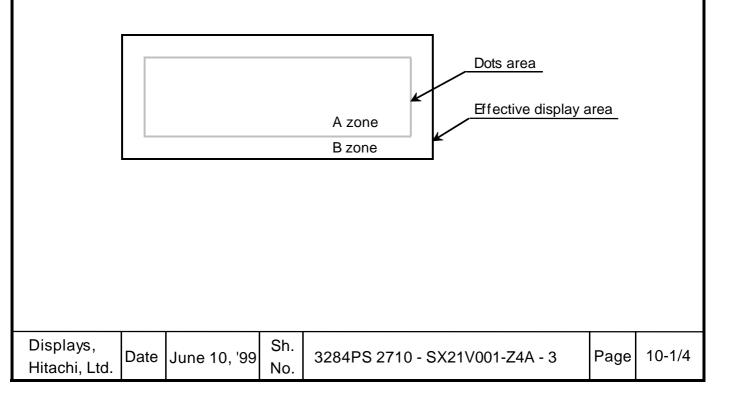
View ing angle $\leq 25^{\circ}$



10.2 DEFINITION OF ZONE

A zone : The effective display area specified at page 9-1/2 of this document.

B zone : Area betw een the window of bezel line and the effective display are (A zone) line specified at page 9-1/2 of this document.



10.3 APPEARANCE SPECIFICATION

(1) LCD APPEARANCE

*) If the problem related to this section occurs about this item, the responsible persons of both party (Customer and HITACHI) will discuss the matter in detail.

No.	ПЕМ		CRITERIA			A PPLIED ZONE			
	Scratches	Distinguished one i (To be judged by H	Distinguished one is not acceptable (To be judged by HITACHI STANDARD)						
	Dent	Same as above				A			
	Wrinkles in Polarizer	Same as above				A			
	Bubbles	Average diameter	D (mm)	Maximum	acceptable number				
		D <u>≤</u> 0.2	2		ignored				
L		0.2 < D ≤ 0.3	3		12	A			
		0.3 < D ≤ 0.5	5		3				
		0.5 < D			none				
	Stains,	Fila	mentous (l	Line shape))				
с	Foreign materials	Length L (mm)	mm) Width W (mm)		Maximum acceptable number	A,B			
	Dark spot	L <u>≤</u> 2.0	W <u>≤</u> 0.03		ignored				
		L <u>≤</u> 3.0	0.03 < W ≦ 0.05		6				
		L <u>≤</u> 2.5	0.05 < W <u>≤</u> 0.1		1				
_		Round (Dot shape)							
D		Average diameter D (mm)		imum ble number	Minimum space				
		D < 0.2	ign	ored					
		0.2 <u>≤</u> D < 0.3		10	10 mm	A,B			
		0.3 <u>≤</u> D < 0.4		5	30 mm	А,В			
		0.4 <u>≤</u> D	n	ione					
		The total number	The total number Filamentous + Round = 10						
		Those wiped out ea							
	Color tone	To be judged by HIT	TACHI STA	NDARD		A			
	Color uniformity	Same as above				A			

No.	ITEM		A PPLIED ZONE					
	Contrast irregularity (Spot)	Average diameter D (mm)	Contrast	Maximum acceptable number	Minimum space			
		D <u>≤</u> 0.25	Taha	ignored				
L		0.25 <d<u>≤0.35</d<u>	To be judged by	10	20mm	A		
	0.35 <d<u>≤0.5</d<u>	HITACHI	4	20mm]			
		0.5 <d<u>≤0.7</d<u>	STANDARD	3	50mm			
С		0.7 <d< td=""><td></td><td>none</td><td></td><td></td></d<>		none				
U	Contrast irregularity (Line)	Width W (mm)	Length L (mm)	Maximum acceptable number	Minimum space			
_	(A pair of scratches)	W <u>≤</u> 0.25	L <u>≤</u> 1.2	2	20mm			
D		W ≤ 0.2	L <u>≤</u> 1.5	3	20mm	A		
		W <u>≤</u> 0.15	L <u>≤</u> 2.0	3	20mm			
		W <u>≤</u> 0.1	L <u>≤</u> 3.0	4	20mm			
		The who	ole number	6				
	Rubbing Scratch	To be judged b	To be judged by HITACHI STANDARD					

(2) CFL BACKLIGHT APPEARANCE

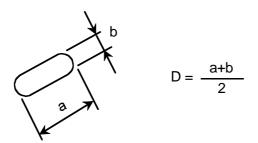
No.	ПЕМ	CRITERIA				
С	Dark spots	Average diameter	D (mm)	Maximum	Acceptable number	
F	White spots Foreign materials	D <u>≤</u> 0.	4		ignored	А
L	(Spot)	0.4 < D			none	
B	Foreign materials B (Line) A C	Width W (mm)	Length	L (mm)	Maximum acceptable number	
		W <u>≤</u> 0.2	L <u>≤</u> 2.5		1	А
С			2.5 < L	-	none	~
к		0.2 < W			none	
L	Scratches	Width W (mm)	Length L (mm)		Maximum acceptable number	
G		W <u>≤</u> 0.1	_		ignored	
		0.1 < W <u>≤</u> 0.2		L <u>≤</u> 11.0	1	А
Т			11.0 <	L	none	
		0.2 < W			none	

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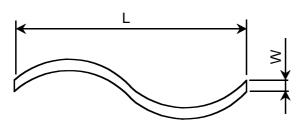
(3) TOUCH PANEL A PPEARANCE

No.	ПЕМ	CRITEF	RIA	A PPLIED ZONE
	Foreign material	Average diameter D (mm)	Criteria	
	(Black or White spots)	D <u>≤</u> 0.25	ignored	А
Т		0.25 < D ≤ 0.35	6	
o		0.35 < D	none	
U	Foreign material (Line)	Width W (mm)	Criteria	
С	or	W <u>≤</u> 0.05	ignored	
н	Scratches	0.05 < W <u>≤</u> 0.1	10 <u>≤</u> L : none L < 10 : 4	A
Р		0.1 < W	Spote spec	
А	Fisheyes on film surface	Average diameter D (mm)	Criteria	
Ν		D≦0.2	ignored	
E		$0.2 < D \le 0.35$	6	А
		0.35 < D ≦ 0.6	2	
		0.6 < D	none	
	Uncleanliness	No conspicuous dirt		А
	Crack in glass plate	No cracks are allow ed		Α

Note (1) Definition of Average diameter (D)



Note (2) Definition of Length (L) and Width (W)

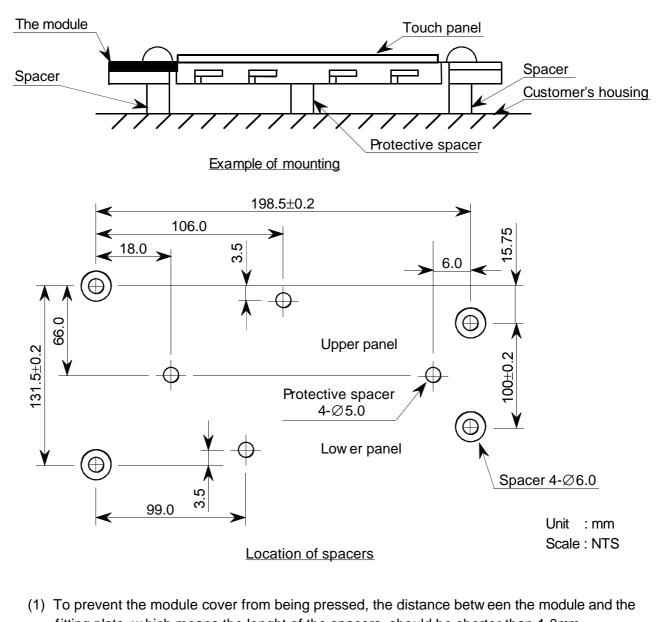


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11. PRECAUTION IN DESIGN

11.1 MOUNTING PRECAUTION

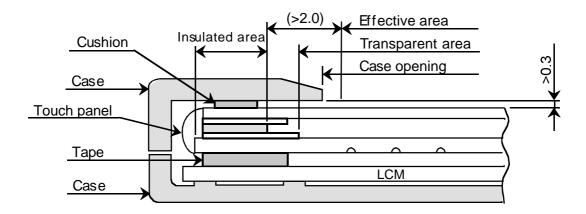
Please mount the LCD Module using mounting holes arranged in 4 corners, and please pay attention to the follow ings.



- fitting plate, which means the lenght of the spacers, should be shorter than 1.0mm.(2) We recommend you to use protective spacers in order to protect the module from any kinds of
- shocks to your set.
- (3) For the module to be used at upright position, the case shall have a structure where the touch panel screen does not shift with its ow n w eight.

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(4) When assembling the touch panel and your case, please refer to the figure below.



- (5) The clearance betw een the touch panel and the case shall be designed so that the case edge never presses the input screen when it is deformed by heat or other causes.
- (6) The case shall be designed not to touch the tail portion (FPC for touch panel).

11.2 PRECAUTIONS AGAINST ELECTROSTATIC DISCHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band etc. And don't touch I/F pins directly.

11.3 POWER ON SEQUENCE

Input signals should not be applied to LCD module before power supply voltage is applied and reaches to specified voltage $(3.0\pm0.15V)$.

If the above sequence is not kept, C-MOS LSIs of LCD module may be damaged due to latch up phenomenon.

11.4 HANDLING PRECAUTIONS

- (1) Since the polarizer on the top, and the aluminum plate on the bottom tend to be easily damaged, they should be handled with full care so as not to get them touched, pushed or rubbed by a piece on glass, tw eezers and anything else which are harder than a pencil lead 3H.
- (2) As the adhesives used for adhering upper/low er polarizers and aluminum plate are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, tuluene, ethanole and isopropylalcohol. The following solvents are recommended for use : Normal hexane

Please contact us when it is necessary for you to use chemicals other than the above.

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- (3) Lightly wipe to clean the dirty surface with absorbent cotton or other soft material like chamois, soaked in the recommended chemicals without scrubbing it hardly. Always wipe the surface horizontally or vertically. Never give a wipe in a circle. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (4) Immediately wipe off saliva or water drop attached on the display area because it may cause deformation or faded color.
- (5) Fogy dew deposited on the surface may cause a damage, stain or dirt to the polarizer. When you need to take out the LCD module from some place at low temperature for test, etc. It is required to be warmed them up to be temperature higher than room temperature before taking them out.
- (6) Touching the display area or *V*F pins with bare hands or contaminating them are prohibited, because the stain on the display area and poor insulation betw een terminals are often caused by being touched with bare hands.
 (Some competing are detrimental to polarizers.)

(Some cosmetics are detrimental to polarizers.)

- (7) In general, the glass is fragile so that it, especially on its periphery, tends to be cracked or chipped in handling. Please do not give the LCD module sharp shocks caused by falling etc.
- Maximum pressure to the surface must be less than 1.96×10⁴ Pa (0.2kgf/cm²).
 And if the pressure area is less than 1cm², maximum pressure must be less than 1.96N (0.2kgf).
- (9) Since the metal width is narrow on these locations (see page 9-1/2), please be careful with handling.
- (10) Top sheets shall be cleaned gently using a soft cloth such as those used for glasses. Hard w iping accumulated dust w ill leave scars on the surface even using a cloth.

11.5 OPERATION PRECAUTION

- Using a LCM module beyond its maximum ratings may result in its permanent destruction. LCM module's should usually be used under recommended operating conditions show n in chapter 5. Exceeding any of these conditions may adversely affect its reliability.
- (2) Response time will be extremely delayed at low er temperature than the specified operating temperature range and on the other hand LCD's show s dark blue color at higher temperature. How ever those phenomena do not mean defects of the LCD module. Those phenomena will disappear in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some display patterns will be abnormally displayed.

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- (4) A slight dew depositing on terminals may cause electrochemical reaction which leads to terminal open circuit. Please operate the LCD module under the relative condition of 40°C 85%RH.
- (5) Resistance renge : Your controller shall be set up to allow the resistance range of touch panel specified in our CAS.
- (6) Pointed position of touch panel may shift ow ing to a change in resistance of touch panel depending on the operation condition. To compensate this shift, the set shall be given a calibration function.
- (7) Input shall be made with a stylus pen (polyacetal, R0.8). Chances are very high that use of a metal piece including a ball point pen or sharp edge will impair accuracy.
- (8) The touch panel is an auxiliary input device. The system shall be designed to have other input device.

11.6 STORAGE

In case of storing LCD module for a long period of time (for instance, for years) for the purpose of replacement use, the following precautions necessary.

- (1) Store the LCD modules in a dark place ; do not expose them to sunlight or ultraviolet rays.
- (2) Keep the temperature betw een 10°C and 35°C at normal humidity.
- (3) Store the LCD modules in the container which is used for shipping from us.
- (4) No articles shall be left on the surface over an extended period of time.

11.7 SAFETY

The LCD modules include Cold Cathode Fluorescent Lamp (CFL). CFL contains a small amount of mercury. Please follow local ordinances or regulations for disposal.

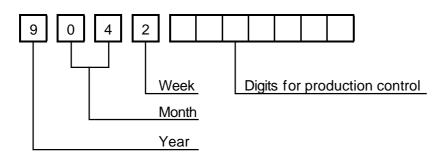
Wear finger cots or gloves whenever handling or assembling a touch panel because its glass edges are sharp.

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12. DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 4 digits for production lot and 6 or 7 digits for production control.



Year	Figure in lot mark
1999	9
2000	0
2001	1
2002	2

Month	Figure in lot mark	Month	Figure in lot mark
Jan.	01	July	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
June	06	Dec.	12

Week (day in Calender)	Figure in lot mark
1~7	1
8~14	2
15~21	3
22~28	4
29~31	5

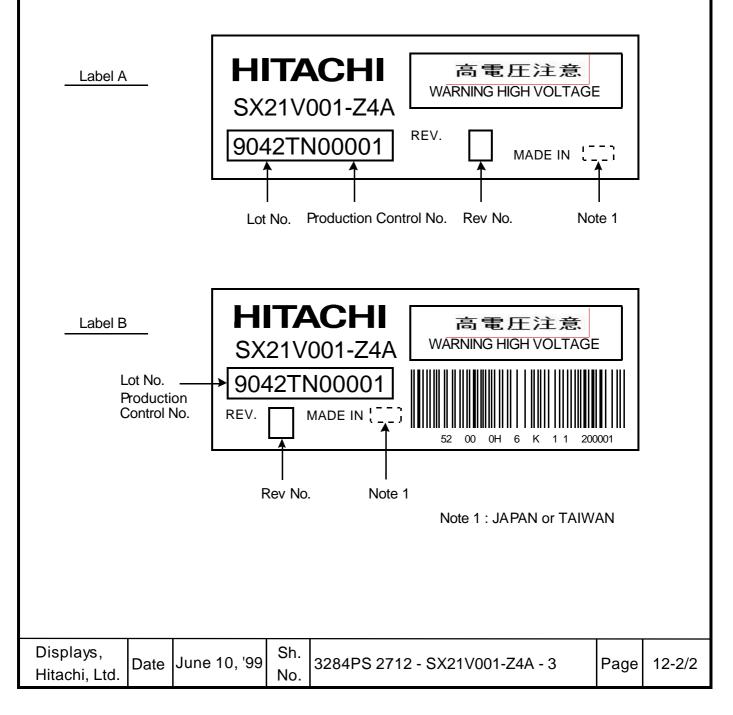
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12.2 REVISION

REV No.	ITEM	LOT No.	PRODUCTION CONTROL No.
А			00001~
В	Changed PCB		

12.3 LOCATION OF LOT MARK

Either Label A or Label B is being attached on the back side of LCM.



13. PRECAUTION FOR USE

A limit sample should be provided by the both parities on an occasion when the both parties agree to its necessity.
 Judgment by a limit sample shall take effect after the limit sample has been established and

Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

- (2) On the follow ing occasions, the handling of the problem should be decided through discussion and agreement betw een responsible persons of the both parties.
 - (1) When a question is arisen in the specifications.
 - (2) When a new problem is arisen which is not specified in the specifications.
 - (3) When an inspection specification change or operating condition change by customer is reported to HITACHI, and some problem is arisen in the specification due to the change.
 - (4) When a new problem is arisen at the customer's operating set for sample evaluation
- (3) Regarding the treatment for maintenance and repairing, both parties will discuss it in six month later after latest delivery of this product.

The precaution that should be observed when handling LCM have been explained above. If any points are unclear or if you have any requests, please contact Hitachi.

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