

# MMBT4403LT1

## Switching Transistor

### PNP Silicon

#### Features

- Pb-Free Package is Available

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	$V_{CEO}$	–40	Vdc
Collector–Base Voltage	$V_{CBO}$	–40	Vdc
Emitter–Base Voltage	$V_{EBO}$	–5.0	Vdc
Collector Current – Continuous	$I_C$	–600	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board (Note 1) $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	225 1.8	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	556	$^\circ\text{C/W}$
Total Device Dissipation Alumina Substrate, (Note 2) $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	300 2.4	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	417	$^\circ\text{C/W}$
Junction and Storage Temperature	$T_J, T_{stg}$	–55 to +150	$^\circ\text{C}$

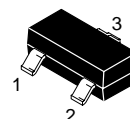
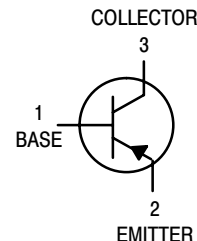
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

- FR–5 =  $1.0 \times 0.75 \times 0.062$  in.
- Alumina =  $0.4 \times 0.3 \times 0.024$  in. 99.5% alumina.



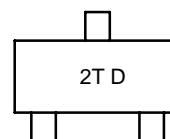
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**SOT–23 (TO–236)  
CASE 318–08  
STYLE 6**

#### MARKING DIAGRAM



2T = Specific Device Code  
D = Date Code

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

# MMBT4403LT1

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector–Emitter Breakdown Voltage (Note 3) ( $I_C = -1.0\text{ mA}$ , $I_B = 0$ )	$V_{(BR)CEO}$	-40	–	Vdc
Collector–Base Breakdown Voltage ( $I_C = -0.1\text{ mA}$ , $I_E = 0$ )	$V_{(BR)CBO}$	-40	–	Vdc
Emitter–Base Breakdown Voltage ( $I_E = -0.1\text{ mA}$ , $I_C = 0$ )	$V_{(BR)EBO}$	-5.0	–	Vdc
Base Cutoff Current ( $V_{CE} = -35\text{ Vdc}$ , $V_{EB} = -0.4\text{ Vdc}$ )	$I_{BEV}$	–	-0.1	$\mu\text{Adc}$
Collector Cutoff Current ( $V_{CE} = -35\text{ Vdc}$ , $V_{EB} = -0.4\text{ Vdc}$ )	$I_{CEX}$	–	-0.1	$\mu\text{Adc}$

## ON CHARACTERISTICS

DC Current Gain ( $I_C = -0.1\text{ mA}$ , $V_{CE} = -1.0\text{ Vdc}$ ) ( $I_C = -1.0\text{ mA}$ , $V_{CE} = -1.0\text{ Vdc}$ ) ( $I_C = -10\text{ mA}$ , $V_{CE} = -1.0\text{ Vdc}$ ) ( $I_C = -150\text{ mA}$ , $V_{CE} = -2.0\text{ Vdc}$ ) (Note 3) ( $I_C = -500\text{ mA}$ , $V_{CE} = -2.0\text{ Vdc}$ ) (Note 3)	$h_{FE}$	30 60 100 100 20	– – – 300 –	–
Collector–Emitter Saturation Voltage (Note 3) ( $I_C = -150\text{ mA}$ , $I_B = -15\text{ mA}$ ) ( $I_C = -500\text{ mA}$ , $I_B = -50\text{ mA}$ )	$V_{CE(sat)}$	– –	-0.4 -0.75	Vdc
Base–Emitter Saturation Voltage (Note 3) ( $I_C = -150\text{ mA}$ , $I_B = -15\text{ mA}$ ) ( $I_C = -500\text{ mA}$ , $I_B = -50\text{ mA}$ )	$V_{BE(sat)}$	-0.75 –	-0.95 -1.3	Vdc

## SMALL–SIGNAL CHARACTERISTICS

Current–Gain — Bandwidth Product ( $I_C = -20\text{ mA}$ , $V_{CE} = -10\text{ Vdc}$ , $f = 100\text{ MHz}$ )	$f_T$	200	–	MHz
Collector–Base Capacitance ( $V_{CB} = -10\text{ Vdc}$ , $I_E = 0$ , $f = 1.0\text{ MHz}$ )	$C_{cb}$	–	8.5	pF
Emitter–Base Capacitance ( $V_{BE} = -0.5\text{ Vdc}$ , $I_C = 0$ , $f = 1.0\text{ MHz}$ )	$C_{eb}$	–	30	pF
Input Impedance ( $I_C = -1.0\text{ mA}$ , $V_{CE} = -10\text{ Vdc}$ , $f = 1.0\text{ kHz}$ )	$h_{ie}$	1.5	15	k $\Omega$
Voltage Feedback Ratio ( $I_C = -1.0\text{ mA}$ , $V_{CE} = -10\text{ Vdc}$ , $f = 1.0\text{ kHz}$ )	$h_{re}$	0.1	8.0	$\times 10^{-4}$
Small–Signal Current Gain ( $I_C = -1.0\text{ mA}$ , $V_{CE} = -10\text{ Vdc}$ , $f = 1.0\text{ kHz}$ )	$h_{fe}$	60	500	–
Output Admittance ( $I_C = -1.0\text{ mA}$ , $V_{CE} = -10\text{ Vdc}$ , $f = 1.0\text{ kHz}$ )	$h_{oe}$	1.0	100	$\mu\text{mhos}$

## SWITCHING CHARACTERISTICS

Delay Time	$(V_{CC} = -30\text{ Vdc}$ , $V_{EB} = -2.0\text{ Vdc}$ , $I_C = -150\text{ mA}$ , $I_{B1} = -15\text{ mA}$ )	$t_d$	–	15	ns
Rise Time		$t_r$	–	20	
Storage Time	$(V_{CC} = -30\text{ Vdc}$ , $I_C = -150\text{ mA}$ , $I_{B1} = I_{B2} = -15\text{ mA}$ )	$t_s$	–	225	ns
Fall Time		$t_f$	–	30	

3. Pulse Test: Pulse Width  $\leq 300\text{ }\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

# MMBT4403LT1

## ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
MMBT4403LT1	SOT-23 (TO-236)	3000 Tape & Reel
MMBT4403LT1G	SOT-23 (TO-236) (Pb-Free)	3000 Tape & Reel
MMBT4403LT3	SOT-23 (TO-236)	10,000 Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## SWITCHING TIME EQUIVALENT TEST CIRCUIT

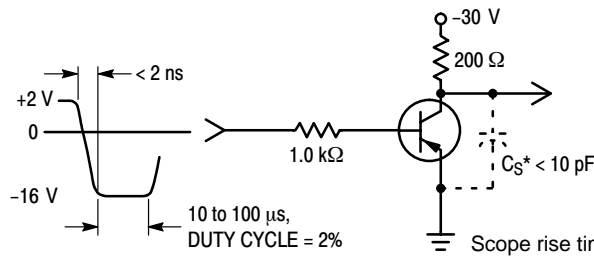


Figure 1. Turn-On Time

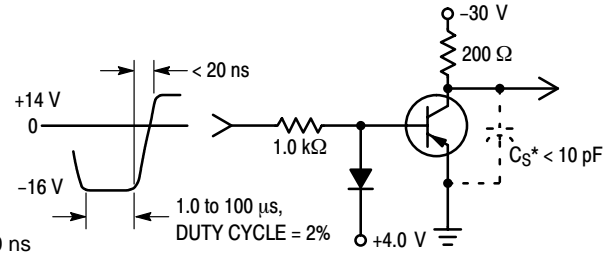


Figure 2. Turn-Off Time

# MMBT4403LT1

## TRANSIENT CHARACTERISTICS

— 25°C — 100°C

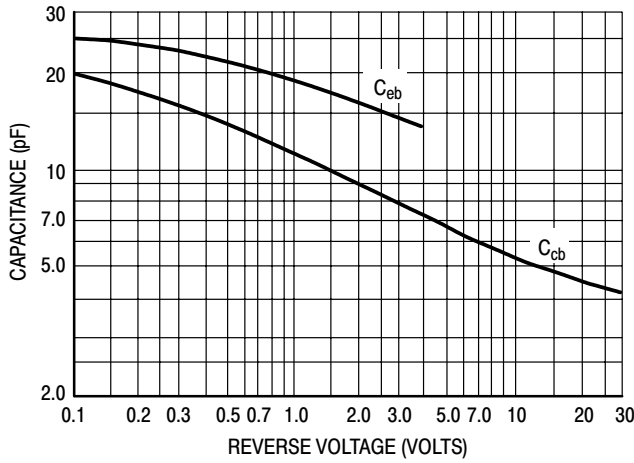


Figure 3. Capacitances

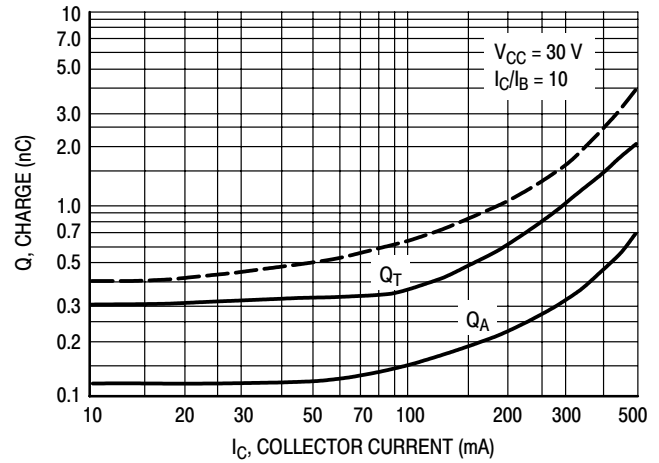


Figure 4. Charge Data

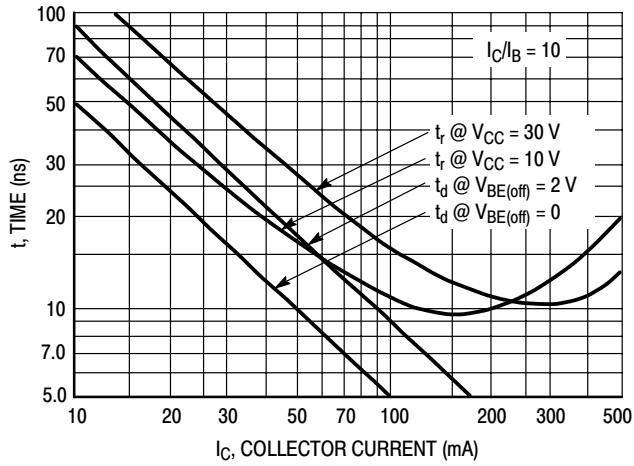


Figure 5. Turn-On Time

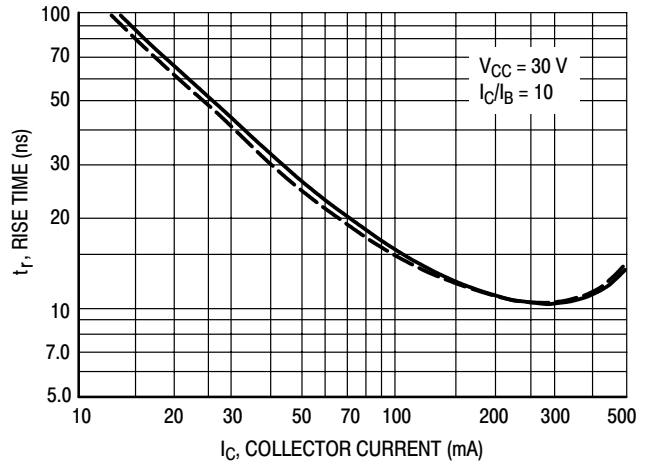


Figure 6. Rise Time

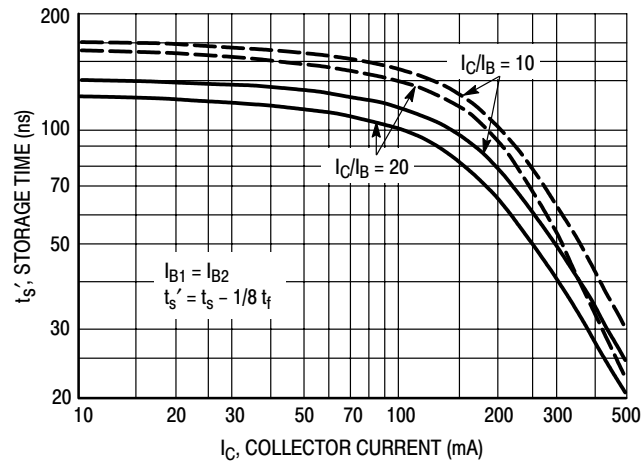


Figure 7. Storage Time

# MMBT4403LT1

## SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE

$V_{CE} = -10 \text{ Vdc}$ ,  $T_A = 25^\circ\text{C}$ ; Bandwidth = 1.0 Hz

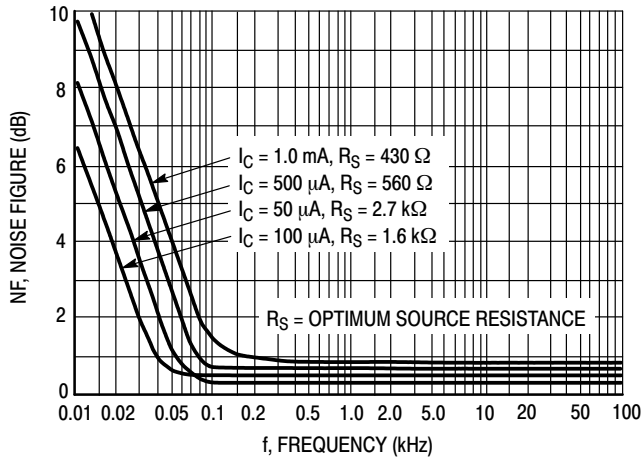


Figure 8. Frequency Effects

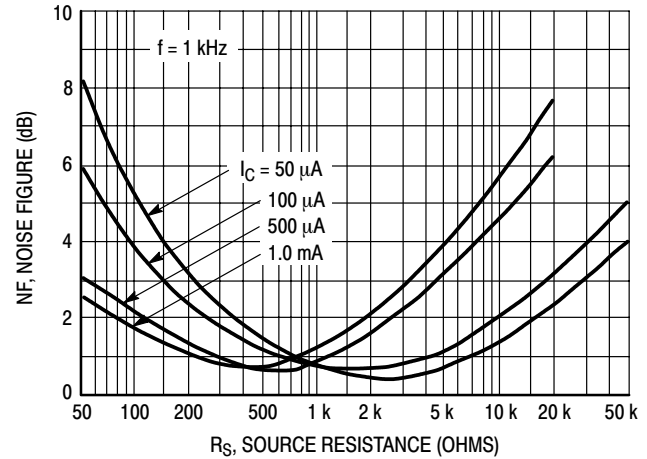


Figure 9. Source Resistance Effects

## h PARAMETERS

$V_{CE} = 10 \text{ Vdc}$ ,  $f = 1.0 \text{ kHz}$ ,  $T_A = 25^\circ\text{C}$

This group of graphs illustrates the relationship between  $h_{fe}$  and other “h” parameters for this series of transistors. To obtain these curves, a high-gain and a low-gain unit were selected from the MMBT4403LT1 lines, and the same units were used to develop the correspondingly numbered curves on each graph.

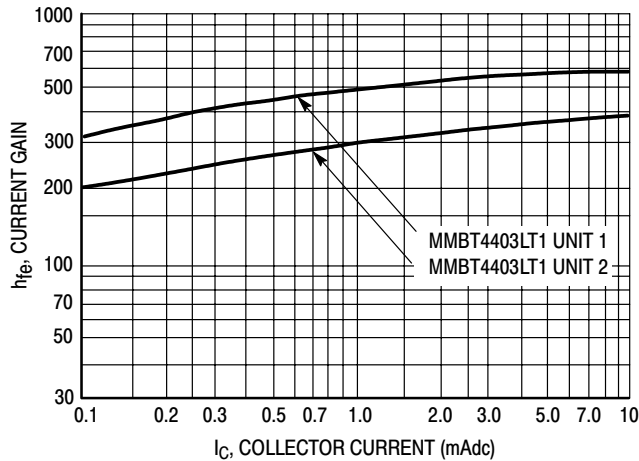


Figure 10. Current Gain

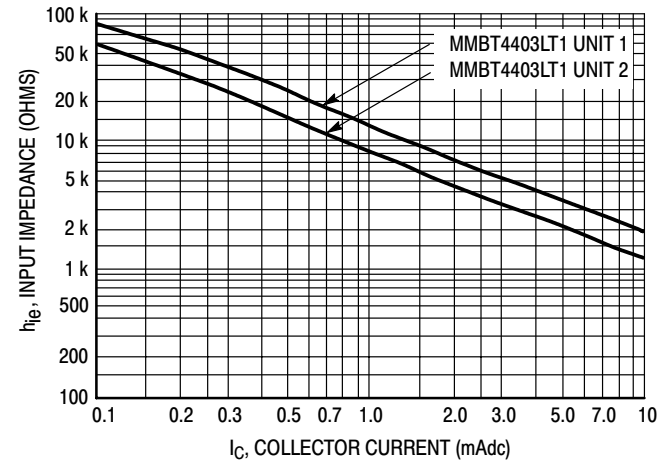


Figure 11. Input Impedance

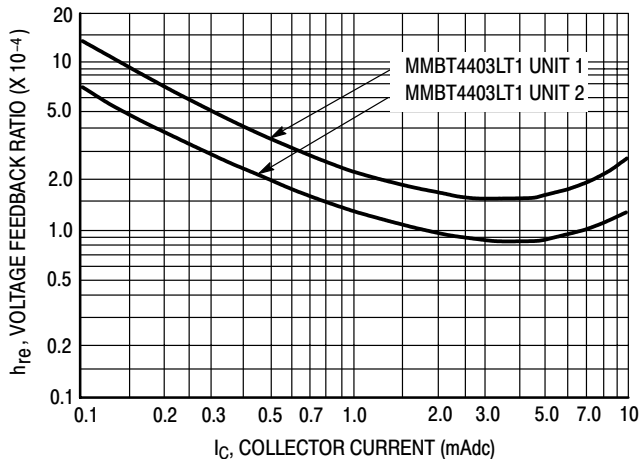


Figure 12. Voltage Feedback Ratio

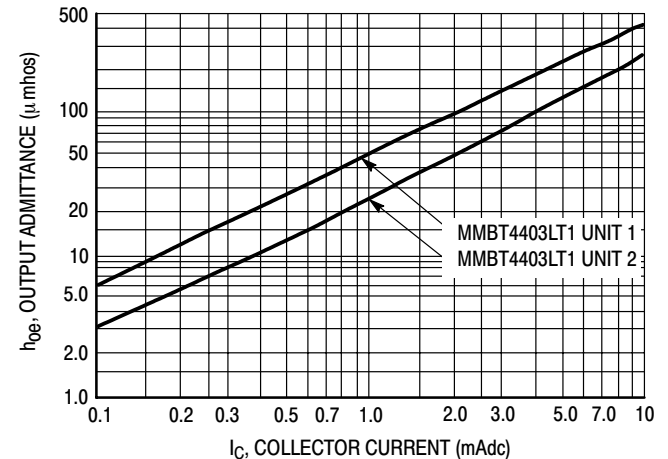


Figure 13. Output Admittance

# MMBT4403LT1

## STATIC CHARACTERISTICS

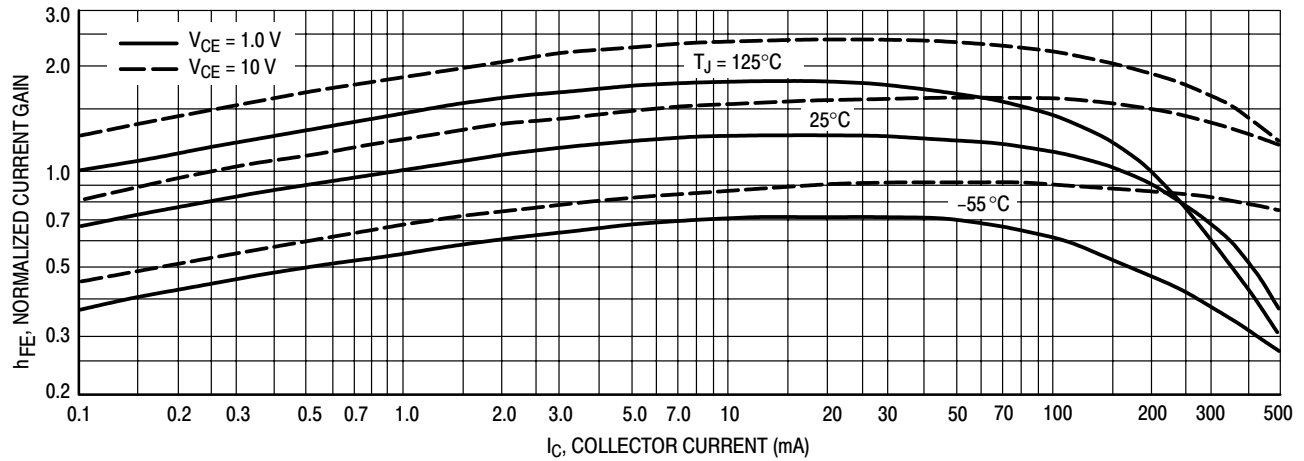


Figure 14. DC Current Gain

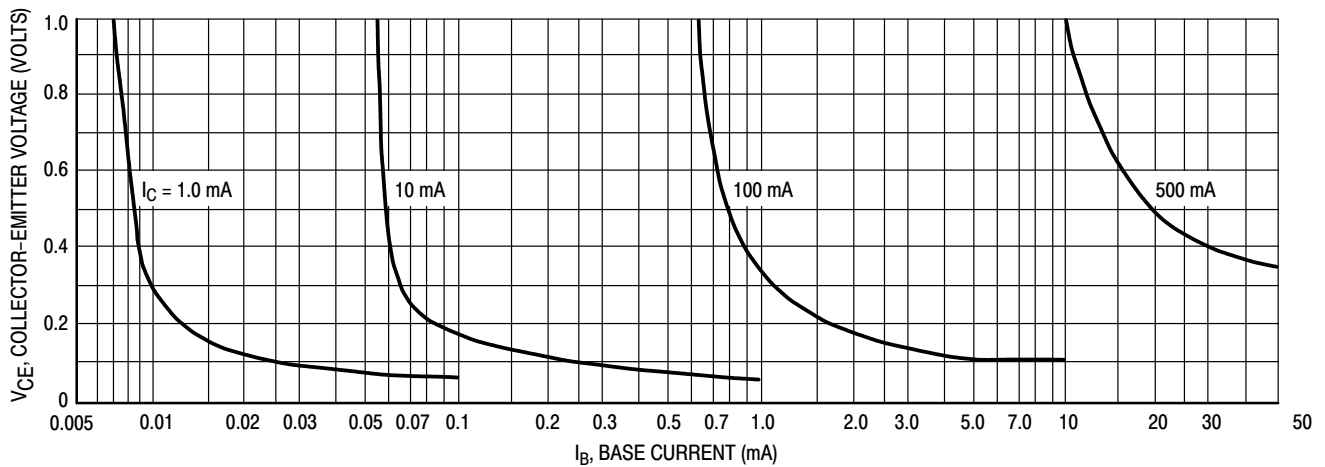


Figure 15. Collector Saturation Region

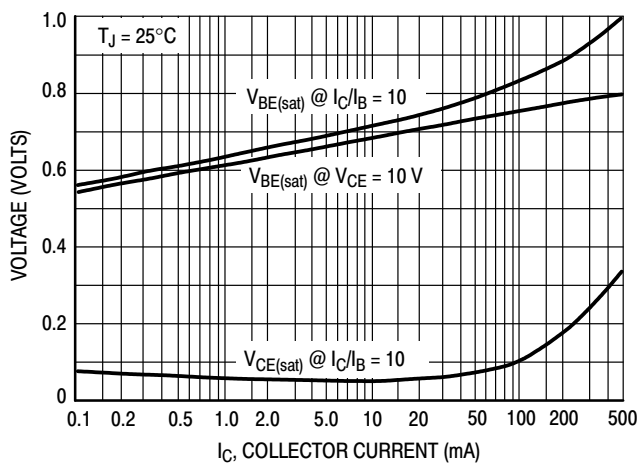


Figure 16. "On" Voltages

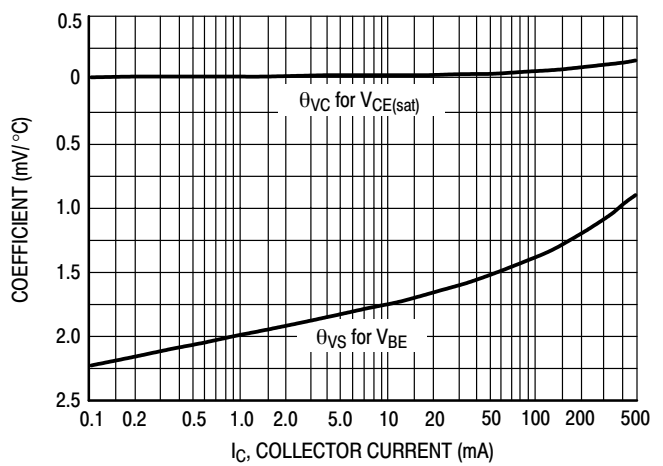
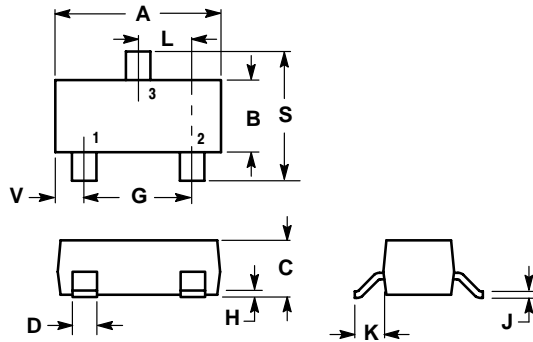


Figure 17. Temperature Coefficients

# MMBT4403LT1

## PACKAGE DIMENSIONS

**CASE 318-08**  
SOT-23 (TO-236)  
ISSUE AH



### NOTES:

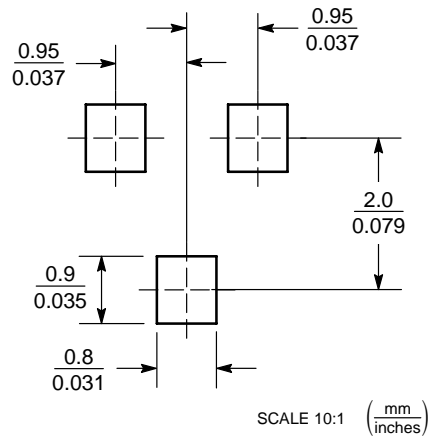
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. 318-03 AND -07 OBSOLETE, NEW STANDARD 318-08.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

### STYLE 6:

- PIN 1. BASE  
2. EMITTER  
3. COLLECTOR

## SOLDERING FOOTPRINT\*



## SOT-23

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MMBT4403LT1

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