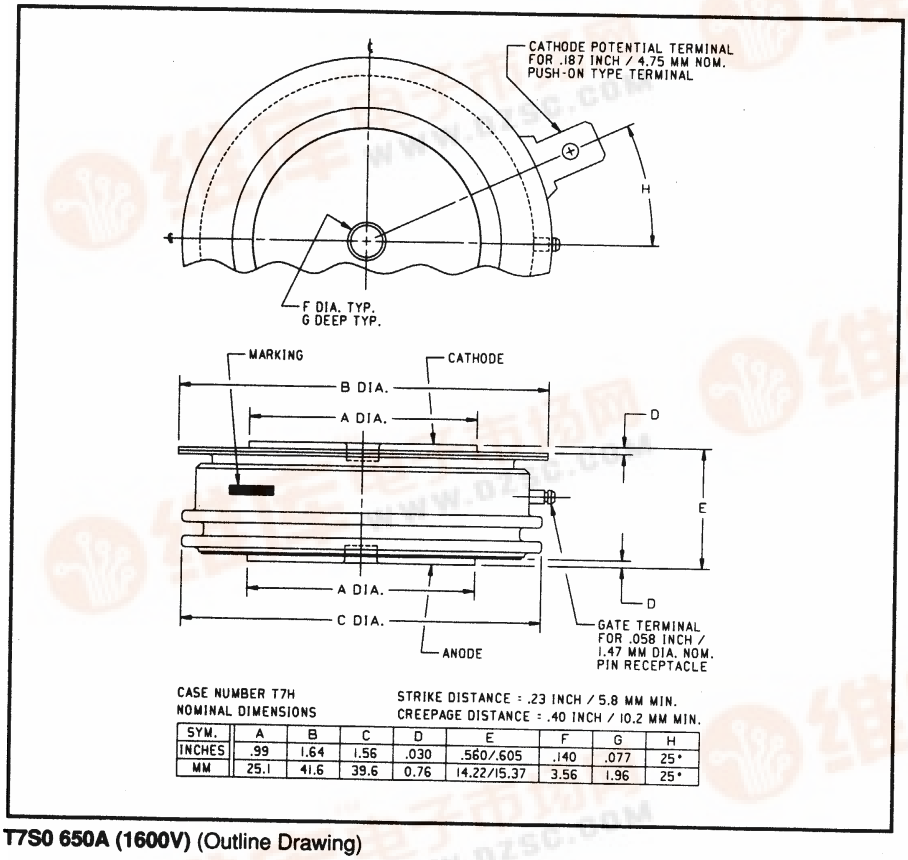
T7S0
650A (1600V)

Powerex, Inc., 200 Hillis Street, Youngwood, Pennsylvania 15697-1800 (412) 925-7272
Powerex, Europe, S.A. 428 Avenue G. Durand, BP107, 72003 Le Mans, France (43) 41.14.14

Phase Control SCR
650 Amperes Average
1600 Volts

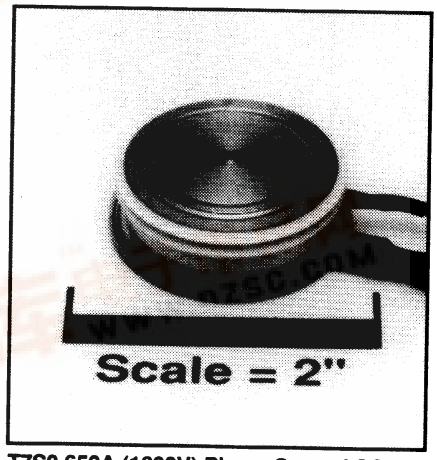


CASE NUMBER T7H
NOMINAL DIMENSIONS

STRIKE DISTANCE = .23 INCH / 5.8 MM MIN.
CREEPAGE DISTANCE = .40 INCH / 10.2 MM MIN.

SYM.	A	B	C	D	E	F	G	H
INCHES	.99	1.64	1.56	.030	.560/.605	.140	.077	25°
MM	25.1	41.6	39.6	0.76	14.22/15.37	3.56	1.96	25°

T7S0 650A (1600V) (Outline Drawing)



T7S0 650A (1600V) Phase Control SCR
650 Amperes Average, 1600 Volts

Description:
Powerex Silicon Controlled Rectifiers (SCR) are designed for phase control applications. These are all-diffused, Press-Pak, hermetic Pow-R-Disc devices employing the field proven amplifying gate.

Features:
- ☐ Low On-State Voltage
- ☐ High di/dt Capability
- ☐ High dv/dt Capability
- ☐ Hermetic Packaging
- ☐ Excellent Surge and I^2t Ratings

Applications:
- ☐ Power Supplies

Ordering Information:
Select the complete 12 digit part number you desire from the table below.

Type	Voltage V_{DRM}/V_{RRM} (Volts)	Current $I_{T(av)}$ (A)	Turn-off t_q (µsec)	Gate Current I_{GT} (mA)	Lead Code
T7S0	02 through 16	65	0	4	DN
	200V through 1600V	650A	150µsec (Typical)	150mA	8"

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Absolute Maximum Ratings

Characteristics	Symbol	T7S0 650A (1600V)	Units
Non-repetitive Transient Peak Reverse Voltage	V_{RSM}	V_{RRM} + 100V	Volts
RMS On-state Current, T_C = 65°C	$I_{T(rms)}$	1020	Amperes
Average Current 180° Sine Wave, T_C = 65°C	$I_{T(av)}$	650	Amperes
RMS On-state Current, T_C = 55°C	$I_{T(rms)}$	1125	Amperes
Average Current 180° Sine Wave, T_C = 55°C	$I_{T(av)}$	715	Amperes
Peak One Cycle Surge On-state Current (Non-repetitive) 60Hz	I_{tsm}	9000	Amperes
Peak One Cycle Surge On-state Current (Non-repetitive) 50Hz	I_{tsm}	8200	Amperes
Critical Rate-of-rise of On-state Current (Non-repetitive)	di/dt	600	A/µsec
Critical Rate-of-rise of On-state Current (Repetitive)	di/dt	150	A/µsec
I^2t (for Fusing) for One Cycle, 60Hz	I^2t	338,000	A^2sec
Peak Gate Power Dissipation	P_{GM}	16	Watts
Average Gate Power Dissipation	$P_{G(av)}$	3	Watts
Operating Temperature	T_j	-40 to +125°C	°C
Storage Temperature	T_{stg}	-40 to +150°C	°C
Approximate Weight		4	oz.
		113	g
Mounting Force		2000 to 24000	lb.
		900 to 1090	kg.

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Electrical Characteristics, T_j = 25°C Unless Otherwise Specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Repetitive Peak Reverse Leakage Current	I_{RRM}	T_j = 125°C, $V_R = V_{RRM}$			30	mA
Repetitive Peak Forward Leakage Current	I_{DRM}	T_j = 125°C, $V_D = V_{DRM}$			30	mA
Peak On-state Voltage	V_{TM}	I_{TM} = 625A Peak Duty Cycle < 0.1%			1.5	Volts
Threshold Voltage, Low-level	$V_{(TO)1}$	T_j = 125°C, I = 15%, $I_{T(av)}$ to $\pi I_{T(av)}$			1.0336	Volts
Slope Resistance, Low-level	r_{T1}				0.62862	mΩ
Threshold Voltage, High-level	$V_{(TO)2}$	T_j = 125°C, I = $\pi I_{T(av)}$ to I_{TSM}			1.68191	Volts
Slope Resistance, High-level	r_{T2}				0.36847	mΩ
V_{TM} Coefficients, Low-level		T_j = 125°C, I = 15% $I_{T(av)}$ to $\pi I_{T(av)}$			A_1 = 1.41917 B_1 = -0.1663 C_1 = 1.243E-04 D_1 = 0.04196	
V_{TM} Coefficients, High-level		T_j = 125°C, I = $\pi I_{T(av)}$ to I_{TSM}			A_2 = 13.50422 B_2 = -1.82507 C_2 = 1.8133E-04 D_2 = 0.06793	
Typical Turn-on Time	t_{on}	I_T = 100A, V_D = 100V		7		µsec
Typical Turn-off Time	t_q	T_j = 125°C, I_T = 250A, di_R/dt = 25A/µsec Reapplied dv/dt = 20V/µsec Linear to 80% V_{DRM}		150		µsec
Minimum Critical dv/dt - Exponential to V_{DRM}	dv/dt	T_j = 125°C	300			V/µsec
Gate Trigger Current	I_{GT}	T_j = 25°C, V_D = 12V			150	mA
Gate Trigger Voltage	V_{GT}	T_j = 25°C, V_D = 12V			3.0	Volts
Non-Triggering Gate Voltage	V_{GDM}	T_j = 125°C, $V_D = V_{DRM}$			0.15	Volts
Peak Forward Gate Current	I_{GTM}				4	A
Peak Reverse Gate Voltage	V_{GRM}				5	Volts

Thermal Characteristics

Maximum Thermal Resistance, Double Sided Cooling						
Junction-to-Case	$R_{\theta(j-c)}$				0.04	°C/W
Case-to-Sink	$R_{\theta(c-s)}$				0.02	°C/W

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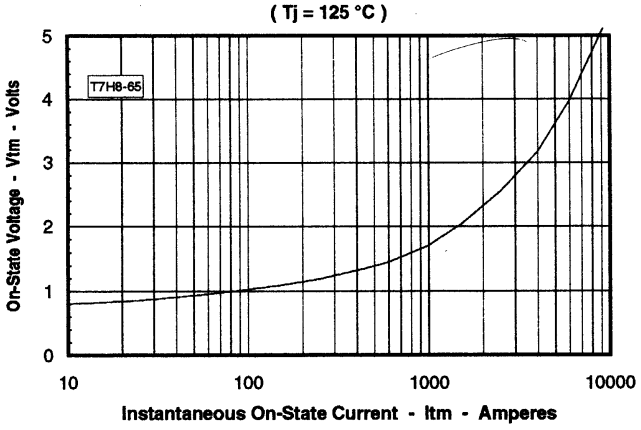
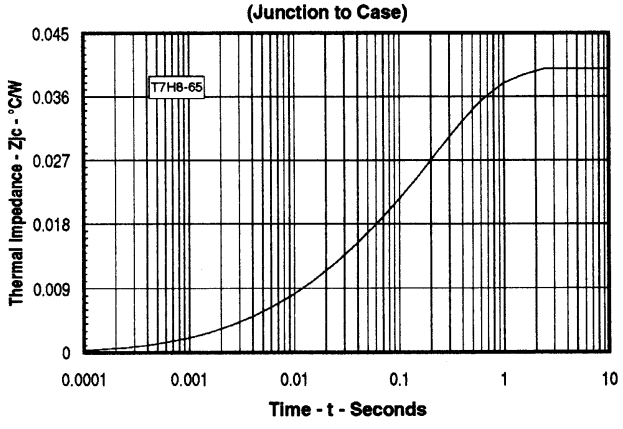
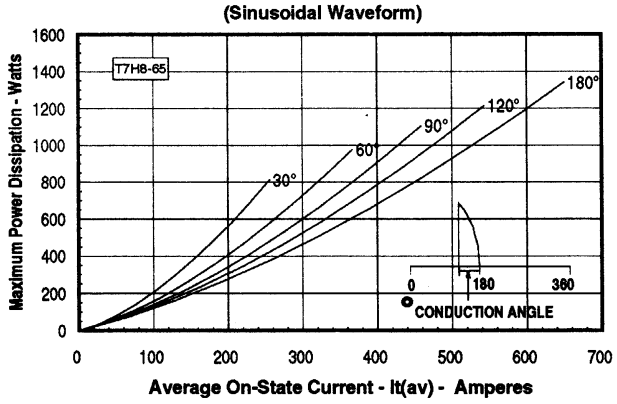
Maximum On-State Forward Voltage Drop
(Tj = 125 °C)



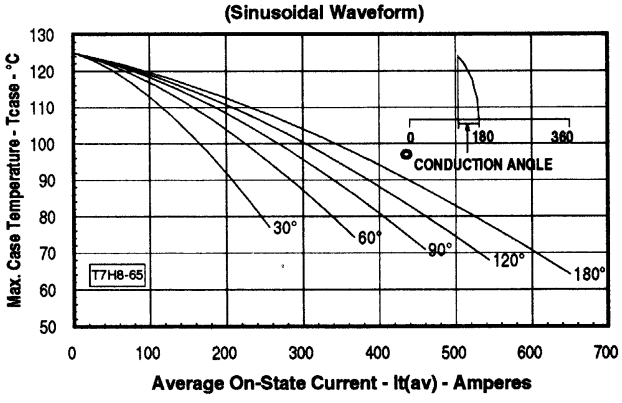
Maximum Transient Thermal Impedance
(Junction to Case)



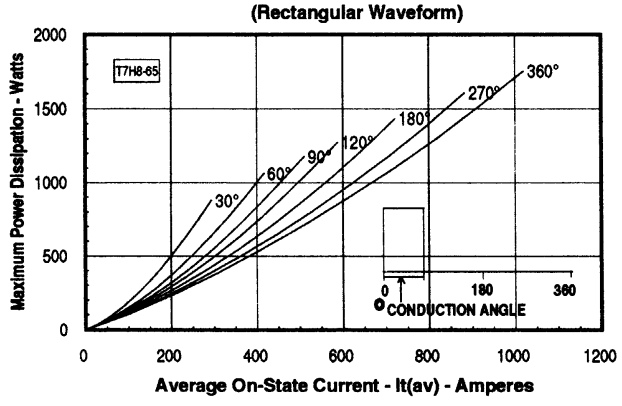
Maximum On-State Power Dissipation
(Sinusoidal Waveform)



Maximum Allowable Case Temperature
(Sinusoidal Waveform)



Maximum On-State Power Dissipation
(Rectangular Waveform)



Maximum Allowable Case Temperature
(Rectangular Waveform)