

General Description

The AAT9460 is a low threshold MOSFET designed for applications in DC-DC Converter, battery, cell phone, and PDA markets. Using AnalogicTech™'s ultra-high density proprietary TrenchDMOS™ technology, this product demonstrates high power handling and small size.

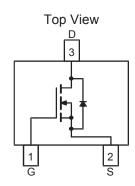
Applications

- **DC-DC Converters**
- **Battery Packs**
- Cellular & Cordless Telephones
- Battery-powered portable equipment

Features

- $V_{DS(MAX)} = 30V$ $I_{D(MAX)}^{-1} = 3.4A @ 25^{\circ}C$ Low $R_{DS(ON)}^{-1}$ 58 m Ω @ $V_{GS} = 4.5V$
- - 84 m Ω @ V_{GS} = 2.5V

SC59 Package



Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

| Symbol | Description | | Value | Units | |
|-----------------------------------|--|-----------------------|------------|-------|--|
| V _{DS} | Drain-Source Voltage | | 30 | V | |
| V _{GS} | Gate-Source Voltage | | ±12 | | |
| I _D | Continuous Drain Current @ T _J =150°C ¹ | T _A = 25°C | ±3.4 | | |
| | | T _A = 70°C | ±2.7 | Α | |
| I _{DM} | Pulsed Drain Current ² | | ±8.0 | | |
| I _S | Continuous Source Current (Source-Drain Diode) 1 | | 1.0 | | |
| P _D | Maximum Power Dissipation ¹ | $T_A = 25^{\circ}C$ | 1.1 | W | |
| | | T _A = 70°C | 0.7 | | |
| T _J , T _{STG} | Operating Junction and Storage Temperature Range | | -55 to 150 | °C | |

Thermal Characteristics

| Symbol | Description | Value | Units | |
|------------------|---|-------|-------|--|
| $R_{	heta JA}$ | Typical Junction-to-Ambient steady state ¹ 140 | | | |
| $R_{\theta JA2}$ | Maximum Junction-to-Ambient t<5 seconds ¹ 115 °C/W | | °C/W | |
| $R_{\theta JF}$ | Typical Junction-to-Foot 1 | 45 | | |



Electrical Characteristics (T_J=25°C unless otherwise noted)

| Symbol | Description | Conditions | Min | Тур | Max | Units |
|---------------------|---|--|-----|-----|------|-------|
| DC Charac | DC Characteristics | | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =-250μA | 30 | | | V |
| | Drain-Source ON-Resistance ² | V _{GS} =-4.5V, I _D =3.4A | | 46 | 58 | mΩ |
| R _{DS(ON)} | | V_{GS} =-2.5V, I_{D} =2.8A | | 65 | 84 | |
| I _{D(ON)} | On-State Drain Current ² | V _{GS} =4.5V, V _{DS} =5V (Pulsed) | 8 | | | Α |
| V _{GS(th)} | Gate Threshold Voltage | $V_{GS}=V_{DS}$, $I_{D}=-250\mu A$ | 0.6 | | | V |
| I _{GSS} | Gate-Body Leakage Current | V _{GS} =±12V, V _{DS} =0V | | | ±100 | nA |
| 1 | Drain Source Leakage Current | V_{GS} =0V, V_{DS} =30V | | | 1 | |
| I _{DSS} | | V _{GS} =0V, V _{DS} =24V, T _J =70°C ³ | | | 5 | μA |
| 9 _{fs} | Forward Transconductance ² | V_{DS} =-5V, I_D =3.4A | | 9 | | S |
| Dynamic C | Characteristics ³ | | • | | | |
| Q_G | Total Gate Charge | V_{DS} =15V, R_{D} =4.2 Ω , V_{GS} =4.5V | | 5 | | |
| Q_{GS} | Gate-Source Charge | V_{DS} =15V, R_{D} =4.2 Ω , V_{GS} =4.5V | | 0.9 | | nC |
| Q_{GD} | Gate-Drain Charge | V_{DS} =15V, R_{D} =4.2 Ω , V_{GS} =4.5V | | 1 | | |
| t _{D(ON)} | Turn-ON Delay | V_{DS} =15V, R_{D} =4.2 Ω , V_{GS} =4.5V, R_{G} =6 Ω | | 6 | | |
| t _R | Turn-ON Rise Time | V_{DS} =15V, R_{D} =4.2 Ω , V_{GS} =4.5V, R_{G} =6 Ω | | 3 | | ns |
| t _{D(OFF)} | Turn-OFF Delay | V_{DS} =15V, R_{D} =4.2 Ω , V_{GS} =4.5V, R_{G} =6 Ω | | 30 | | 113 |
| t _F | Turn-OFF Fall Time | V_{DS} =15V, R_{D} =4.2 Ω , V_{GS} =4.5V, R_{G} =6 Ω | | 8 | | |
| Source-Dr | Source-Drain Diode Characteristics | | | | | |
| V_{SD} | Source-Drain Forward Voltage ² | V _{GS} =0, I _S =3.4A | | | 1.3 | V |
| Is | Continuous Diode Current ¹ | | | | 1.0 | Α |

Note 1: Based on thermal dissipation from junction to ambient while mounted on a 1" x 1" PCB with optimized layout. A 5 second pulse on a 1" x 1" PCB approximates testing a device mounted on a large multi-layer PCB as in many applications. $R_{\theta JF} + R_{\theta FA} = R_{\theta JA}$ where the foot thermal reference is defined as the normal solder mounting surface of the device's leads. $R_{\theta JF}$ is guaranteed by design; however, $R_{\theta FA}$ is determined by PCB design. Actual maximum continuous current is limited by the application's design.

Note 2: Pulse test: Pulse width = $300 \mu s$.

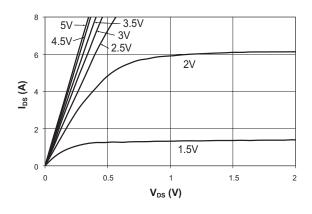
Note 3: Guaranteed by design. Not subject to production testing.



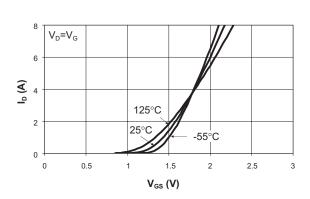
Typical Characteristics

(T_{.1} = 25°C unless otherwise noted)

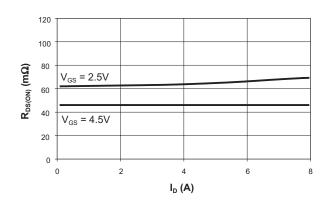
Output Characteristics



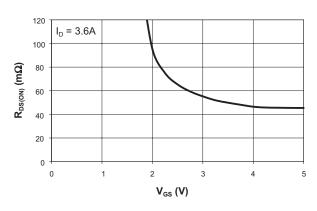
Transfer Characteristics



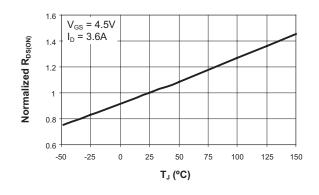
On-Resistance vs. Drain Current



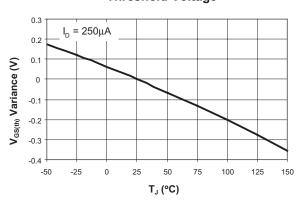
On-Resistance vs. Gate to Source Voltage



On-Resistance vs. Junction Temperature



Threshold Voltage

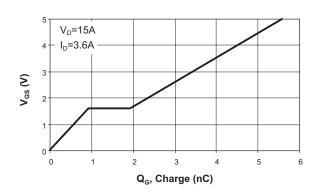




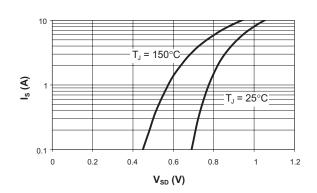
Typical Characteristics

 $T_J = 25^{\circ}$ C unless otherwise noted)

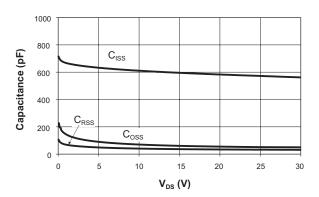
Gate Charge



Source-Drain Diode Forward Voltage



Capacitance



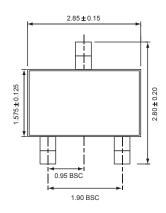


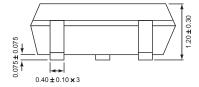
Ordering Information

| Package | Marking | Part Number (Tape and Reel) |
|---------|---------|-----------------------------|
| SC59 | HA | AAT9460IGY-T1 |

Package Information

SC59







All dimensions in millimeters.



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