# BTA225 series C

# GENERAL DESCRIPTION

Glass passivated high commutation triacs in a plastic envelope intended for use in circuits where high static and dynamic dV/dt and high dl/dt can occur loads. These devices will commutate the full rated rms current at the maximum rated junction temperature, without the aid of a snubber.

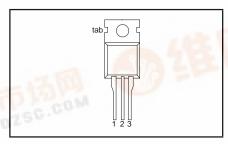
### **QUICK REFERENCE DATA**

| SYMBOL                                  | PARAMETER   | MAX.            | MAX.            | MAX.            | UNIT   |
|---|---|-----------------|-----------------|-----------------|--------|
| V <sub>DRM</sub>                        | BTA225-<br>Repetitive peak off-state<br>voltages          | <b>500C</b> 500 | <b>600C</b> 600 | <b>800C</b> 800 | V      |
| I <sub>T(RMS)</sub><br>I <sub>TSM</sub> | RMS on-state current Non-repetitive peak on-state current | 25<br>190       | 25<br>190       | 25<br>190       | A<br>A |

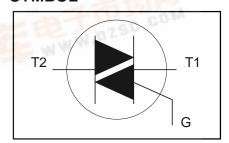
# **PINNING - TO220AB**

| PIN | DESCRIPTION     |  |
|-----|-----------------|--|
| 1   | main terminal 1 |  |
| 2   | main terminal 2 |  |
| 3   | gate            |  |
| tab | main terminal 2 |  |

# PIN CONFIGURATION



# **SYMBOL**



# LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

| SYMBOL   | PARAMETER   | CONDITIONS   | MIN.        | -                | MAX.                             | EC.CO | UNIT                  |
|--|---|--|-------------|------------------|----------------------------------|-------|-----------------------|
|  |   |  |             | -500             | -600                             | -800  |                       |
| $V_{DRM}$  | Repetitive peak off-state voltages  | الم الماحد   | 9-1         | 600 <sup>1</sup> | 600¹                             | 800   | V                     |
| I <sub>T(RMS)</sub>  | RMS on-state current  | full sine wave;  | - [         |                  | 25                               |       | Α                     |
| I <sub>TSM</sub> I <sup>2</sup> t dI <sub>T</sub> /dt  | Non-repetitive peak on-state current  I²t for fusing Repetitive rate of rise of   | $T_{mb} \le 91$ °C full sine wave;<br>$T_j = 25$ °C prior to surge $t = 20$ ms $t = 16.7$ ms $t = 10$ ms | -<br>-<br>- |                  | 190<br>209<br>180<br>100         |       | Α<br>Α<br>Α²s<br>Α/μs |
| I <sub>GM</sub> V <sub>GM</sub> P <sub>GM</sub> P <sub>G(AV)</sub> T <sub>stg</sub> T <sub>j</sub> | on-state current after triggering Peak gate current Peak gate voltage Peak gate power Average gate power Storage temperature Operating junction temperature | $dl_G^2/dt = 0.2 \text{ Å/}\mu\text{s}$<br>over any 20 ms<br>period  | -40<br>-    |                  | 2<br>5<br>5<br>0.5<br>150<br>125 |       | A<br>W<br>W<br>°C°C   |

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# THERMAL RESISTANCES

| SYMBOL                     | PARAMETER   | CONDITIONS                              | MIN.  | TYP.         | MAX.            | UNIT              |
|----------------------------|---|---|-------|--------------|-----------------|-------------------|
| $R_{th j-mb}$ $R_{th j-a}$ | Thermal resistance junction to mounting base Thermal resistance junction to ambient | full cycle<br>half cycle<br>in free air | 1 1 1 | -<br>-<br>60 | 1.0<br>1.4<br>- | K/W<br>K/W<br>K/W |

# STATIC CHARACTERISTICS

T<sub>i</sub> = 25 °C unless otherwise stated

| SYMBOL          | PARAMETER                         | CONDITIONS   | MIN.  | TYP. | MAX. | UNIT |
|-----------------|-----------------------------------|--|-------|------|------|------|
| I <sub>GT</sub> | Gate trigger current <sup>2</sup> | $V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$                              |       |      |      |      |
|                 |                                   | T2+ G-   |       | 18   | 35   | mA   |
|                 |                                   | T2+ G-   | 2 2   | 21   | 35   | mA   |
|                 |                                   | T2- G-   | 2     | 34   | 35   | mA   |
| I <sub>L</sub>  | Latching current                  | $V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$                           |       |      |      |      |
|                 | _                                 | T2+ G-   | ·   - | -    | 20   | mA   |
|                 |                                   | T2+ G-   | -     | -    | 30   | mA   |
|                 |                                   | T2- G-   | -     | -    | 20   | mA   |
| I <sub>H</sub>  | Holding current                   | $V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$                           | -     | -    | 15   | mΑ   |
| V <sub>T</sub>  | On-state voltage                  | $I_{T} = 30 \text{ A}$   | -     | 1.3  | 1.55 | V    |
| V <sub>GT</sub> | Gate trigger voltage              | $\dot{V}_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}$                    | -     | 0.7  | 1.5  | V    |
|                 |                                   | $V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_i = 125 ^{\circ}\text{C}$ | 0.25  | 0.4  | -    | V    |
| I <sub>D</sub>  | Off-state leakage current         | $V_D = V_{DRM(max)}$ ; $T_j = 125 °C$                                  | -     | 0.1  | 0.5  | mA   |

# **DYNAMIC CHARACTERISTICS**

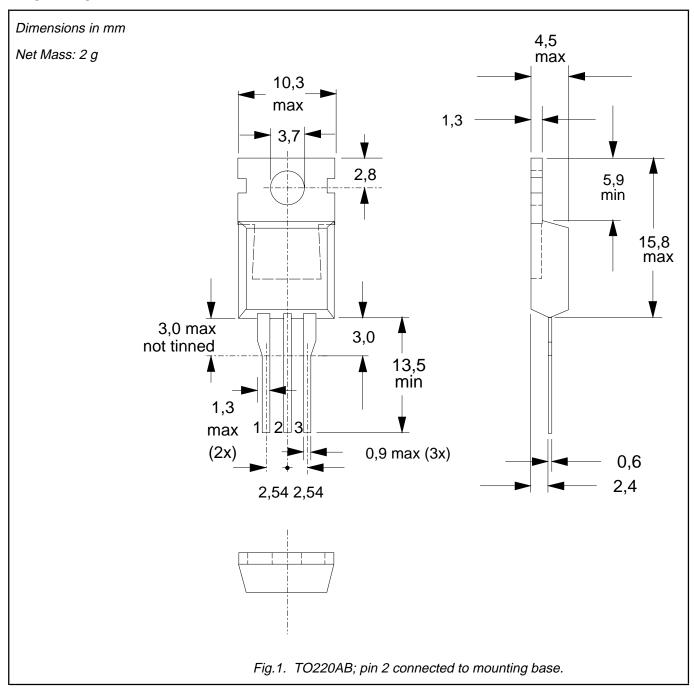
T<sub>i</sub> = 25 °C unless otherwise stated

| SYMBOL                | PARAMETER  | CONDITIONS   | MIN. | TYP. | MAX. | UNIT |
|-----------------------|--|--|------|------|------|------|
| dV <sub>D</sub> /dt   | Critical rate of rise of   | $V_{DM} = 67\% V_{DRM(max)}; T_j = 125 °C;$  | 1000 | -    | -    | V/μs |
| dl <sub>com</sub> /dt | off-state voltage Critical rate of change of commutating current | exponential waveform; gate open circuit $V_{DM} = 400 \text{ V}$ ; $T_j = 125 \text{ °C}$ ; $I_{T(RMS)} = 25 \text{ A}$ ; without snubber; gate open circuit | -    | 14   | -    | A/ms |
| t <sub>gt</sub>       | Gate controlled turn-on time                                     | $I_{TM} = 30 \text{ A}; V_D = V_{DRM(max)}; I_G = 0.1 \text{ A};$<br>$dI_G/dt = 5 \text{ A}/\mu\text{s}$   | -    | 2    | -    | μs   |

<sup>2</sup> Device does not trigger in the T2-, G+ quadrant.

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# **MECHANICAL DATA**



- Notes
  1. Refer to mounting instructions for TO220 envelopes.
  2. Epoxy meets UL94 V0 at 1/8".

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#### DEFINITIONS

| Data sheet status         |   |  |  |  |  |
|---------------------------|---|--|--|--|--|
| Objective specification   | This data sheet contains target or goal specifications for product development.       |  |  |  |  |
| Preliminary specification | This data sheet contains preliminary data; supplementary data may be published later. |  |  |  |  |
| Product specification     | This data sheet contains final product specifications.                                |  |  |  |  |
| 1                         |   |  |  |  |  |

# **Limiting values**

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

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