

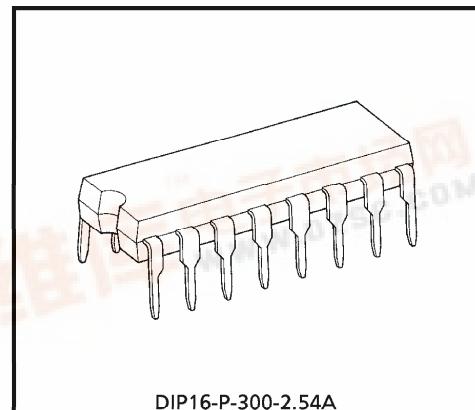
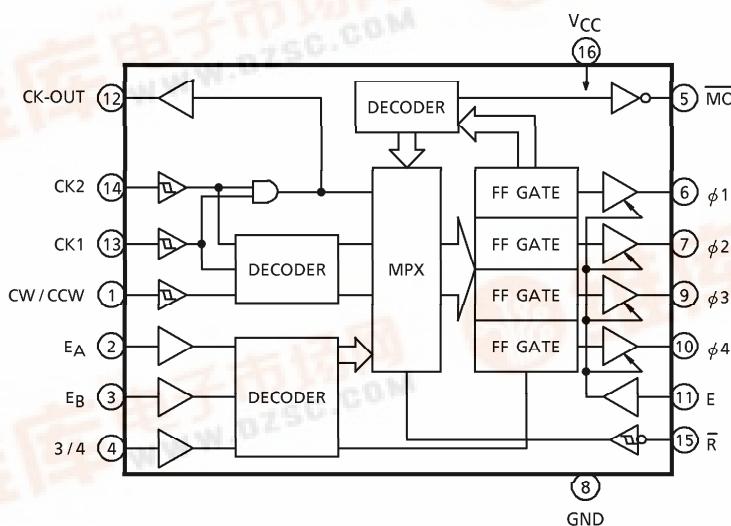
TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8415P**STEPPING MOTOR CONTROLLER / DRIVER**

The TA8415P is general purpose unipolar stepping motor controller/driver, applicable to 3/4 phase motors and 1, 1-2, 2 phase excitation drive by initial setting of control terminals.

FEATURES

- 1 chip stepping motor controller/driver.
- 3 or 4 phase and 1, 1-2, 2 phase excitation drive are available.
- CW / CCW rotation and 1 clock or 2 clock drive are available.
- Hysteresis is provided with clock, CW/CCW, reset inputs for noise protection.
- Output enable, initial detect are available.
- Output current up to 400mA (MAX.)

BLOCK DIAGRAM

DIP16-P-300-2.54A

Weight : 1.11g (Typ.)

PIN FUNCTION

PIN No.	SYMBOL	PIN NAME	FUNCTIONAL DESCRIPTION		
1	CW / CCW	Clock Wise /Counter Clock Wise	Direction Control Input Function Table A		
2	E _A	Excitation A	Phase Excitation Mode Input	Truth Table B	
3	E _B	Excitation B			
4	3 / 4	3 Phases / 4 Phases	Phase Control Input		
5	MO	Monitor Out	MO = "L" at Initial State		
6	ϕ1	ϕ1 Out	ϕ1 Output		
7	ϕ2	ϕ2 Out	ϕ2 Output		
8	GND	GND	GND		
9	ϕ3	ϕ3 Out	ϕ3 Output		
10	ϕ4	ϕ4 Out	ϕ4 Output		
11	E	Output Enable	Outputs are Enable at E = "H"		
12	CK-OUT	Clock-Out	Clock Output		
13	CK1	Clock I _{n-1}	Clock Input 1	Truth Table A	
14	CK2	Clock I _{n-2}	Clock Input 2		
15	̄R	Reset	Reset Input		
16	V _{CC}	V _{CC}	V _{CC}		

TRUTH TABLE A

CK1	CK2	CW / CCW	FUNCTION
	H	L	CW
	L	L	Inhibit
H		L	CCW
L		L	Inhibit
	H	H	CCW
	L	H	Inhibit
H		H	CW
L		H	Inhibit

TRUTH TABLE B

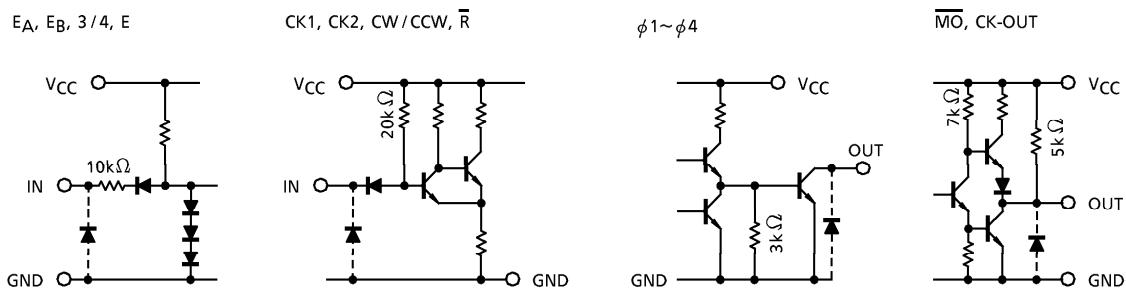
E _A	E _B	3 / 4 (Note)	FUNCTION	
L	L	L	4 Phases	1 Phase Excitation
H	L	L		2 Phase Excitation
L	H	L		1-2 Phase Excitation
H	H	L		Test Mode ϕ1~ϕ4 ON
L	L	H	3 Phases	1 Phase Excitation
H	L	H		2 Phase Excitation
L	H	H		1-2 Phase Excitation
H	H	H	Test Mode ϕ1~ϕ4 ON	

(Note) Conversion of Phase Excitation Mode must be made after the Reset Mode is established.

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SCHEMATIC OF INPUTS AND OUTPUTS



MAXIMUM RATINGS (Ta = 25°C Unless otherwise noted)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _{CC}	-0.3~7.0	V
Output Sustaining Voltage	V _{CE(SUS)} φ	-0.3~28	V
Output Current (φn)	I _{OUT} φ	400	mA
Output Current (MO, CK-OUT)	I _{OUT} MO CK-OUT	10	mA
Input Voltage	V _{IN}	-0.3~V _{CC} + 0.3	V
Input Current	I _{IN}	± 1	mA
Power Dissipation	P _D	1.2	W
Operating Temperature	T _{opr}	-30~85	°C
Storage Temperature	T _{stg}	-55~150	°C

RECOMMENDED OPERATING CONDITIONS (Ta = -30~85°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V _{CC}	—	4.5	5.0	5.5	V
Output Sustaining Voltage	V _{CE(SUS)} φ	—	0	—	26	V
Output Current φn "L" Level	I _{OUT} φ	—	—	—	200	mA
Output Current "H" Level	I _{OH}	—	—	—	-0.4	mA
MO, CK-OUT "L" Level	I _{OL}	—	—	—	8	
Input Voltage	V _{IN}	—	0	—	V _{CC}	V
Clock Frequency	f _{CLOCK}	—	0	—	100	kHz
Power Dissipation	P _D	—	—	—	0.6	W

ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

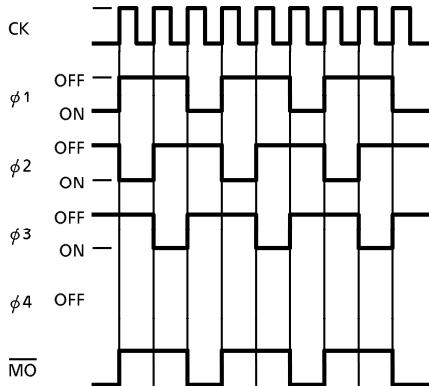
CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Voltage	"H" Level	V_{IH}	—	—	2.0	—	—	V
	"L" Level	V_{IL}	—	—	—	—	0.8	
Input Current	"H" Level	I_{IH}	—	$V_{CC} = 5.5\text{V}, V_{IH} = 5.5\text{V}$	—	—	10	μA
	"L" Level	I_{IL}	—	$V_{CC} = 5.5\text{V}, V_{IL} = 0.4\text{V}$	—	—	-0.4	mA
Hysteresis		ΔV_T	—	—	—	150	—	mV
Supply Current		I_{CC}	—	—	—	—	100	mA
Output Leakage Current ϕn		$I_{OH}\phi$	—	$V_{CC} = 5.5\text{V}, V_{OUT} = 26\text{V}$	—	—	100	μA
Output Voltage	"H" Level	$M\bar{O}$ CK-OUT	V_{OH}	$V_{CC} = 4.5\text{V}, I_{OH} = -0.4\text{mA}$ $V_{CC} = 5.0\text{V}, I_{OH} = -10\mu\text{A}$	2.4	—	—	V
					4.0	—	—	
	"L" Level	$M\bar{O}$ CK-OUT	V_{OL}	$V_{CC} = 4.5\text{V}, I_{OL} = 8\text{mA}$ $V_{CC} = 4.5\text{V}, I_{OUT} = 400\text{mA}$ $t = 100\text{ms}$	—	—	0.4	
					—	—	1.1	
					—	—	0.6	

SWITCHING CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

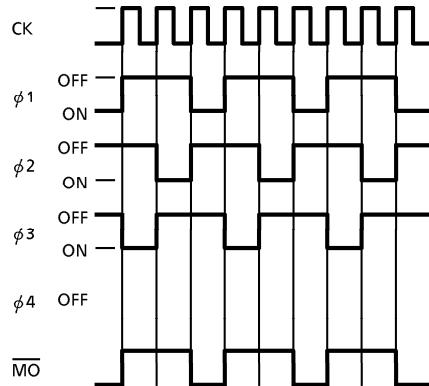
CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Propagation Delay Time	"H" Level	CK- ϕn	t_{pLH}	—	—	2.0	—	μs	
		CK-CK-OUT			—	1.0	—		
		$\bar{C}K-M\bar{O}$			—	2.8	—		
		E- ϕn			—	1.0	—		
		R- ϕn			—	2.0	—		
	"L" Level	CK- ϕn	t_{pHL}	—	—	1.4	—		
		CK-CK-OUT			—	0.7	—		
		$\bar{C}K-M\bar{O}$			—	2.1	—		
		E- ϕn			—	1.2	—		
		$\bar{R}-\phi n$			—	1.0	—		
Maximum Clock Frequency		f_{max}	—	—	—	250	—	kHz	
Set Up Time CK, CW / CCW		t_{set-up}	—	—	—	0.1	—	μs	
Hold Time CK, CW / CCW		t_{hold}	—	—	—	0.1	—		
Minimum Clock Pulse Width		$t_w(\text{CK})$	—	—	—	1.0	—		
Minimum Reset Pulse Width		$t_w(R)$	—	—	—	1.0	—		
Maximum Clock Rise Time		$t_r(\text{CK})$	—	—	—	10	—	μs	

TIMING CHART
3 PHASES METHOD

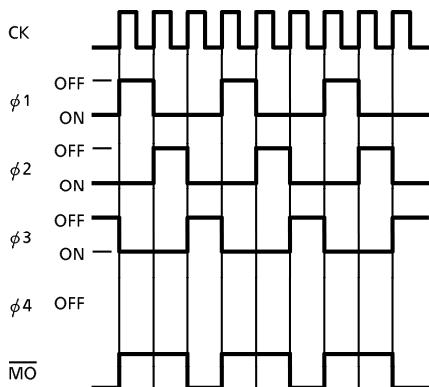
1 PHASE EXCITATION CW



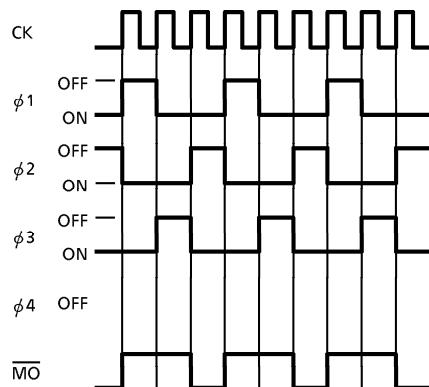
1 PHASE EXCITATION CCW



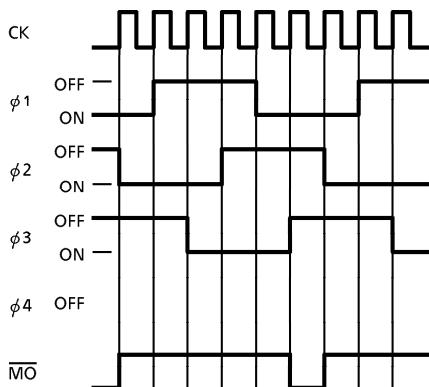
2 PHASE EXCITATION CW



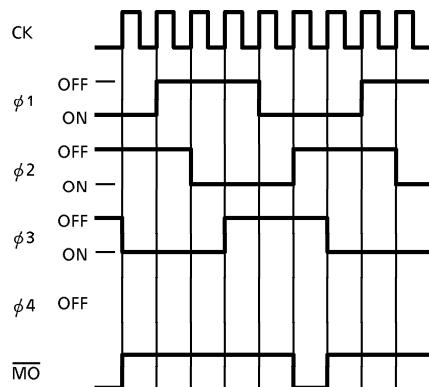
2 PHASE EXCITATION CCW



1-2 PHASE EXCITATION CW

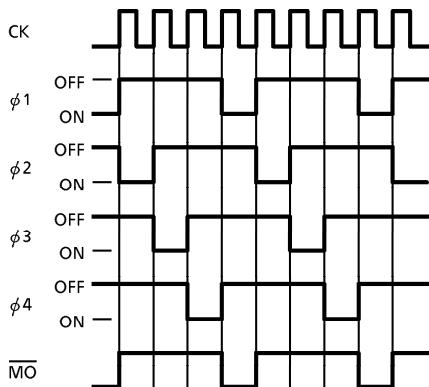


1-2 PHASE EXCITATION CCW

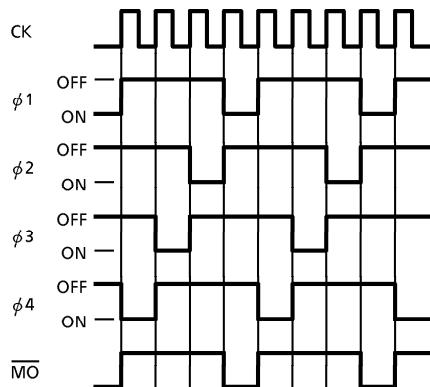


4 PHASES METHOD

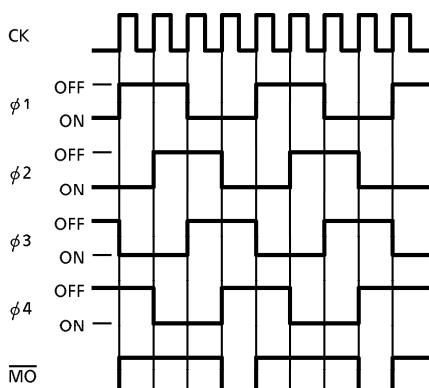
1 PHASE EXCITATION CW



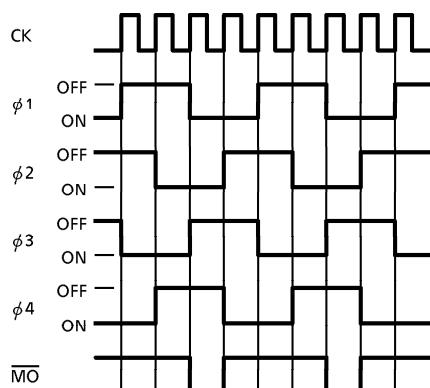
1 PHASE EXCITATION CCW



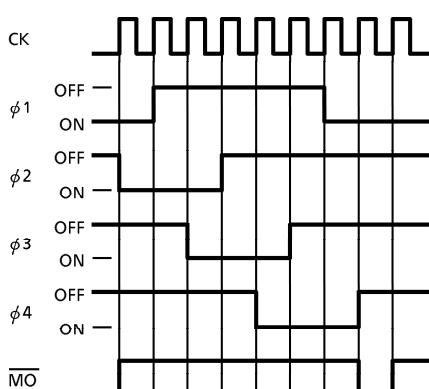
2 PHASE EXCITATION CW



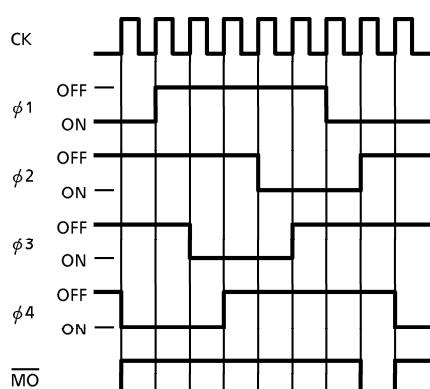
2 PHASE EXCITATION CCW

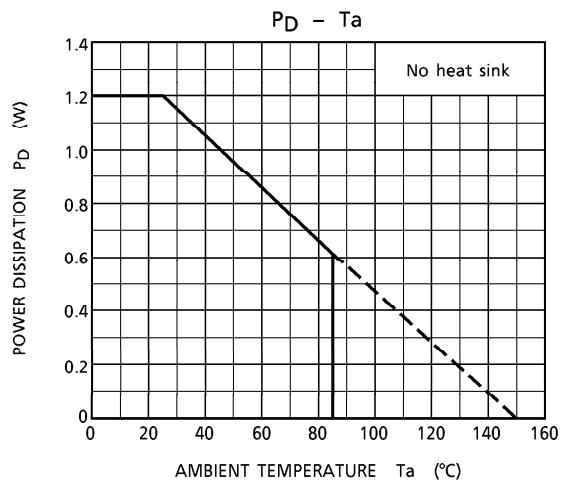
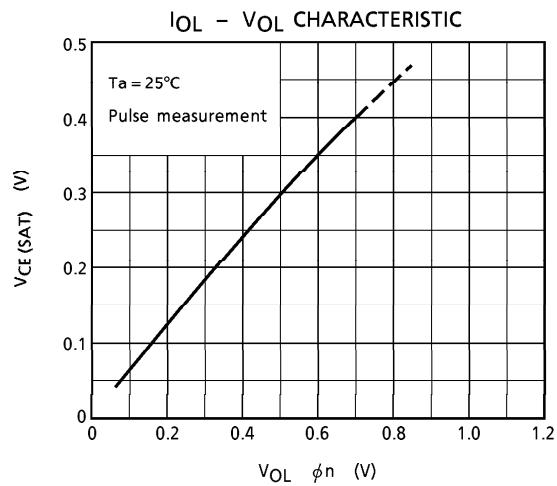


1-2 PHASE EXCITATION CW



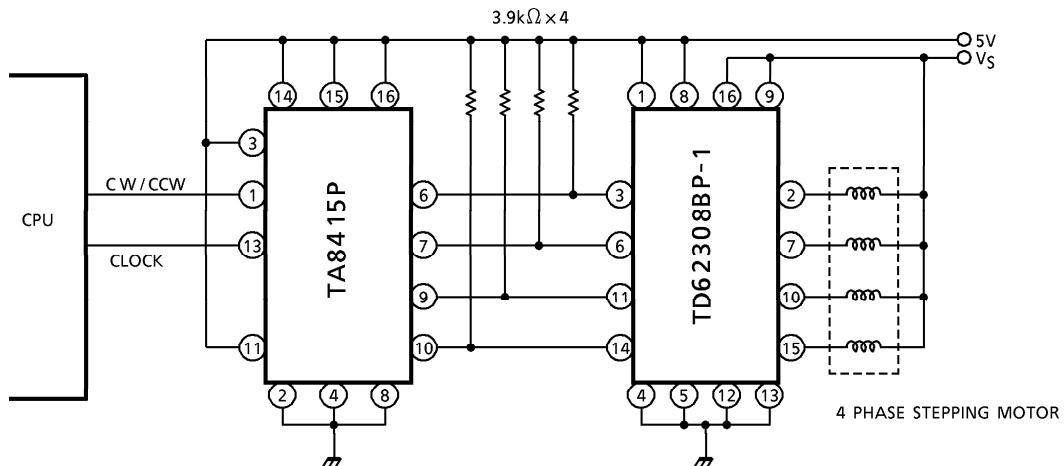
1-2 PHASE EXCITATION CCW



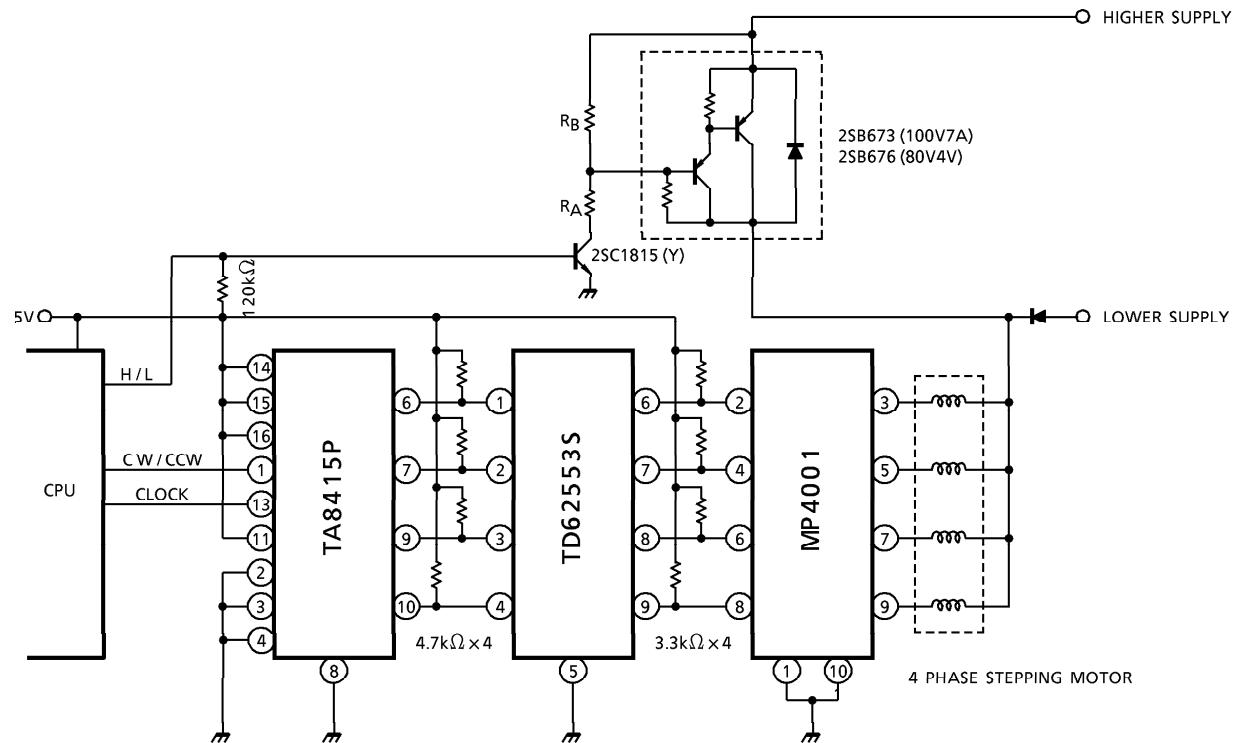


APPLICATION CIRCUIT 1

(TA8415P + TD62308BP 4 phase stepping motor driver circuit)

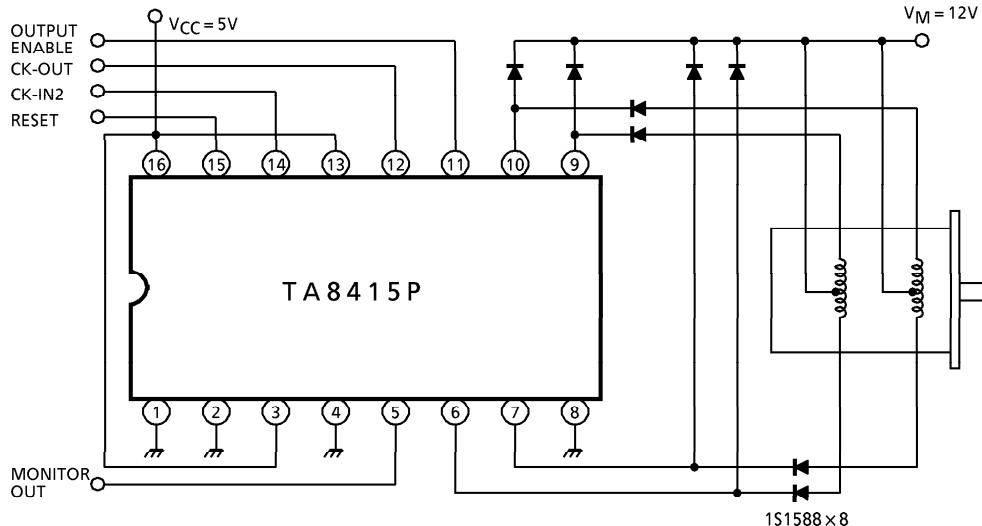
**APPLICATION CIRCUIT 2**

(TA8415P + TD62553S + MP4001 high efficiency stepping motor driver circuit)

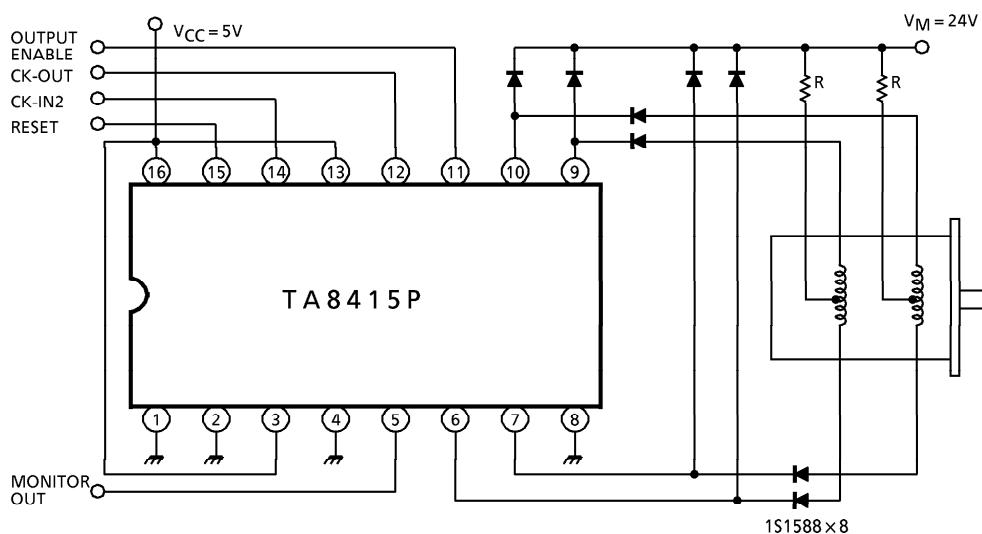


APPLICATION CIRCUIT 3

4 phase motor 1-2 phase excitation drive I .

**APPLICATION CIRCUIT 4**

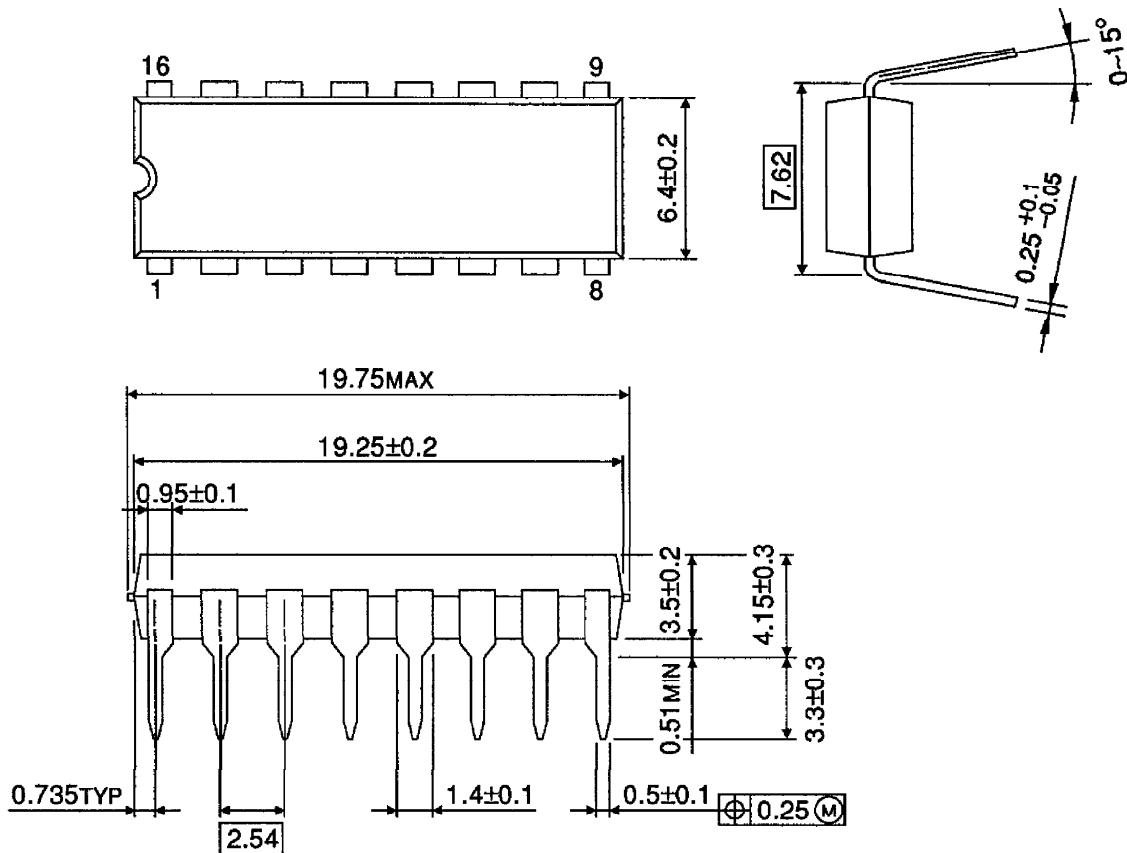
4 phase motor 1-2 phase excitation drive II .



(Note) Utmost care is necessary in the design of the output line, power supply and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

OUTLINE DRAWING
DIP16-P-300-2.54A

Unit : mm



Weight : 1.11g (Typ.)