## TOSHIBA CMOS Integrated Circuit Silicon Monolithic

## TB6548F

## 3-Phase Full-Wave PWM Sensorless Controller for Brushless DC Motors

TB6548F is a 3 -phase full-wave sensorless controller for brushless DC motors. It is capable of controlling voltage by PWM signal input. It is capable of PWM type sensorless driving when used conjunction with TA84005F

## Features

- 3-phase full-wave sensorless drive
- PWM control (PWM signal is supplied from external sources.)
- Turn-on signal output current: 20 mA


Weight: 0.32 g (typ.)

- Built-in protection against overcurrent
- Forward/reverse modes
- Built-in lead angle control function (0, 7.5, 15 and 30 degrees)
- Built-in lap turn-on function


## Block Diagram



## Pin Assignment



## Pin Description

| Pin No. | Symbol | I/O | Description |
| :---: | :---: | :---: | :---: |
| 1 | LA0 | 1 | Lead angle setting signal input pin <br> - LA0 = Low, LA1 = Low: Lead angle 0 degree <br> - LA0 = High, LA1 = Low: Lead angle 7.5 degree |
| 2 | LA1 | 1 | - LA0 = Low, LA1 = High: Lead angle 15 degree <br> - LA0 $=$ High, LA1 $=$ High: Lead angle 30 degree <br> - Built-in pull-down resistor |
| 3 | PWM | 1 | PWM signal input pin <br> - Inputs Low-active PWM signal <br> - Built-in pull-up resistor <br> - Disables input of duty-100\% (Low) signal High for 250 ns or longer is required. |
| 4 | CW_CCW | I | Rotation direction signal input pin <br> - High: Reverse $(\mathrm{U} \rightarrow \mathrm{W} \rightarrow \mathrm{V})$ <br> - Low, Open: Forward $(\mathrm{U} \rightarrow \mathrm{V} \rightarrow \mathrm{W})$ <br> - Built-in pull-down resistor |
| 5 | NC | - | Not connected |
| 6 | FG_OUT | 0 | Number of ratation detection signal output pin <br> - Equiralent to U-phase signal (except PWM) |
| 7 | NC | - | Not connected |
| 8 | SEL_LAP | 1 | Lap turn-on select pin <br> - Low: Lap turn-on <br> - High: 120 degrees turn-on <br> - Built-in pull-up resistor |
| 9 | NC | - | Not connected |
| 10 | $\mathrm{X}_{\mathrm{T}}$ | - | Resonator connecting pin |
| 11 | $\mathrm{X}_{\text {Tin }}$ | - | - Selects starting commutation frequency. <br> Starting commutation frequency $\mathrm{f}_{\text {st }}=$ Resonator frequency $\mathrm{f}_{\mathrm{xt}} /\left(6 \times 2^{17}\right)$ |
| 12 | GND | - | Connected to GND. |
| 13 | $V_{\text {DD }}$ | - | Connected to 5-V power supply. |
| 14 | OUT_UP | 0 | U-phase upper turn-on signal output pin <br> - U-phase winding wire positive ON/OFF switching pin <br> - ON: Low, OFF: High |
| 15 | OUT_UN | O | U-phase lower turn-on signal output pin <br> - U-phase winding wire negative ON/OFF switching pin <br> - ON: High, OFF: Low |
| 16 | NC | - | Not connected |
| 17 | OUT_VP | O | V-phase upper turn-on signal output pin <br> - V-phase winding wire positive ON/OFF switching pin <br> - ON: Low, OFF: High |
| 18 | NC | - | Not connected |
| 19 | OUT_VN | O | V-phase lower turn-on signal output pin <br> - V-phase winding wire negative ON/OFF switching pin <br> - ON: High, OFF: Low |
| 20 | NC | - | Not connected |


| Pin No. | Symbol | I/O | Description |
| :---: | :---: | :---: | :---: |
| 21 | OUT_WP | O | W-phase upper turn-on signal output pin <br> - W-phase winding wire positive ON/OFF switching pin <br> - ON: Low, OFF: High |
| 22 | OUT_WN | O | W-phase lower turn-on signal output pin <br> - W-phase winding wire negative ON/OFF switching pin <br> - ON: High, OFF: Low |
| 23 | OC | 1 | Overcurrent signal input pin <br> - High on this pin can put constraints on the turn-on signal which is performing PWM control. <br> - Built-in pull-up resistor |
| 24 | WAVE | 1 | Positional signal input pin <br> - Inputs majority logic synthesis signal of three-phase pin voltage. <br> - Built-in pull-up resistor |

## Functional Description

## 1. Sensorless Drive

On receipt of PWM signal start instruction turn-in signal for forcible commutation (commutation irrespective of the motor's rotor position) is output and the motor starts to rotate. The motor's rotation causes induced voltage on winding wire pin for each phase.

When signals indicating positive or negative for pin voltage (including induced voltage) for each phase are input on respective positional signal input pin, the turn-on signal for forcible commutation is automatically switched to turn-on signal for positional signal (induced voltage).
Thereafter turn-on signal is formed according to the induced voltage contained in the pin voltage so as to drive the brushless DC motor.

## 2. Starting commutation frequency (resonator pin and counter bit select pin)

The forcible commutation frequency at the time of start is determined by the resonator's frequency and the number of counter bit (within the IC).

Starting commutation frequency $f_{s t}=$ Resonator frequency $f_{\mathrm{Xt}} /\left(6 \times 2^{(b i t}{ }^{+} 3\right) \quad$ bit $=14$
The forcible commutation frequency at the time of start can be adjusted using inertia of the motor and load.

- The forcible commutation frequency should be set higher as the number of magnetic poles increases.
- The forcible commutation frequency should be set lower as the inertia of the load increases.


## 3. PWM Control

PWM signal can be reflected in turn-on signal by supplying PWM signal from external sources.
The frequency of the PWM signal shoud be set adequately high with regard to the electrical frequency of the motor and in accordance to the switching characteristics of the drive circuit.

Because positional detection is performed in synchronization with the falling edges of PWM signal, positional detection cannot be performed with $0 \%$ duty or $100 \%$ duty.


The voltage applied to the motor is duty $100 \%$ because of the storage time of the drive circuit even if the duty is $99 \%$.

## 4. PWM Control

Upper turn-on
signal (OUT-P)

Lower turn-on signal (OUT-N)


## 5. Positional Variation

Since positional detection is performed in synchronization with PWM signal, positional variation occurs in connection with the frequency of PWM signal. Be especially careful when the IC is used for high-speed motors.


Positional signal


Variation is calculated by detecting at two consecutive rising edges of PWM signal.
$1 / \mathrm{f}_{\mathrm{p}}<$ Detection time variation $<2 / \mathrm{f}_{\mathrm{p}} \quad \mathrm{f}_{\mathrm{p}}$ : PWM frequency

## 6. Lead Angle Control

The lead angle is 0 degree during the starting forcible commutation and when normal commutation is started, automatically changes to the lead angle which has been set using LA0 and LA1. However, if both LA0 and LA1 are set for High, the lead angle is 30 degrees in the starting forcible commutation as well as in normal commutation.


## 7. Lap Turn-on Control

When SEL_LAP = High, the turn-on degree is 120 degrees. When SEL_LAP = Low, Lap Turn-on Mode starts.

In Lap Turn-on Mode, the time between zero-cross point and the 120 degrees turn-on timing becomes longer (shaded area in the below chart) so as to create some overlap when switching turn on signals. The lap time differs depending ong the lead angle setting.


## 8. Start/Stop Control

Start/Stop is controlled using PWM signal input pin.
A stop is acknowledged when PWM signal duty is 0 , and a start is acknowledged when ON-signal of a frequency 4 times higher than the resonator frequency or even higher is input continuously.

## Timing chart



[^0]Maximum Ratings ( $\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ )

| Characteristics | Symbol | Rating | Unit |
| :--- | :---: | :---: | :---: |
| Power supply voltage | $\mathrm{V}_{\text {DD }}$ | 5.5 | V |
| Input voltage | $\mathrm{V}_{\text {in }}$ | -0.3 to $\mathrm{V}_{\mathrm{DD}}+0.3$ | V |
| Turn-on signal output current | I OUT | 20 | mA |
| Power dissipation | $\mathrm{PD}_{\mathrm{D}}$ | 590 | mW |
| Operating temperature | $\mathrm{T}_{\text {opr }}$ | -30 to 85 | ${ }^{\circ}{ }^{\circ} \mathrm{C}$ |
| Storage temperature | $\mathrm{T}_{\text {stg }}$ | -55 to 150 | ${ }^{\circ} \mathrm{C}$ |

## Recommended Operating Conditions ( $\mathrm{Ta}=-30$ to $85^{\circ} \mathrm{C}$ )

| Characteristics | Symbol | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Power supply voltage | VDD | - | 4.5 | 5.0 | 5.5 | V |
| Input voltage | $\mathrm{V}_{\text {in }}$ | - | -0.3 | - | $\begin{aligned} & V_{D D} \\ & +0.3 \end{aligned}$ | V |
| PWM frequency | $\mathrm{f}_{\text {PWM }}$ | - | - | 16 | - | kHz |
| Oscillation frequency | $\mathrm{f}_{\text {Osc }}$ | - | 1.0 | - | 10 | MHz |

Electrical Characteristics ( $\mathrm{Ta}=\mathbf{2 5 ^ { \circ }} \mathbf{C}, \mathrm{V}_{\mathrm{DD}}=5 \mathrm{~V}$ )

| Characteristics | Symbol | Test Circuit | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Static power supply current | IDD | - | $\mathrm{PWM}=\mathrm{H}, \mathrm{X}_{\text {Tin }}=\mathrm{H}$ | - | 0.1 | 0.3 | mA |
| Dynamic power supply current | IDD (opr) | - | $\mathrm{PWM}=50 \%$ Duty, $\mathrm{X}_{\text {Tin }}=4 \mathrm{MHz}$ | - | 1 | 3 | mA |
| Input current | $\mathrm{I}_{\mathrm{N}-1}(\mathrm{H})$ | - | VIN $=5 \mathrm{~V}, \mathrm{PWM}, \mathrm{OC}$, WAVE_U, SEL_LAP | - | 0 | 1 | $\mu \mathrm{A}$ |
|  | $\mathrm{I}_{\mathrm{IN}-1}(\mathrm{~L})$ | - | VIN $=0 \mathrm{~V}$, PWM, OC, WAVE_U, SEL_LAP | -75 | -50 | - |  |
|  | IIN-2 (H) | - | $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{CW}$ | - | 50 | 75 |  |
|  | $\mathrm{I}_{\mathrm{IN}-2}(\mathrm{~L})$ | - | $\mathrm{V}_{\text {IN }}=0 \mathrm{~V}, \mathrm{CW}$ | -1 | 0 | - |  |
| Input voltage | $\mathrm{V}_{\text {IN }}(\mathrm{H})$ | - | PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1 | 3.5 | - | 5 | V |
|  | $V_{\text {IN }}(\mathrm{L})$ | - | PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1 | GND | - | 1.5 |  |
| Input hysteresis voltage | $\mathrm{V}_{\mathrm{H}}$ | - | PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1 | - | 0.6 | - | V |
| Output voltage | $\mathrm{V}_{\mathrm{O}-1}(\mathrm{H})$ | - | $\mathrm{I}_{\mathrm{OH}}=-1 \mathrm{~mA}$ <br> OUT_UP, OUT_VP, OUT_WP | 4.3 | - | $V_{D D}$ | V |
|  | $\mathrm{V}_{\mathrm{O-1}}(\mathrm{~L})$ | - | $\mathrm{IOL}=20 \mathrm{~mA}$ <br> OUT_UP, OUT_VP, OUT_WP | GND | - | 0.5 |  |
|  | $\mathrm{V}_{\mathrm{O}-2}(\mathrm{H})$ | - | $\mathrm{IOH}=-20 \mathrm{~mA}$ <br> OUT_UN, OUT_VN, OUT_WN | 4.0 | - | $V_{D D}$ |  |
|  | $\mathrm{V}_{\mathrm{O}-2}(\mathrm{~L})$ | - | $\mathrm{IOL}=1 \mathrm{~mA}$ <br> OUT_UN, OUT_VN, OUT_WN | GND | - | 0.5 |  |
|  | $\mathrm{V}_{\mathrm{O}-3}(\mathrm{H})$ | - | $\begin{aligned} & \mathrm{I}_{\mathrm{OH}}=-0.5 \mathrm{~mA} \\ & \text { FG_OUT } \end{aligned}$ | 4.0 | - | $V_{D D}$ |  |
|  | $\mathrm{V}_{\mathrm{O}-3}(\mathrm{~L})$ | - | $\begin{aligned} & \mathrm{IOL}=0.5 \mathrm{~mA} \\ & \mathrm{FG} \text { _OUT } \end{aligned}$ | GND | - | 0.5 |  |
| Output leak current | L ( H$)$ | - | $\mathrm{V}_{\mathrm{DD}}=5.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{OUT}}=0 \mathrm{~V}$ <br> OUT_UP, OUT_VP, OUT_WP OUT_UN, OUT_VN, OUT_WN FG_OUT | - | 0 | 10 | $\mu \mathrm{A}$ |
|  | l L (L) | - | $\mathrm{V}_{\mathrm{DD}}=5.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{OUT}}=5.5 \mathrm{~V}$ <br> OUT_UP, OUT_VP, OUT_WP OUT_UN, OUT_VN, OUT_WN FG_OUT | - | 0 | 10 |  |
| Output delay time | $\mathrm{t}_{\mathrm{pLH}}$ | - | PWM-Output | - | 0.5 | 1 | $\mu \mathrm{s}$ |
|  | $\mathrm{t}_{\mathrm{pHL}}$ |  |  | - | 0.5 | 1 |  |

## Application Circuit Example



Note 1: Take enough care in designing output $V_{D D}$ line and GND line to avoid short circuit between outputs, $V_{D D}$ fault or GND fault which may cause the IC to break down.

Note 2: The above application circuit and values mentioned are just an example for reference. Since the values may vary depending on the motor to be used, appropriate values must be determined through experiments before using the device.

## Package Dimensions

SSOP24-P-300-1.00


Weight: 0.32 g (typ.)

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[^0]:    Note: Take sufficient care for noise on PWM signal input pin.

