

TC3967
REV.1 04/27/2005

# 2W Packaged Self-Bias PHEMT GaAs Power FETs

### **FEATURES**

- 2W Typical Output Power
- 12.5dB Typical Linear Power Gain at 2.45GHz
- High Linearity: IP3 = 43 dBm Typical
- High Power Added Efficiency: Nominal PAE of 35%
- Breakdown Voltage: BV<sub>DGO</sub> ≥ 15V
- Wg = 5 mm
- 100 % DC Tested
- Suitable for High Reliability Application
- Lost Cost Ceramic Package

### **DESCRIPTION**

The TC3967 is a self-bias Cu-based ceramic packaged device with TC1601N PHEMT GaAs FETs, which is designed to provide the single power supply application. The Cu-based ceramic package provides excellent thermal conductivity for the GaAs FET. The devices only need to provide the positive voltage to drain and ground the source, which is suitable for oscillator, power amplifier application in a wide range of commercial application. All devices are 100% DC tested to assure consistent quality.

## **ELECTRICAL SPECIFICATIONS (@ 2.45 GHz)**

Symbol	CONDITIONS	MIN	TYP	MAX	UNIT
$P_{1dB}$	Output Power at 1dB Gain Compression Point			- 17	1234
	$V_{DS} = 8 \text{ V}$	32	33		dBm
$G_{L}$	Linear Power Gain		(B)	-90-	
	$V_{DS} = 8 \text{ V}$	THE RESERVE	12.5	01-	dB
IP3	Intercept Point of the 3 <sup>rd</sup> -order Intermodulation		43		dBm
	$V_{DS} = 8 \text{ V}, *P_{SCL} = 20 \text{ dBm}$		43		ubili
PAE	Power Added Efficiency at 1dB Compression Power		35		%
$I_{DS}$	Drain-Source Current at V <sub>DS</sub> = 8 V		600		mA
$BV_{DGO}$	Drain-Gate Breakdown Voltage at I <sub>DGO</sub> = 1.2mA	15	18		Volts
$R_{th}$	Thermal Resistance		8		°C/W

Note: \*P<sub>SCL</sub>: Output Power of Single Carrier Level.

#### PHOTO ENLARGEMENT



