TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC83220-0027

TC83220-0027 Single-Chip CMOS LSI for FL (fluorescent) Calculator

The Toshiba printing/display calculator circuit TC83220-0027 is 10- or 12-digit calculator on single-chip CMOS LSI.

TC83220-0027 can drive the printing machine (M41TAV/M42TV/M42V; EPSON) with magnet driver circuit, and can drive the fluorescent display tube with DC-DC converter. It contains a 4 K-word ROM, a 256×4 -bit RAM.

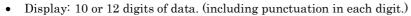


Operational Features

• Print: 11 or 13 digits of data.

(including decimal point. 2 digits of operational symbol.)

3 digits of commas.



1 digit of floating minus sign, memory load, error symbol.

3 digits of commas.

• Decimal output: Decimal set lock key controls output format.

Fixed decimal setting ("0", "1", "2", "3", "4", "6"), full floating decimal, and ADD mode.

• Key input buffer: 8 stages

• Function: 4 basic arithmetic functions (+, -, ×, ÷).

Repeat addition and subtraction.

Automatic constants in multiplication, division, percent calculation, calculations.

Automatic percent add-on and percent discount calculation.

Memory calculation.

Automatic accumulating calculation.

Gross margin profit calculation.

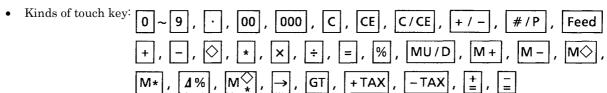
Delta percent calculation.

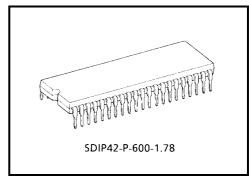
Tax calculation.

Grand total calculation.

Two-key rollover.

- Item counter: 0~999 count up or -999~0~999 count up/down by depressing of +, key.
- Punctuation: Commas for thousands on display





Weight: 4.12 g (typ.)

• Kinds of lock key: "NP" printing mode selectable switch.

" Σ " summation mode selectable switch.

"5/4" "CUT" "UP" rounding switch.

Fixed point mode selectable switch.

"0", "1", "2", "3", "4", "6", "F", "A".

"IC+" item counter mode selectable switch.

"GT" grand total memory selectable switch.

"SET", "CAL" tax memory selectable switch.

- Duty of display: Duty = 1/17.77
- · Leading zero suppression
- Trailing zero suppression
- m 1 1 1 1 mix

• Tax calculation: +TAX | key is calculation for included tax.

-TAX key is calculation for excluded tax.

SET selects set mode for tax rate.

CAL selects normal calculation mode.

Changing lock key from SET to CAL stores number of display to tax memory.

Changing lock key from CAL to SET recalls tax rate to display from tax memory.

Depression of +TAX following data key at CAL mode performs the calculating included tox

Depression of <u>-TAX</u> following data key at CAL mode performs the calculating excluded tax.

Electrical Features

- P-MOS output buffer with pull down resistor for direct driving of fluorescent display tube.
- Oscillator/clock generator internal to chip.
- Key board encoding internal to chip.
- Dual in line package.

Protection

(1) In the overflow condition, all key except "C", "C/CE", "CE", "Feed", "→" key are inoperative.

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(2) Key bouncing protection (at 4 MHz clock)

Key read in: 15 ms

Key off: 40 ms

Function Select

(1) "10/12" selectable with auto power off mode

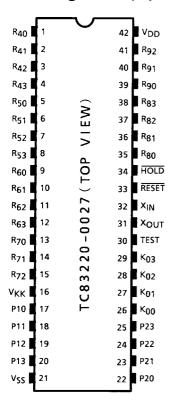
ON ······ 10-digit calculated OFF ···· 12-digit calculated

Speed of Calculation (at 4 MHz clock)

(1) Addition 1 + 1 + 31.2 ms

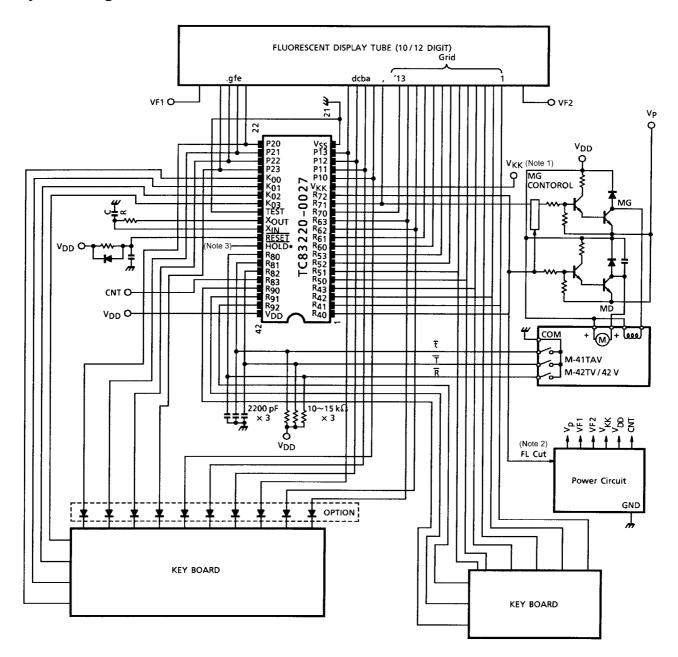
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Pin Assignment (top view)



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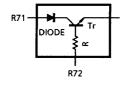
System Diagram



C = 100 pF

 $R = 1 k\Omega = 2\%$

Note 1: MG CONTROL CIRCUIT



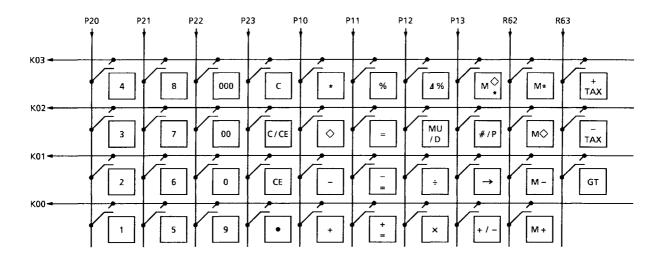
Note 2: FL CUT (R72)

Print CN......Power (VKK) CUT

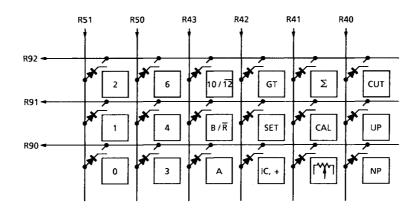
Note 3: Connection to HOLD pin is shown in the following page.

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Key Connection



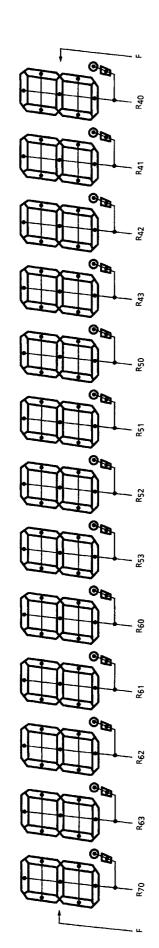
Touch Key

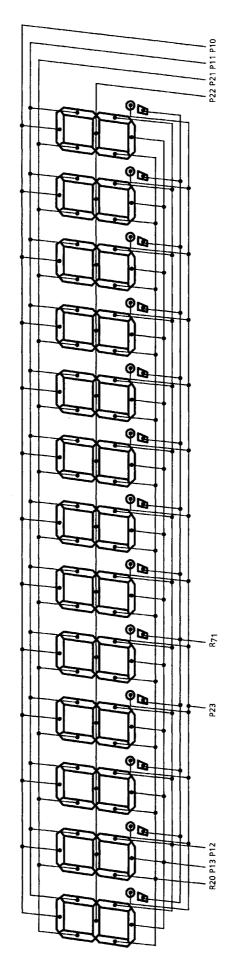


Lock Key

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Connection of FL





Note 4: R₇₀ digit (P20) of "E" data.

Note 5: R₇₀ digit (P22) of "-" data.

Note 6: R₇₀ digit (P23) of "M" data.

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Operation Example

Key				
TAB 4/5 IC 10/12 Σ GT MOD	Touch	Print		Display
F 4/5 OFF 10 OFF OFF CAL	POWER ON			
		<pf></pf>		
		С		
		<pf></pf>		0.
	1+	1. +		1.
	2-	2	R	-1.
	\Diamond	-1. ◊	R	-1.
	*	-1. *	R	
		<pf></pf>		-1.
IC+	1+	1. +		1.
	2-	2	R	-1.
	\Diamond	002		
		-1. ◊	R	-1.
	*	002		
		-1. *	R	
		<pf></pf>		-1.
OFF	3×	3. ×		3.
	4÷	4. ÷		12.
	=	4. =		
		3. *		
		<pf></pf>		3.
	5×	5. ×		5.
	6%	6. %		
		0.3 *		
		<pf></pf>		0.3
	+	5.3 + %		
		<pf></pf>		5.3
	2÷	2. ÷		2.
	3%	3. %		
		66.66666666 *		
		<pf></pf>		66.6666666
	2 MU/D	2. GM		2.
	3=	3. %		
		0.06185567 Δ *		
		2.06185567 *		
		<pf></pf>		2.06185567
	2∆%	2. Δ		2.
	3=	3. =		
		1. Δ *		
		50. Δ %		50.
		<pf></pf>		

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Note 7: <PF>Paper feed

PRINT COLOR..... R: Red

..... No mark: Black

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Key		5	5: 1
TAB 4/5 IC 10/12 Σ GT MOD	Touch	Print	Display
F 4/5 OFF 10 Σ OFF CAL	3×	3. ×	3.
	4÷	4. ÷	12.
	=	4. =	
		3. +	
		<pf></pf>	3.
	5×	5. ×	5.
	6%	6. %	
		0.3 +	
		<pf></pf>	0.3
	+	5.3 + %	
		<pf></pf>	5.3
	2÷	2. ÷	2.
	3%	3. %	
		66.6666666 +	
		<pf></pf>	66.6666666
	2 MU/D	2. GM	2.
	3=	3. %	
		0.06185567 Δ *	
		2.06185567 +	
		<pf></pf>	2.06185567
	2∆%	2. Δ	2.
	3=	3. =	
		1. Δ *	
		50. +	
		<pf></pf>	50.
	*	122.0285223 *	
		<pf></pf>	122.0285223
	GT	0. G ◊	0.
GT	2+	2. +	2.
	3+	3. +	5.
	*	5. G +	
		<pf></pf>	5.
	3-	3	R -3.
	4 –	4	R -7.
	5-	5	R -12.
	*	-12. G +	R
		<pf></pf>	-12.
	GT	-7. G ◊	R -7.
	GT	-7. G *	R
		<pf></pf>	-7.
OFF	M+	-7. M +	R M -7.
	С	0. C	М 0.

Note 7: <PF>Paper feed

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..... No mark: Black

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Key				
TAB 4/5 IC 10/12 Σ GT MOD	Touch	Print		Display
	М◊	<pf></pf>		м -7.
	M*	-7. M ◊	R	
		-7. M *	R	
F 4/5 OFF 10 Σ OFF CAL		<pf></pf>		-7.
	#/P	-7. ◊	R	-7.
	2 #/P	#2		2.
	#/P	2. ◊		2.
	0÷	0. ÷		0.
	=	0. =		
		0. *		
		<pf></pf>		E 0.
	С	0. C		
		<pf></pf>		0.
F CUT OFF 12 OFF OFF CAL	POWER ON	<pf></pf>		
		С		
		<pf></pf>		
SET		0. %		
		<pf></pf>		0.
	3			3.
CAL		3. %		
		<pf></pf>		0.
	С	0. C		
		<pf></pf>		0.
SET		3. %		
		<pf></pf>		3.
CAL				0.
	1560			1,560.
	+TAX	1,560.		
		46.8 Δ		
		1,606.8 *		1 606 0
	m 7, 57	<pf></pf>		1,606.8
	+TAX	1,606.8		
		1,655.004 *		
		1,055.004 ^ <pf></pf>		1,655.004
	1560	\r_r/		1,633.004
	1300 ×	1,560. ×		1,560.
	78900	1,000.		78,900.
	+TAX	78,900. =		70,300.
	. 11111	123,084,000. ◊		
		3,692,520. Δ		
		126,776,520. *		
		120,770,320.		

Note 7: <PF>.....Paper feed PRINT COLOR...... R: Red

..... No mark: Black

Key		Drink		Diantage		
TAB 4/5 IC 10/12 Σ GT MOD	Touch	Print		Display		
		<pf></pf>		126,776,520.		
	=			126,776,520.		
	5			5.		
	×	5. ×		5.		
	+TAX			5.		
	=	5. =				
		25. *				
		<pf></pf>		25.		
F CUT OFF 12 OFF OFF CAL	+TAX	25. ◊				
		0.75 Δ				
		25.75 *				
		<pf></pf>		25.75		
	=			25.75		
	С	0. C				
		<pf></pf>		0.		
2	1560			1,560.		
	+	1,560.00 +		1,560.00		
	1100	1 100 00		1,100.		
	+	1,100.00 +		2,660.00		
	+TAX	2,660.00 ♦				
		79.80 Δ 2,739.80 *				
		2,739.80 ^ <pf></pf>		2,739.80		
F	+TAX	2,739.80 ◊		2,739.00		
E	TIAN	82.194 Δ				
		2,821.994 *				
		<pf></pf>		2,821.994		
	980000000000	X117		980,000,000,000.		
	+TAX	980,000,000,000.				
		29,400,000,000. Δ				
		1.0094000000 *				
		<pf></pf>		E 1.0094000000		
	С	0. C				
		<pf></pf>		0.		
	1560			1560.		
	+/-			-1,560.		
	+TAX	-1,560.	R			
		-46.8 A	R			
		-1,606.8 *	R			
		<pf></pf>		-1,606.8		
	1560			1,560.		
	-TAX	1,560.				

Note 7: <PF>Paper feed

PRINT COLOR..... R: Red

..... No mark: Black



Key		Print	Display		
TAB 4/5 IC 10/12 Σ GT MOD	Touch	FIIII		Display	
F CUT OFF 12 OFF OFF CAL		-45,43689321 Δ	R		
		1,514.56310679 *			
		<pf></pf>		1,514.56310679	
	-TAX	1,514.56310679 ◊			
		-44.11348855 Δ	R		
		1,470.44961824 *			
		<pf></pf>		1,470.44961824	
SET		3. %			
		<pf></pf>		3.	
	С			0.	
CAL		0. %			
		<pf></pf>		0.	
SET		0. %			
		<pf></pf>		0.	
	1234			1,234.	
CAL		1,234. %			
		<pf></pf>		0.	
	980000000000			980,000,000,000.	
	+TAX	980,000,000,000.			
		0. *			
		<pf></pf>		E 0.	
	С	0. C			
		<pf></pf>		0.	

Note 7: <PF>Paper feed PRINT COLOR R: Red

..... No mark: Black

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Maximum Ratings (V_{SS} = 0 V)

Characteristics	Symbol	Rating	Unit
Supply voltage 1	V_{DD}	-0.5~7	V
Supply voltage 2	V _{KK}	-40~+0.5	V
Input voltage	V_{IN}	$-35 \sim V_{DD} + 0.5$	V
Output voltage	V _{OUT}	$-35 \sim V_{DD} + 0.5$	V
Output current	lout	-10	mA
Power dissipation (T _{opr} = 70°C)	P _D	600	mW
Soldering temperature, time	T _{sld}	260 (10 s)	°C
Storage temperature	T _{stg}	-55~125	°C
Operating temperature	T _{opr}	0~40	°C

Recommended Operating Conditions ($V_{SS} = 0 V$)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Max	Unit
Operating temperature	T _{opr}	_	_	0	40	°C
Supply voltage	V_{DD}	_	_	4.5	6	V
Supply voltage (FL)	V _{KK}	_	_	-30	-15	V
Supply voltage (hold)	V_{DDH}	_	_	2	6	V
Input high voltage (except schmitt circuit input)	V _{IH1}	_	N>4EV	V _{DD} × 0.7	V _{DD}	V
Input high voltage (schmitt circuit input)	V _{IH2}	_	V _{DD} ≧ 4.5 V	V _{DD} × 0.75	V _{DD}	V
Input high voltage	V _{IH3}	_	V _{DD} < 4.5 V	V _{DD} × 0.9	V _{DD}	V
Input low voltage (except schmitt circuit input)	V _{IL1}	_	V _{DD} ≥ 4.5 V	V _{KK}	V _{DD} × 0.3	V
Input low voltage (schmitt circuit input)	V _{IL2}	_		V _{KK}	V _{DD} × 0.25	٧
Input low voltage	V _{IL3}	_	V _{DD} < 4.5 V	V _{KK}	V _{DD} × 0.1	V
Output voltage (source open drain)	V _{OUT}	_	_	V _{DD} – 35	V _{DD}	V
Clock high pulse width (Note 8)	T _{WCH}	_	$V_{IN} = V_{IH}$	80	_	ns
Clock low pulse width (Note 8)	T _{WCL}	_	$V_{IN} = V_{IL}$	80	_	ns

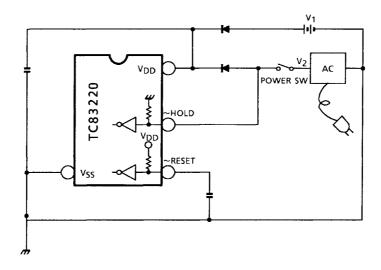
Note 8: In case of the external clock operation.

Electrical Characteristics

DC Characteristics (VSS = 0 V, VDD \pm 10%, T_{opr} = 0~40°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Hysteresis voltage (schmitt circuit input)	V_{HS}	_		_	0.7	_	V
Input current (RESET, HOLD, TEST)	I _{IN}	_	V _{DD} = 5.5 V, V _{IN} = 5.5/0 V	_		±50	μА
Output leak current (source open drain)	I _{LO}	_	$V_{DD} = 5.5 \text{ V}, V_{OUT} = -32 \text{ V}$	_	_	-10	μА
Output high voltage (P1~P2, R ₄ ~R ₉)	V _{OH}	_	V _{DD} = 4.5 V, I _{OH} = -6 mA	2.4	_	_	V
Input pull down resistor (K ₀ , R ₇ ~R ₉)	R _{IN}	_	- V _{DD} = 5.5 V, V _{KK} = -30 V	_	100	_	kΩ
Pull down resistor (source open drain)	R _{KK}	_		50	80	200	kΩ
Operating supply current	I _{DD} 0	_	V_{DD} (V_{DDH}) 5.5 V, f_{c} = 4 MHz, V_{IN} = 5.3/0.2 V	_	3	6	mA
Supply current (after clear)	I _{KK} 1	_	\\ 20\\ f 4 MI.I=	_	0.6	0.9	mA
Supply current (shown full digits)	I _{KK} 2	_	$V_{KK} = -30 \text{ V, } f_{C} = 4 \text{ MHz}$	_	3.5	6	mA
Holding supply current	I _{DD} H	_	V _{DD} = 5.5 V	_	0.5	10	μΑ
Oscillating frequency	Fφ	_	V_{DD} = 5.0 V, C = 100 pF R = 1 k Ω ± 2%	2.4	4.0	5.6	MHz

The Proposal of Outer Circuit for Tax Rate Holding with Back-Up Battery



Note 9: $V_1 = 3 V$: Battery supply

 $V_2 = 5 \text{ V: AC supply}$

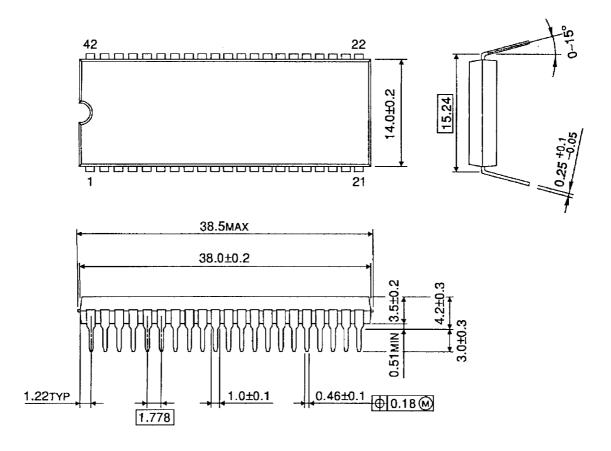
 \sim HOLD pin is pulled down in the LSI, but normally pulled up to VDD. \sim RESET pin is pulled up to VDD.

- (1) Setting POWER SW to ON, V₂ is supplied to V_{DD} pin, and also to HOLD pin. Then calculator operates normally.
- (2) Setting POWER SW from ON to OFF, V₁ is supplied to V_{DD} pin and V_{SS} is supplied to HOLD pin. Under this connection, TAX RATE is held.
- (3) Setting POWER SW to ON, V_2 is supplied to V_{DD} pin, and also to HOLD pin. Then calculator operates normally with TAX RATE to be held.

Note 10: V_1 (battery) should be supplied to the circuit after V_2 (AC) supply, because of prevention from exhaustion of battery and abnormal operation.

Package Dimensions

SDIP42-P-600-1.78 Unit: mm



Weight: 4.12 g (typ.)

RESTRICTIONS ON PRODUCT USE

000707EBA

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