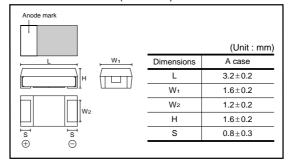
Chip tantalum capacitors

TCO Series

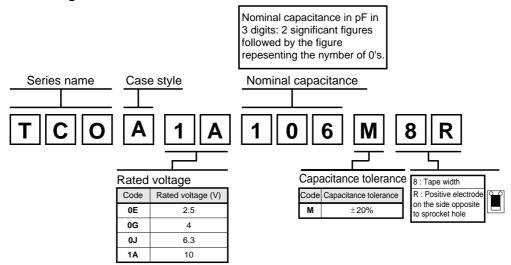
●Features (A)

- 1) Conductive polymer used for the cathode material.
- 2) Ultra-low ESR.(1/10 compared with the conventional type)
- 3) Screening by thermal shock.

●External dimensions (Unit : mm)



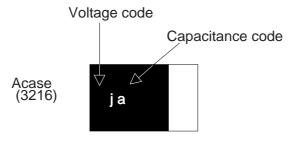
● Model name configuration



●Rated Table. Marking

TCO Series

		Raited voltage (V.DC)					
	μF	2.5 0E	4 0G	6.3 0J	10 1A		
Α	1.0						
Е	1.5						
J	2.2						
N	3.3				Α		
S	4.7				Α		
W	6.8			Α	Α		
а	10		Α	Α	Α		
е	15	Α	Α	Α			
j	22	Α	Α	Α			
n	33	Α	Α				
s	47						
W	68						



● Characteristics

Item						Performance	Test conditions (based on JIS C 5101–3)		
Operating Temperature		-55	5°C ∙	to +	105	C	Voltage reduction when temperature exceeds+85°C		
Maximum operating temperature with no voltage derating		+85	5℃						
Rated voltage (VDC)		2.5	4	6.3	10		at 85°C		
Category voltage (VDC)		2	3.2	5	8		at 105°C		
Surge voltage (VDC)		3.2	5.2	8	13		at 85℃		
DC Leakage current		3μF or 0.1CV whichever is greater Shown in " Standard list "					Ratedvoltage for 5min		
Capacitance tolerance		±20% Shall be satisfied allowance range.			ll be	satisfied allowance range.	Measuring frequency: 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circut : DC Equivalent series circuit		
Tangent of loss angle (Df, $\tan \delta$)		Shall be satisfied the voltage on " Standard list "			atisfi	ed the voltage on " Standard list "	Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circut : DC Equivalent series circuit		
ESR		Shall be satisfied the voltage on " Standard list "				ed the voltage on " Standard list "	Measuring frequency : 100±10kHz Measuring voltage : 0.5Vrms or less		
Resistance to Soldering heat	Appearance	The	ere :	sho	uld k	pe nosignificant abnormality. s should be clear.	Dip in the solder bath Solder temp : 240±5°C		
	L.C.	Less than 150% of initial limit			150	% of initial limit	Duration : 5±0.5s Repetition : 1		
	ΔC / C	Wit	thin	n±20% of initial value			·		
	tan δ	Les	ss th	an	150	% of initial limit			

Item		Performance		Test conditions (based on JIS C 5101–1 and JIS C 5101–3)			
Temperature cycle	Appearance			Repetition: 5 cycles (1 cycle: steps 1 to 4) without discontinuation.			
	L.C	Less than 500% of initial limit		Temp.	Time		
	ΔC / C	Within±20% of intial value		–55±3℃	30±3min		
			2	Room temp.	3min.or less		
			3	105±2℃	30±3min		
	Df (tan δ)	Less than 150% of initial limit		Room temp.	3min.or less		
Moisture resistance	Appearance	There should be no significant abnormality. The indications should be	After leaving the sample under such atmospheric condition that the temperature and humidity are 60 2°C and 90 to 95% RH,respectively,for 500 12h leave it at room				
	L.C	Less than 150% of initial limit					
	ΔC / C	+30% / -20%	temperature for 1 to 2h and then measure the sample.				
	Df (tan δ)	Less than 150% of initial limit					
Temperature	Temp.	–55℃					
Stebility	ΔC / C	Within 0/–20% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C	-					
	Temp.	+105°C					
	ΔC / C	Within +50/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C	Less than 1CV					
Surge voltage	Appearance	There should be no significant avnormality.	Apply the spesified sergevoltage every 5±0.5 min. for 30±5 s. each time in the atmospheric condition of 85±2°C. Repeat this rocedure 1,000 times.				
	L.C	Less than initial limit					
	ΔC / C	Within±20% of initial value	Repeat this rocedure 1,000 times.				
	Df (tan δ)	Less than initial limit					

ROHM

Tantalum capacitors

Item		Performance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3)
Loading at High temperature	Appearance	There should be nosignificant abnormality.	After applying the rated voltage for 1000 ⁺³⁶ h
	L.C	Less than 200% of initial limit	of 3Ω or less at a temperature of 85 ± 2 °C, leave
	ΔC / C	Within±20% of initial value	the sample at room temperature / humidity for
	Df (tan δ)	150% of initial limit less than	1 to 2h and measure the value.
Terminal strength	Capacitance	The measured value should be stable.	A force is applied to the terminal until it bends
	Appearance	There should no significant abnormality.	to 1mm and by a perscribed tool maintain the condition for5s.(See the figure below)
			(Unit : mm) (Apply force) 1 mm thickness=1.6mm
Adhesiveness		The terminal should not come off.	Apply force of 5N in the two directions shown in the figure below for 10: 1s after mounting the terminal on a circuit board.
Dimensions		Refer to "External dimensions"	Measure using a caliper of JISB 7507 Class 2 or higher grade.
Resistance to solv	rents	The indication should be clear	Dip in the isopropyl alcohol for 30 5s, at room temperature.
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	Dip speed=25± 2.5mm / s Pre-treatment(accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 235± 5° C Duration : 2±0.5s Solder : H63A Flux : Rosin25% IPA75%
Vibration	Capacitance	Measure value shoule not fluctuate during the measurement.	Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm Time : 2h each in X and Ydirections
	Appearance	There should no significant abnormality.	Mounting: The terminal is soldered on a print circuit board.

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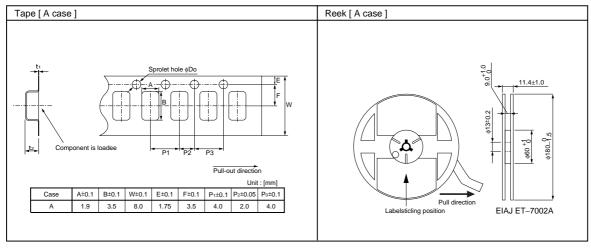
●Standard list, TCO series

< A case : 3216 size >

Part No.	Rated Voltage 85°C	Category Voltage 105°C	Surge Voltage 85°C	Cap. 120Hz	Tolerance	Leakage Current 25°C		Df 120Hz (%)		ESR 100kHz
	(V)	(V)	(V)	(μF)	(%)	1WV 5min (μA)	–55°C	25°C 85°C	105°C	$(m\Omega)$
TCO A 0E 106 □				10		3.0				
TCO A 0E 156 □	2.5	2.0	3.2	15	±20	3.8	6	6	9	500
TCO A 0E 226 □	2.0	2.0	5.2	22	±20	5.5	0		3	300
TCO A 0E 336 □				33		8.3				
TCO A 0G 685 □				6.8		3.0				800
TCO A 0G 106 □				10		4.0				
TCO A 0G 156 □	4	3.2	5.2	15	±20	6.0	6	6	9	500
TCO A 0G 226 □				22		8.8				000
TCO A 0G 336 □				33		13.2				
TCO A 0J 475 □				4.7		3.0				800
TCO A 0J 685 □				6.8		4.3				
TCO A 0J 106 □	6.3	5	8	10	±20	6.3	6	6	9	
TCO A 0J 156 □				15		9.5				500
TCO A 0J 226 □				22		13.9				
TCO A 1A 335 □				3.3		3.3				
TCO A 1A 475 □	10	8	13	4.7	±20	4.7	6	6	9	800
TCO A 1A 685 □	10		13	6.8		6.8			3	
TCO A 1A 106 □				10		10.0				500

□=Tolerance(M: ±20%)

●Packaging specifications



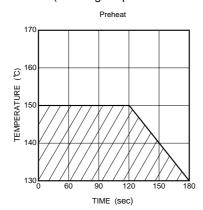
Packaging style

Case code	package	Packaç	ging style	Symbol	Basic ordering units
А	Taping	plastic taping	φ180mmReel	R	2,000pcs



• Electrical characteristics and operation notes

(1) Soldering conditions (soldering temperature and soldering time)



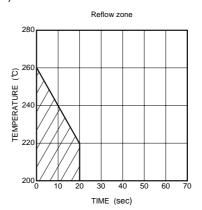


Fig.1 reflow soldering

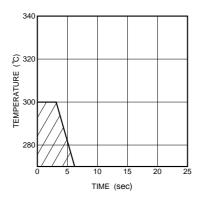


Fig.2 Hand soldering (Wattage : 30W MAX.)

(2) Leakage current-to-voltage ratio

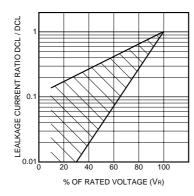
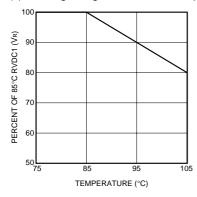


Fig.3

Tantalum capacitors

(3) Derating voltage as function of temperature



85	105°C	
Rated Voltage	Surge Voltage	Category Voltage
(V.DC)	(V.DC)	(V.DC)
2.5	3.2	2
4	5.2	3.2
6.3	8	5
10	13	8

Fig.4

(4) Reliability

The malfunction rate of tantalum solid state electrolytic capacitors varies considerably depending on the conditions of usage (ambient temperature, applied voltage, circuit resistance).

Formula for calculating malfunction rate

 $\lambda p = \lambda b \times (\pi E \times \pi SR \times \pi Q \times \pi CV)$

 λp : Malfunction rate stemming from operation

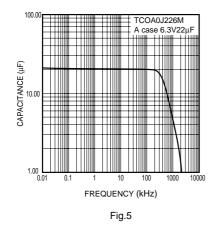
 $\begin{array}{ll} \lambda b & : \mbox{Basic malfunction rate} \\ \pi \mbox{E} & : \mbox{Environmental factors} \\ \pi \mbox{SR} & : \mbox{Series resistance} \end{array}$

 πQ : Level of malfunction rate

πcv: Capacitance

For details on how to calculate the malfunction rate stemming from operation, see the tantalum solid state electrolytic capacitors column in MIL-HDBK-217.

(5) Impedance frequency characteristics



(6) ESR frequency characteristics

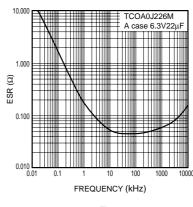
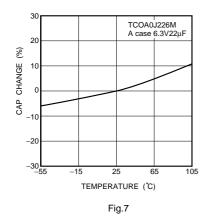


Fig.6

(7) Capacitance temperature characteristics 120Hz



(8) ESR temperature characteristics 100kHz

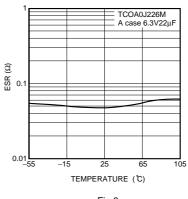


Fig.8

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