INTEGRATED CIRCUITS

DATA SHEET

TDA1519B
12 W BTL or 2 x 6 W stereo car
radio power amplifier

Product specification
File under Integrated Circuits, IC01

May 1992







12 W BTL or 2 x 6 W stereo car radio power amplifier

TDA1519B

GENERAL DESCRIPTION

The TDA1519B is an integrated class-B dual output amplifier in a 9-lead single in-line (SIL) plastic medium power package. The device is primarily developed for car radio applications.

Features

- Requires very few external components for Bridge Tied Load (BTL)
- · Stereo or BTL application
- High output power
- Low offset voltage at output (important for BTL)
- · Fixed gain
- Good ripple rejection
- Mute/stand-by switch
- Load dump protection

- ullet AC and DC short-circuit-safe to ground and V_{P}
- · Thermally protected
- Reverse polarity safe
- Capability to handle high energy on outputs (V_P = 0 V)
- No switch-on/switch-off plop
- Protected against electrostatic discharge
- · Identical inputs (inverting and non-inverting)
- Compatible with TDA1519A (except output power).

QUICK REFERENCE DATA

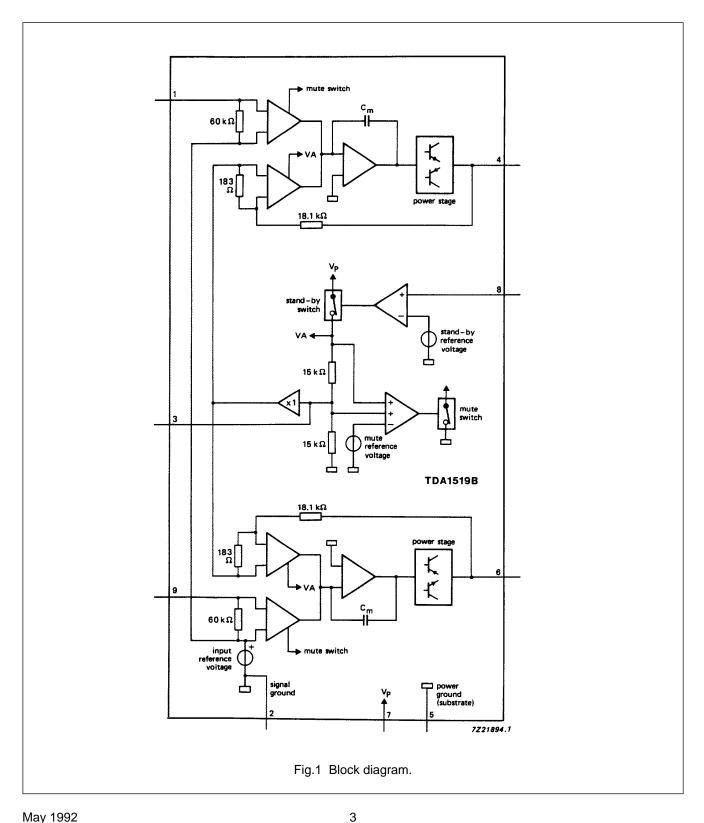
PARAMETER	CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage range						
operating		V _P	6.0	14.4	18.0	V
non-operating		V _P	_	_	30	V
load dump protected		V _P	_	_	45	V
Repetitive peak output						
current		I _{ORM}	_	_	2.5	Α
Total quiescent current		I _{tot}	_	40	80	mA
Stand-by current		I _{sb}	_	0.1	100	μΑ
Switch-on current		I _{sw}	_	_	40	μΑ
Input impedance						
BTL		Z _I	25	_	_	kΩ
stereo		$ Z_{I} $	50	_	_	kΩ
Stereo application						
Output power	THD = 5%; 4 Ω	Po	_	5	_	W
	THD = 10%; 4 $Ω$	Po	_	6	_	W
Channel separation		α	40	_	_	dB
Noise output voltage		V _{no(rms)}	_	150	_	μV
BTL application						
Output power	THD = 10%; 8 Ω	Po	_	12	_	W
Supply voltage ripple rejection	$R_S = 0 \Omega$					
	f = 100 Hz	RR	34	_	_	dB
	f = 1 kHz to 10 kHz	RR	48	_	_	dB
DC output offset voltage		$ \Delta V_0 $	_	_	250	mV
Crystal temperature		T _c	_	_	150	°C

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PACKAGE OUTLINE

9-lead SIL; plastic (SOT110B); SOT110-1; 1996 July 22.



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PINNING

NINV non-inverting input 1 2 GND1 ground (signal) 3 RR supply voltage ripple rejection output 1 4 OUT1 5 GND2 ground (substrate) OUT2 6 output 2

7 V_P positive supply voltage
 8 M/SS mute/stand-by switch

9 INV inverting input

FUNCTIONAL DESCRIPTION

The TDA1519B contains two identical amplifiers with differential input stages. The gain of each amplifier is fixed at 40 dB. A special feature of this device is the mute/stand-by switch which has the following features:

- Low stand-by current (< 100 μA)
- Low mute/stand-by switching current (low cost supply switch)
- Mute condition.

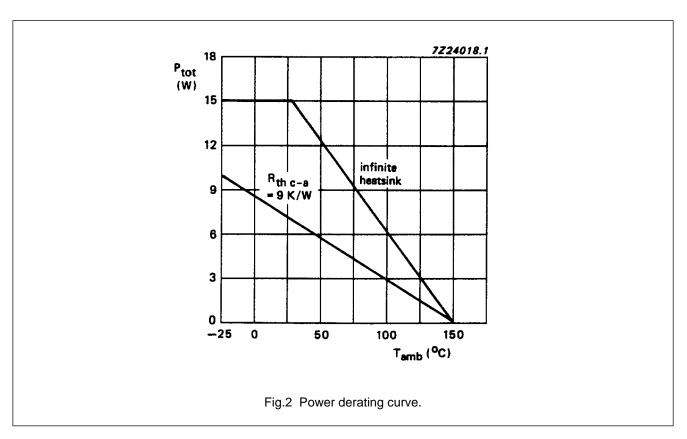
RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

PARAMETER	CONDITIONS	SYMBOL	MIN.	MAX.	UNIT
Supply voltage					
operating		V _P	_	18	V
non-operating		V _P	_	30	V
load dump protected	during 50 ms;				
	$t_r \ge 2.5 \text{ ms}$	V _P	_	45	V
AC and DC short-circuit-safe					
voltage		V _{PSC}	_	18	V
Reverse polarity		V _{PR}	_	6	V
Energy handling capability					
at outputs	$V_P = 0 V$		_	200	mJ
Non-repetitive peak output current		I _{OSM}	_	4	Α
Repetitive peak output current		I _{ORM}	_	2.5	A
Total power dissipation	see Fig.2	P _{tot}	_	15	W
Crystal temperature		T _c	_	150	°C
Storage temperature range		T _{stg}	–55	+150	°C

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DC CHARACTERISTICS

 V_P = 14.4 V; T_{amb} = 25 °C; measurements taken using Fig.3; unless otherwise specified

PARAMETER	CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply						
Supply voltage range	note 1	V _P	6.0	14.4	18.0	V
Total quiescent current		I _{tot}	_	40	80	mA
DC output voltage	note 2	V _O	_	6.95	_	V
DC output offset voltage		ΔV ₄₋₆	_	_	250	mV
Mute/stand-by switch						
Switch-on voltage level		V _{ON}	8.5	_	_	V
Mute condition		V _{mute}	3.3	_	6.4	V
Output signal in mute position	V _I = 1 V (max.);					
	f = 20 Hz to 15 kHz	V _O	_	_	20	mV
DC output offset voltage		ΔV ₄₋₆	_	_	250	mV
Stand-by condition		V _{sb}	0	_	2	V
DC current in stand-by condition		I _{sb}	_	_	100	μΑ
Switch-on current		I _{sw}	_	12	40	μΑ

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AC CHARACTERISTICS

 V_P =14.4 V; R_L = 4 Ω ; f = 1 kHz; T_{amb} = 25 °C; measurements taken using Fig.3; unless otherwise specified

PARAMETER	CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Stereo application						
Output power	note 3					
	THD = 0.5%	Po	4	5	_	W
	THD = 10%	Po	5.5	6.0	_	W
Output power at V _P = 13.2 V	note 3					
	THD = 0.5%	Po	_	3.5	_	W
	THD = 10%	Po	_	4.8	_	W
Total harmonic distortion	P _o = 1 W	THD	_	0.1	_	%
Low frequency roll-off	note 4					
	-3 dB	fL	_	45	_	Hz
High frequency roll-off	-1 dB	f _H	20	_	_	kHz
Closed loop voltage gain		G _v	39	40	41	dB
Supply voltage ripple rejection						
ON	notes 5 and 6	RR	40	_	_	dB
ON	notes 5 and 7	RR	45	_	_	dB
mute	notes 5, 6 and 7	RR	45	_	_	dB
stand-by	notes 5, 6 and 7	RR	80	_	_	dB
Input impedance		Z _i	50	60	75	kΩ
Noise output voltage (RMS value)	note 8					
ON	$R_S = 0 \Omega$	V _{no(rms)}	_	150	_	μV
ON	$R_S = 10 \text{ k}\Omega$	V _{no(rms)}	_	250	500	μV
mute	note 9	V _{no(rms)}	_	120	_	μV
Channel separation	$R_S = 10 \text{ k}\Omega$	α	40	_	_	dB
Channel unbalance		∆G _v	_	0.1	1	dB

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AC CHARACTERISTICS

 $V_P = 14.4 \text{ V}$; $R_L = 8 \Omega$; f = 1 kHz; $T_{amb} = 25 \,^{\circ}\text{C}$; measurements taken using Fig.4; unless otherwise specified

PARAMETER	CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
BTL application						
Output power	note 3					
	THD = 0.5%	Po	8	10	_	W
	THD = 10%	Po	11	12	_	W
Output power at V _P = 13.2 V	note 3					
	THD = 0.5%	Po	_	7.5	_	W
	THD = 10%	Po	_	10	_	W
Total harmonic distortion	P _o = 1 W	THD	_	0.1	_	%
Power bandwidth	THD = 0.5%;					
	$P_0 = -1 \text{ dB};$			35 to		
	w.r.t. 15 W	B _w	_	15 000	_	Hz
Low frequency roll-off	note 4					
	-1 dB	fL	_	45	_	Hz
High frequency roll-off	-1 dB	f _H	20	_	_	kHz
Closed loop voltage gain		G _v	45	46	47	dB
Supply voltage ripple rejection						
ON	notes 5 and 6	RR	34	_	_	dB
ON	notes 5 and 7	RR	48	_	_	dB
mute	notes 5, 6 and 7	RR	48	_	_	dB
stand-by	notes 5, 6 and 7	RR	80	_	_	dB
Input impedance		Z _i	25	30	38	kΩ
Noise output voltage						
(RMS value)	note 8					
ON	$R_S = 0 \Omega$	V _{no(rms)}	_	200	_	μV
ON	$R_S = 10 \text{ k}\Omega$	V _{no(rms)}	_	350	700	μV
mute	note 9	V _{no(rms)}	_	180	_	μV

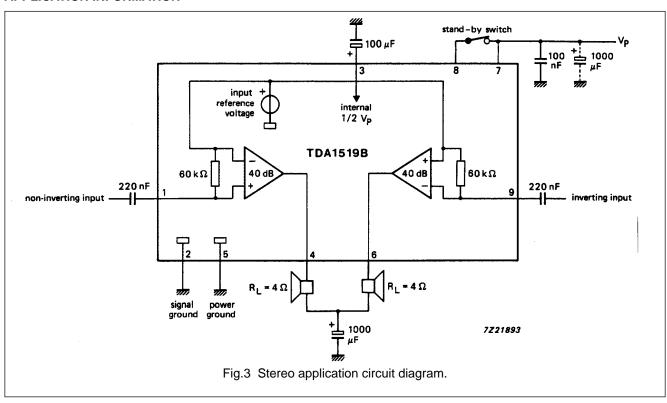
Notes to the characteristics

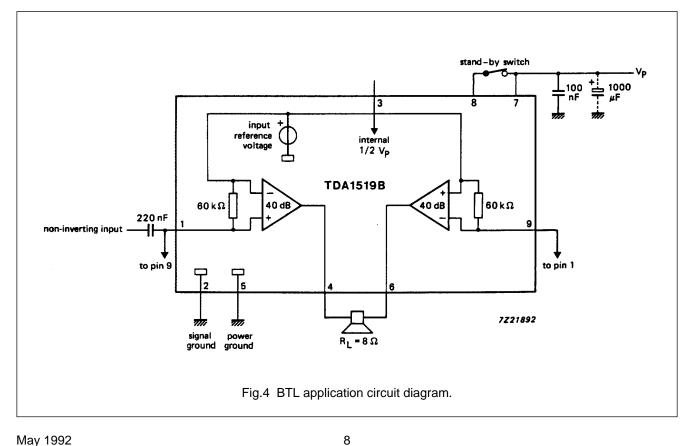
- 1. The circuit is DC adjusted at $V_P = 6 \ V$ to 18 V and AC operating at $V_P = 8.5 \ V$ to 18 V.
- 2. At 18 V < V_P < 30 V the DC output voltage $\leq V_P/2.$
- 3. Output power is measured directly at the output pins of the IC.
- 4. Frequency response externally fixed.
- 5. Ripple rejection measured at the output with a source impedance of 0 Ω (maximum ripple amplitude of 2 V).
- 6. Frequency f = 100 Hz.
- 7. Frequency between 1 kHz and 10 kHz.
- 8. Noise voltage measured in a bandwidth to 20 Hz to 20 kHz.
- 9. Noise output voltage independent of R_S ($V_I = 0 V$).

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APPLICATION INFORMATION

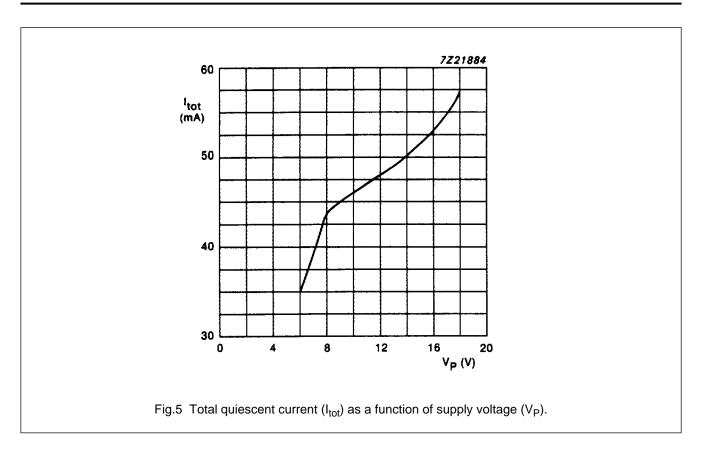


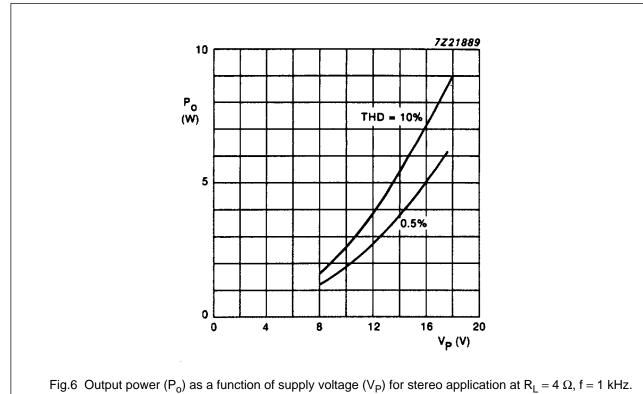


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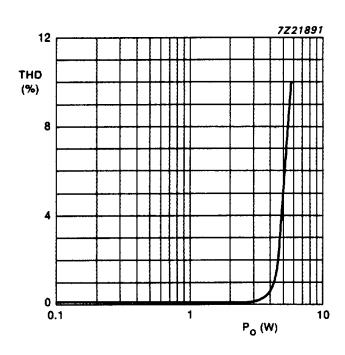


Fig.7 Total harmonic distortion (THD) as a function of output power (P_o) for stereo application at $R_L = 4~\Omega$, f = 1~kHz.

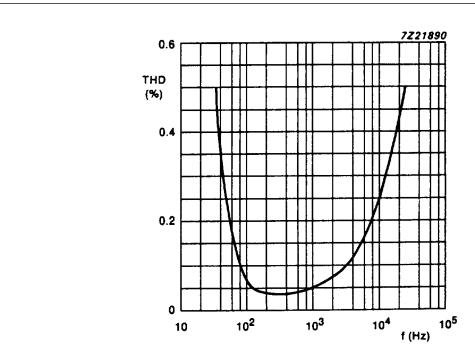


Fig.8 Total harmonic distortion (THD) as a function of operating frequency (f) for stereo application at $R_L = 4~\Omega$, $P_o = 1~W$.

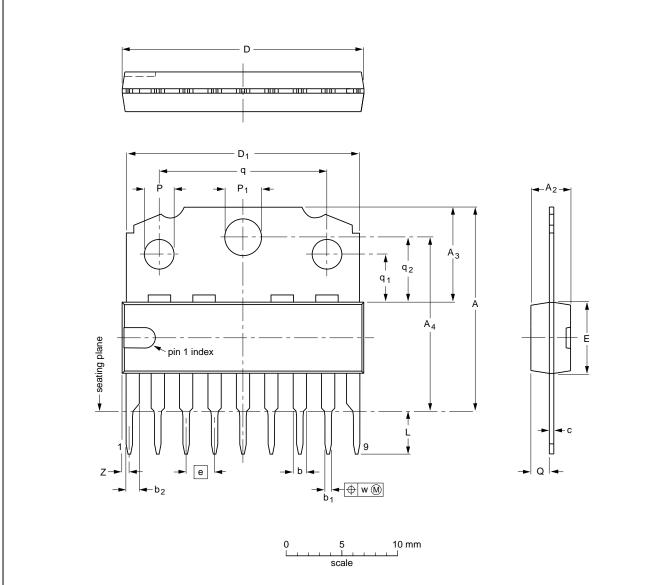
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PACKAGE OUTLINE

SIL9MPF: plastic single in-line medium power package with fin; 9 leads

SOT110-1



DIMENSIONS (mm are the original dimensions)

			•																			
UN	IIT	A	A ₂ max.	A ₃	A ₄	b	b ₁	b ₂	С	D ⁽¹⁾	D ₁	E ⁽¹⁾	е	L	Р	P ₁	Q	q	q ₁	q ₂	w	Z ⁽¹⁾ max.
mı	m	18.5 17.8	3.7	8.7 8.0	15.8 15.4	1.40 1.14	0.67 0.50	1.40 1.14	0.48 0.38	21.8 21.4	21.4 20.7	6.48 6.20	2.54	3.9 3.4	2.75 2.50	3.4 3.2	1.75 1.55	15.1 14.9	4.4 4.2	5.9 5.7	0.25	1.0

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT110-1						92-11-17 95-02-25

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SOLDERING

Introduction

There is no soldering method that is ideal for all IC packages. Wave soldering is often preferred when through-hole and surface mounted components are mixed on one printed-circuit board. However, wave soldering is not always suitable for surface mounted ICs, or for printed-circuits with high population densities. In these situations reflow soldering is often used.

This text gives a very brief insight to a complex technology. A more in-depth account of soldering ICs can be found in our "IC Package Databook" (order code 9398 652 90011).

Soldering by dipping or by wave

The maximum permissible temperature of the solder is 260 °C; solder at this temperature must not be in contact with the joint for more than 5 seconds. The total contact time of successive solder waves must not exceed 5 seconds.

The device may be mounted up to the seating plane, but the temperature of the plastic body must not exceed the specified maximum storage temperature ($T_{stg\ max}$). If the printed-circuit board has been pre-heated, forced cooling may be necessary immediately after soldering to keep the temperature within the permissible limit.

Repairing soldered joints

Apply a low voltage soldering iron (less than 24 V) to the lead(s) of the package, below the seating plane or not more than 2 mm above it. If the temperature of the soldering iron bit is less than 300 °C it may remain in contact for up to 10 seconds. If the bit temperature is between 300 and 400 °C, contact may be up to 5 seconds.

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

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