TOSHIBA SOLID STATE I/O INTERFACE MODULE

T F 1 1 0 7

DC INPUT MODULE

TOSHIBA TF1107 is DC Line Voltage Input I/O Interface Module and it includes the optical isolator.

Using this Module, you can design high reliability and compact system.

Recommended Input Voltage: VIN=12~24V

Input Impedance : $Z_{IN} = 3.1 k\Omega$

1500V AC Optical Isolation

Wide Supply Voltage $: V_{CC} = 5 \sim 18V$

Including Delay Time Circuit

Output is Compatible with TTL and CMOS Logic

Small Size and Light Weight

Unit in mm 9MAX 40MAX 21MAX 1. INPUT (-) 4. GND 2. INPUT (+) 5. VCC 6. OUTPUT **JEDEC EIAJ TOSHIBA** 10-40A1A

Small Size and Light Weight	Weight: 7g		
MAXIMUM RATINGS (Ta = 25°C) INPUT (DC LINE VOLTAGE)	50.0	_	
CHARACTERISTIC	SYMBOL	RATING	UNIT
Input Voltage (DC)	v_{IN}	30	V
Input Current (DC)	$I_{ ext{IN}}$	10	mA
Operating Frequency Range	f	65	Hz

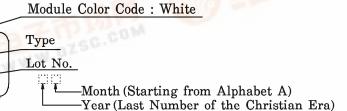
OUTPUT (LOGIC CONTROL)

TOSHIBA TF1107

JAPAN :::::

Logic Supply Voltage	V _{CC}	20	V
Output Voltage	VOUT	$-0.5 \sim V_{\text{CC}} + 0.5$	V
Output Current	IOUT	6	mA
Isolation Voltage (Input-Output) (AC)	BV _S /AC	1500 (1min)	V
Operating Temperature Range	${ m T_{opr}}$	-20~80	°C
Storage Temperature Range	$ m T_{stg}$	-20~80	°C
Lead Soldering Temperature (10s)	T_{sol}	260	°C

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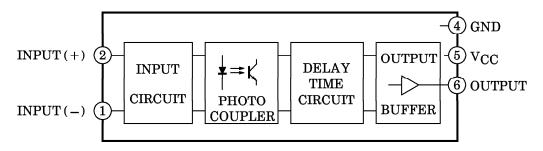


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OSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can part of the control of the buyer, when utilizing oscillab products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of the buyer of the buyer, when utilizing of the buyer of t

TOSHIBA TF1107

BLOCK DIAGRAM



ELECTRICAL CHARACTERISTICS (Ta = 25°C, V_{CC} = 5V) INPUT (DC LINE VOLTAGE)

CHARACTE	RISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Innut Voltogo	"H" Level	v_{ILH}	$ I_{ ext{OUT}} < 1 \mu A, V_{ ext{OUT}} > 4.5 V$		7.8	12	v
Input Voltage	"L" Level	$ m v_{IHL}$	$ I_{ ext{OUT}} < 1 \mu \text{A}, V_{ ext{OUT}} < 0.5 \text{V}$	5	7.5] ' [
Input Current	"H" Level	${ m I_{ILH}}$	$ I_{ m OUT} $ < 1 μ A, $V_{ m OUT}$ > 4.5 V	_	1.4	_	mA
	"L" Level	${ m I}_{ m IHL}$	$ I_{ ext{OUT}} < 1 \mu \text{A}, V_{ ext{OUT}} < 0.5 \text{V}$	_	1.3	. —	
Input Impedance		$ m z_{IN}$	$V_{ m IN}$ = 24V	_	3.1	_	$\mathbf{k}\Omega$

OUTPUT (LOGIC CONTROL)

Output Voltage	"H" Level	$v_{ m OH}$	$I_{OUT} = -10\mu A$, $V_{IN} = 24V$	4.5	4.9	I	V
	"L" Level	$v_{ m OL}$	I_{OUT} =2.5mA, V_{IN} =0V	_	0.3	0.5	
Output Current(s	ink)	$I_{ m OUT}$	$V_{OL}=1.5V, V_{IN}=0V$	6	16	l	mA
Supply Current	"H" Level	I_{CCH}	$ I_{OUT} < 1\mu A$, $V_{IN} = 24V$	-	1.0	5	mA
	"L" Level	I_{CCL}	$ I_{OUT} < 1\mu A, V_{IN} = 0V$	_	1.4	6	
Propagation	"H" Level	t_{pLH}	$V_{IN} = 0 \rightarrow 24V$	_	4.2	8	- m a
Delay Time	"L" Level	t_{pHL}	$V_{IN} = 24 \rightarrow 0V$	_	5.5	10	ms
Isolation Resistan	ce	RS	$V = 1kV, R.H = 40 \sim 60\%$	_	10^{10}	_	Ω

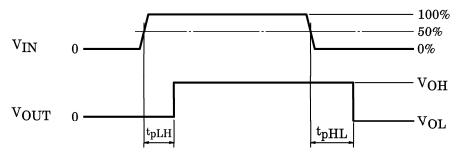


Fig.1 SWITCHING TIME TEST CONDITION

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