### 查询TL-SCSI285供应商

# 捷多邦,专业PCB打样工厂,24小时加急出货TL-SCSI285 FIXED-VOLTAGE REGULATORS FOR SCSI ACTIVE TERMINATION SLVS065F – NOVEMBER 1991 – REVISED JULY 1999

- Fully Matches Parameters for SCSI
  Alternative 2 Active Termination
- Fixed 2.85-V Output
- $\pm 1\%$  Maximum Output Tolerance at  $T_J = 25^{\circ}C$
- 0.7-V Maximum Dropout Voltage
- 620-mA Output Current
- ±2% Absolute Output Variation
- Internal Overcurrent-Limiting Circuitry
- Internal Thermal-Overload Protection
- Internal Overvoltage Protection

## description

The TL-SCSI285 is a low-dropout (0.7-V) fixed-voltage regulator specifically designed for small computer systems interface (SCSI) alternative 2 active signal termination. The TL-SCSI285 0.7-V maximum dropout ensures compatibility with existing SCSI systems, while providing a wide TERMPWR voltage range. At the same time, the  $\pm$ 1% initial tolerance on its 2.85-V output voltage ensures a tighter line-driver current tolerance, thereby increasing the system noise margin.





The GND terminal is in electrical contact with the mounting base.

The fixed 2.85-V output voltage of the TL-SCSI285 supports the SCSI alternative 2 termination standard, while reducing system power consumption. The 0.7-V maximum dropout voltage brings increased TERMPWR isolation, making the device ideal for battery-powered systems. The TL-SCSI285, with internal current limiting, overvoltage protection, ESD protection, and thermal protection, offers designers enhanced system protection and reliability.

When configured as a SCSI active terminator, the TL-SCSI285 low-dropout regulator eliminates the 220- $\Omega$  and the 330- $\Omega$  resistors required for each transmission line with a passive termination scheme, reducing significantly the continuous system power drain. When placed in series with 110- $\Omega$  resistors, the device matches the impedance level of the transmission cable and eliminates reflections.

The TL-SCSI285 is characterized for operation over the virtual junction temperature range of 0°C to 125°C.

AVAILABLE OPTIONS							
	PACKAG	CHIP					
TJ	PLASTIC POWER (KC)	SURFACE MOUNT (PW)	FORM (Y)				
0°C to 125°C	TL-SCSI285KC	TL-SCSI285PWR	TL-SCSI285Y				

The PW package is only available taped and reeled. Chip forms are tested at 25°C.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



### TL-SCSI285 **FIXED-VOLTAGE REGULATORS** FOR SCSI ACTIVE TERMINATION SLVS065F - NOVEMBER 1991 - REVISED JULY 1999

### absolute maximum ratings over operating virtual junction temperature range (unless otherwise noted)<sup>†</sup>

Continuous input voltage, V <sub>I</sub>	7.5 V
Operating virtual junction temperature range, TJ	–55°C to 150°C
Package thermal impedance, $\theta_{JA}$ (see Notes 1 and 2): KC package	22°C/W
PW package	83°C/W
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: KC or PW package	260°C
Storage temperature range, T <sub>stg</sub>	–65°C to 150°C
C C	

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. Maximum power dissipation is a function of T<sub>J</sub>(max), θ<sub>JA</sub>, and T<sub>A</sub>. The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(max) - T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of 150°C can impact reliability. Due to variations in individual device electrical characteristics and thermal resistance, the built-in thermal overload protection may be activated at power levels slightly above or below the rated dissipation.
  - 2. The package thermal impedance is calculated in accordance with JESD 51, except for through-hole packages, which use a trace length of zero.

## recommended operating conditions

			TL-SCSI285		
			MAX	UNIT	
Input voltage, VI	TJ = 25°C			V	
Input voltage, V <sub>I</sub>	$T_J = 0^{\circ}C$ to $125^{\circ}C$	3.55	5.5	V	
	KC package	0	620	m۸	
	PW package	0	500	ША	
Operating virtual junction temperature range, T <sub>J</sub>			125	°C	

## electrical characteristics, VI = 4.5 V, IO = 500 mA, TJ = 25°C (unless otherwise noted)

DADAMETED	TEST CONDITIONS <sup>‡</sup>			TL-SCSI285KC				
PARAMETER				MIN	TYP	MAX	UNIT	
Output voltage	I <sub>O</sub> = 20 mA to 500 mA,	V <sub>I</sub> = 3.55 V to 5.5 V,	TJ = 25°C	2.82	2.85	2.88	v	
Output voltage	I <sub>O</sub> = 500 mA to 620 mA,	V <sub>I</sub> = 3.65 V to 5.5 V,	T <sub>J</sub> = 0 to 125°C	2.79		2.91		
Input regulation	V <sub>I</sub> = 3.55 V to 5.5 V				5	15	mV	
Ripple rejection	f = 120 Hz,	Vripple = 1 VO(PP)			-62		dB	
	I <sub>O</sub> = 20 mA to 620 mA				5	30	mV	
Output regulation	I <sub>O</sub> = 20 mA to 500 mA				5	30		
Output noise voltage	f = 10 Hz to 100 kHz				500		μV	
Dranaut valtage	IO = 500 mA					0.7	V	
Dropout voltage	I <sub>O</sub> = 620 mA					0.8		
	IO = 0				2	5	5 6	
Diog ourread	IO = 27 mA, equivalent 1 line	e asserted			3	6		
bias current	I <sub>O</sub> = 500 mA, equivalent 18	lines asserted (8-bit)			26	26 49 MA		
	I <sub>O</sub> = 620 mA				37	62		

<sup>‡</sup> Pulse-testing techniques are used to maintain the virtual junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.1-µF capacitor across the input and a 22.0-µF tantalum capacitor with equivalent series resistance of 1.5  $\Omega$  on the output.



## electrical characteristics, $V_I = 4.5 V$ , $I_O = 500 mA$ , $T_J = 25^{\circ}C$ (unless otherwise noted)

DADAMETED	TEST CONDITIONS <sup>†</sup>			TL-SCSI285PW			LINUT
PARAMETER				MIN	TYP	MAX	
	$I_{O} = 20 \text{ mA to } 500 \text{ mA}, \qquad V_{I} = 3.55 \text{ V to } 5.5 \text{ V}$	\/ 2 55 \/ to 5 5 \/	TJ = 25°C	2.82	2.85	2.88	V
		T <sub>J</sub> = 0 to 125°C	2.79		2.91	Ň	
Input regulation	$V_{I} = 3.55 \text{ V} \text{ to } 5.5 \text{ V}$				5	15	mV
Ripple rejection	f = 120 Hz,	V <sub>ripple</sub> = 1 V <sub>O(PP)</sub>			-62		dB
Output regulation	I <sub>O</sub> = 20 mA to 500 mA				5	30	mV
Output noise voltage	f = 10 Hz to 100 kHz				500		μV
Dropout voltage	I <sub>O</sub> = 500 mA					0.7	V
	IO = 0				2	5	
Bias current	I <sub>O</sub> = 27 mA, equivalent 1	line asserted			3	6	mA
	I <sub>O</sub> = 500 mA, equivalent	18 lines asserted (8-bit)			26	49	

† Pulse-testing techniques are used to maintain the virtual junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.1-µF capacitor across the input and a 22.0-µF tantalum capacitor with equivalent series resistance of 1.5  $\Omega$  on the output.

DADAMETED	TEST CONDITIONS <sup>†</sup>		TL-SCSI285Y			
FARAWETER			TYP	MAX	UNIT	
Output voltage	$I_{O} = 20 \text{ mA to } 500 \text{ mA}, \qquad V_{I} = 3.55 \text{ V to } 5.5 \text{ V}$		2.85		V	
Input regulation	V <sub>I</sub> = 3.55 V to 5.5 V		5		mV	
Ripple rejection	f = 120 Hz, V <sub>ripple</sub> = 1 V <sub>O</sub> (PP)		-62		dB	
	I <sub>O</sub> = 20 mA to 620 mA		5		m\/	
	I <sub>O</sub> = 20 mA to 500 mA		5		IIIV	
Output noise voltage	f = 10 Hz to 100 kHz		500		μV	
	IO = 0	2				
Pice ourrept	I <sub>O</sub> = 27 mA, equivalent 1 line asserted		3			
Bias current	I <sub>O</sub> = 500 mA, equivalent 18 lines asserted (8-bit)		26		ША	
	I <sub>O</sub> = 620 mA		37			

## electrical characteristics, $V_I$ = 4.5 V, $I_O$ = 500 mA, $T_J$ = 25°C

<sup>†</sup> Pulse-testing techniques are used to maintain the virtual junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.1-µF capacitor across the input and a 22.0-µF tantalum capacitor with equivalent series resistance of 1.5  $\Omega$  on the output.



## TL-SCSI285 FIXED-VOLTAGE REGULATORS FOR SCSI ACTIVE TERMINATION SLVS065F – NOVEMBER 1991 – REVISED JULY 1999



**Figure 1. Typical Application Schematic** 



## **COMPENSATION CAPACITOR SELECTION INFORMATION**

The TL-SCSI285 is a low-dropout regulator. This means that the capacitance loading is important to the performance of the regulator because it is a vital part of the control loop. The capacitor value and the equivalent series resistance (ESR) both affect the control loop and must be defined for the load range and the temperature range. Figures 2 and 3 can be used to establish the capacitance value and ESR range for best regulator performance.





### **IMPORTANT NOTICE**

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1999, Texas Instruments Incorporated