

TOSHIBA Photocoupler Photo Relay

# **TLP598G**

Telecommunication

Data Acquisition

Measurement Instrumentation

The TOSHIBA TLP598G consists of an aluminum gallium arsenide infrared emitting diode optically coupled to a photo–MOS FET in a six lead plastic DIP package (DIP6).

The TLP598G is a bi-directional switch which can replace mechanical relays in many applications.

- Peak off-state voltage: 400 V (min.)
- On-state current: 150 mA (max.) (A connection)
- On–state resistance:  $12 \Omega$  (max.) (A connection)
- Isolation voltage: 2500 Vrms (min.) (A connection)
- UL recognized: UL1577, file no. E67349
- Trigger LED current (Ta = 25°C)

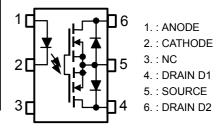
Classification (Note 1)	Trigger LE (m		Marking Of
	@I <sub>ON</sub> =	150 mA	Marking Of Classification
	Min.	Max.	
(IFT2)	_	2	T2
Standard	_	5	T2, blank

(Note 1): Application type name for certification test, please use standard product type name, i.e. TLP598G (IFT2): TLP598G

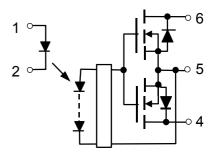
# Unit in mm 3 2 1 4 5 6 8.64 ± 0.25 8.62 7.62 7.85~8.80 11-9A1

Weight: 0.49 g

## Pin Configuration (top view)



#### **Schematic**



# **Maximum Ratings (Ta = 25°C)**

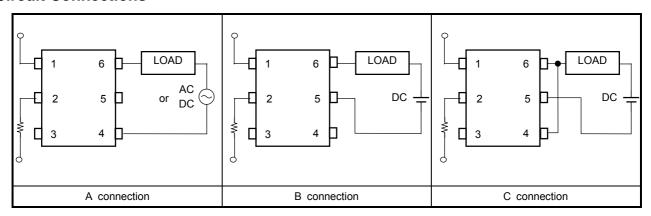
	Characteristic	Symbol	Rating	Unit		
	Forward current		I <sub>F</sub>	30	mA	
	Forward current derating (Ta ≥ 25°C)		ΔI <sub>F</sub> / °C	-0.3	mA / °C	
LED	Peak forward current (100 µs pulse, 100 pps	)	I <sub>FP</sub>	1	Α	
	Reverse voltage		V <sub>R</sub>	5	V	
	Junction temperature		Tj	125	°C	
	Off-state output terminal voltage		V <sub>OFF</sub>	400	V	
	On-state RMS current	A connection	lon	150	mA	
		B connection		200		
ctor		C connection		300		
Detector		A connection	ΔI <sub>ON</sub> / °C	-1.5	mA / °C	
	On–state current derating (Ta ≥ 25°C)	B connection		-2.0		
		C connection		-3.0	•	
	Junction temperature		Tj	125	°C	
Stora	ge temperature range	T <sub>stg</sub>	<b>−55~125</b>	°C		
Oper	Operating temperature range		T <sub>opr</sub>	-40~85	°C	
Lead	Lead soldering temperature (10 s)		T <sub>sol</sub>	260	°C	
Isola	tion voltage (AC, 1 min., R.H. ≤ 60%)	BVS	2500	Vrms		

(Note 2): Device considered a two–terminal device: Pins 1, 2 and 3 shorted together, and pins 4, 5 and 6 shorted together.

### **Recommended Operating Conditions**

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	$V_{DD}$	_	_	320	V
Forward current	l <sub>F</sub>	10	15	20	mA
On–state current	I <sub>ON</sub>	_	_	150	mA
Operating temperature	T <sub>opr</sub>	-20	_	80	°C

#### **Circuit Connections**



# **Individual Electrical Characteristics (Ta = 25°C)**

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.2	1.4	1.7	V
ED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 3 V			10	μA
	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz	_	30	_	pF
Detector	Off-state current	l <sub>OFF</sub>	V <sub>OFF</sub> = 400 V	_		1	μΑ
Dete	Capacitance	C <sub>OFF</sub>	V = 0, f = 1 MHz	_	_	_	pF

# **Coupled Electrical Characteristics (Ta = 25°C)**

Chara	acteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED curre	ent	I <sub>FT</sub>	I <sub>ON</sub> = 150 mA	_	1	5	mA
	A connection		I <sub>ON</sub> = 150 mA, I <sub>F</sub> = 10 mA	_	8	12	
On–state resistance	B connection	R <sub>ON</sub>	I <sub>ON</sub> = 200 mA, I <sub>F</sub> = 10 mA	_	4	6	Ω
	C connection		I <sub>ON</sub> = 300 mA, I <sub>F</sub> = 10 mA	_	2	3	

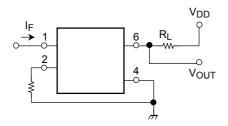
# Isolation Characteristics (Ta = 25°C)

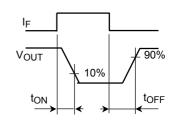
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	CS	V <sub>S</sub> = 0, f = 1 MHz	_	0.8	_	pF
Isolation resistance	$R_S$	V <sub>S</sub> = 500 V, R.H. ≤ 60%	5 × 10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage		AC, 1 minute	2500	_	_	Vrms
	$BV_S$	AC, 1 second (in oil)	_	5000	_	VIIIIS
		DC, 1 minute (in oil)	_	5000	_	$V_{DC}$

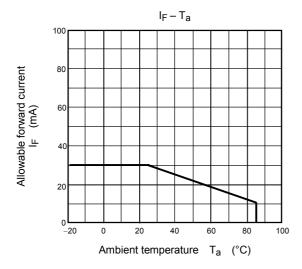
# **Switching Characteristics (Ta = 25°C)**

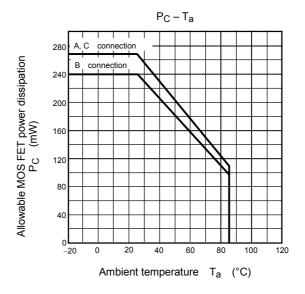
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Turn-on time	t <sub>ON</sub>	V <sub>DD</sub> = 20 V, R <sub>L</sub> = 200 Ω	_	0.3	1.0	ms
Turn-off time	toff	$I_F = 10 \text{ mA}$ (Note 3)	_	0.2	1.0	1113

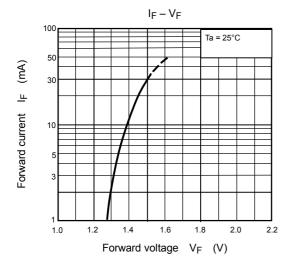
(Note 3): Switching time test circuit

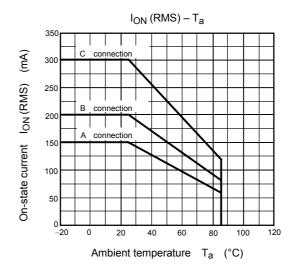


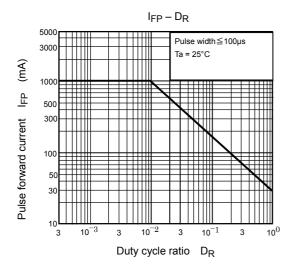


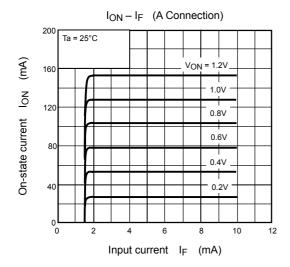


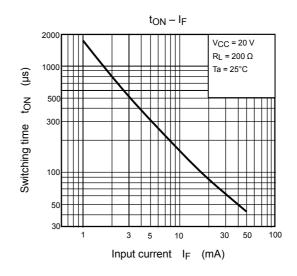


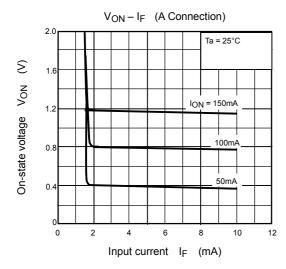


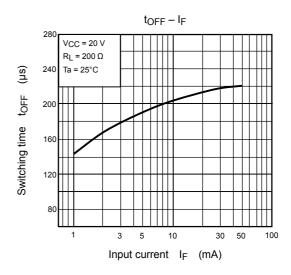


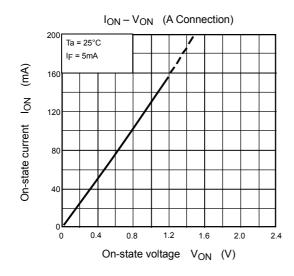


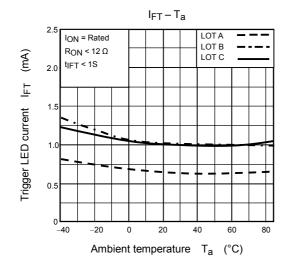


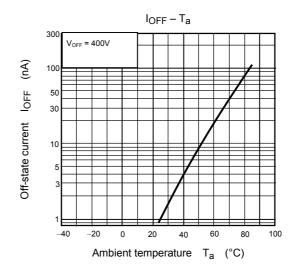


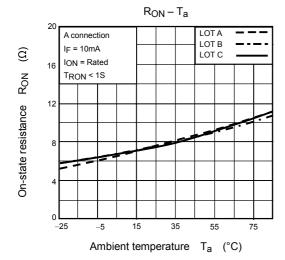


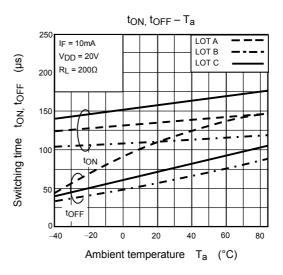












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