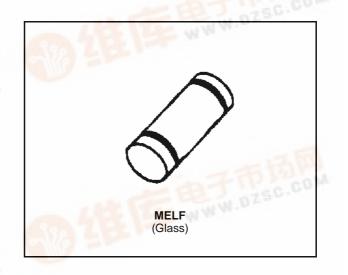
TMBYV 10-60

SMALL SIGNAL SCHOTTKY DIODE

DESCRIPTION

Metal to silicon rectifier diode in glass case featuring very low forward voltage drop and fast recovery time, intended for low voltage switching mode power supply, polarity protection and high fre-WWW.D7 quency circuits.



ABSOLUTE MAXIMUM RATINGS (limiting values)

Symbol	Parameter	Value	Unit V	
V _{RRM}	Repetitive Peak Reverse Voltage	60		
I _{F (AV)}	Average Forward Current	T _i = 25 °C	JWW.DZ	А
I _{FSM}	Surge non Repetitive Forward Current	$T_i = 25 °C$ $t_p = 10ms$	20 Sinusoidal Pulse	A
	HB 子市 TO COM	T _i = 25 °C t _p = 300μs	40 Rectangular Pulse	
T _{stg} Tj	Storage and Junction Temperature Range		- 65 to + 150 - 65 to + 125	°C °C
TL	Maximum Lead Temperature for Soldering du	260	°C	

THERMAL RESISTANCE

0
0



ELECTRICAL CHARACTERISTICS

STATIC CHARACTERISTICS

Symbol	Test Conditions			Тур.	Max.	Unit
I _R *	T _j = 25°C	$V_{R} = V_{RRM}$			0.5	mA
	T _j = 100°C				10	
V _F *	I _F = 1A	$T_j = 25^{\circ}C$			0.7	V
	I _F = 3A				1	

* Pulse test: $t_p \le 300 \mu s \ \delta < 2\%$.

DYNAMIC CHARACTERISTICS

Symbol	Test Conditions	Min.	Тур.	Max.	Unit
С	$T_j = 25^{\circ}C$ $V_R = 0$		150		pF
	$T_j = 25^{\circ}C$ $V_R = 5V$		40		

Forward current flow in a Schottky rectifier is due to majority carrier conduction. So reverse recovery is not affected by storage charge as in conventional PN junction diodes.

Nevertheless, when the device switches from forward biased condition to reverse blocking state, current is required to charge the depletion capacitance of the diode. This current depends only of diode capacitance and external circuit impedance. Satisfactory circuit behaviour analysis may be performed assuming that Schottky rectifier consists of an ideal diode in parallel with a variable capacitance equal to the junction capacitance (see fig. 5 page 4/4).

ΔΥ/

I_F (mA) 104 10³ 102 10 100 °C τ_j 25 °C = 1 = -55 °C -Ţj 10-1 ۷F (V) 10-2 0.2 0.4 0 0.6 0.8 1

Figure 1. Forward current versus forward voltage at low level (typical values).

Figure 2. Forward current versus forward voltage at high level (typical values).

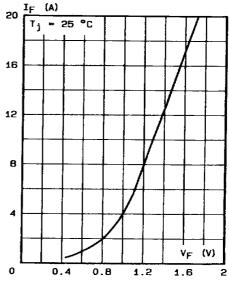
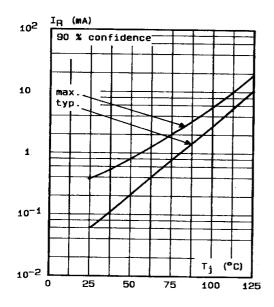
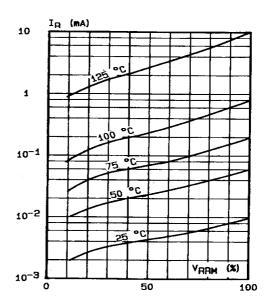


Figure 3. Reverse current versus junction temperature.



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Figure 4. Reverse current versus V_{RRM} in per cent.



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Figure 5. Capacitance C versus reverse applied voltage V_R (typical values)

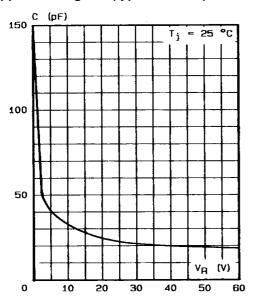


Figure 6. Surge non repetitive forward current for a rectangular pulse with t \leq 10 ms.

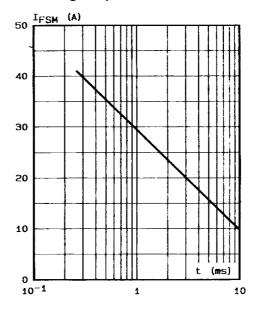
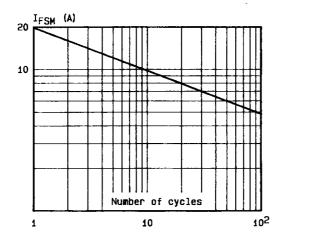
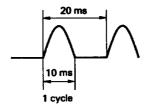


Figure 7. Surge non repetitive forward current versus number of cycles.

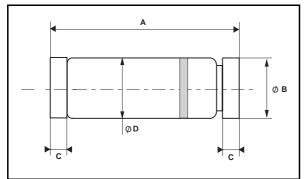




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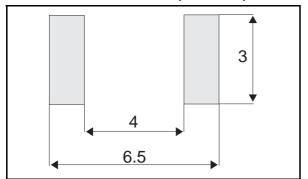
PACKAGE MECHANICAL DATA

MELF Glass



			DIMEN	ISIONS			
REF.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	4.80		5.20	0.189		0.205	
ØB	2.50		2.65	0.098		0.104	
С	0.45		0.60	0.018		0.024	
ØD		2.50			0.098		

FOOT PRINT DIMENSIONS (Millimeter)



Marking: ring at cathode end. Weight: 0.15g

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