

TRIAC(Through Hole / Non-isolated)

TMG8D80

(Sensitive Gate)

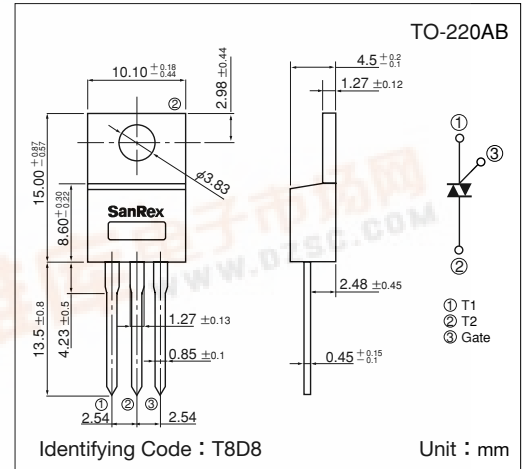
SanRex Triac **TMG8D80** is designed for full wave AC control applications. It can be used as an ON/OFF function or for phase control operation.

Typical Applications

- Home Appliances : Washing Machines, Vacuum Cleaners, Rice Cookers, Micro Wave Ovens, Hair Dryers, other control applications
- Industrial Use : SMPS, Copier Machines, Motor Controls, Dimmer, SSR, Heater Controls, Vending Machines, other control applications

Features

- $I_{T(RMS)}=8A$
- High Surge Current
- Low Voltage Drop
- Lead-Free Package



Maximum Ratings

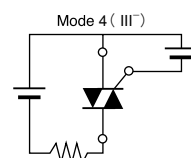
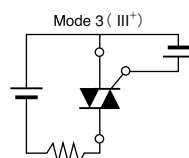
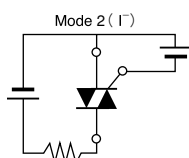
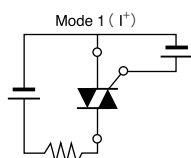
(Tj=25°C unless otherwise specified)

Symbol	Item	Reference	Ratings	Unit
V_{DRM}	Repetitive Peak Off-State Voltage		800	V
$I_{T(RMS)}$	R.M.S. On-State Current	$T_c=105^{\circ}C$	8	A
I_{TSM}	Surge On-State Current	One cycle, 50Hz/60Hz, Peak value non-repetitive	80/88	A
I^2t	I^2t (for fusing)		32	A ² S
PGM	Peak Gate Power Dissipation		5	W
PG(AV)	Average Gate Power Dissipation		0.5	W
IGM	Peak Gate Current		2	A
VGM	Peak Gate Voltage		10	V
Tj	Operating Junction Temperature		-40~+125	°C
Tstg	Storage Temperature		-40~+150	°C
	Mass		2	g

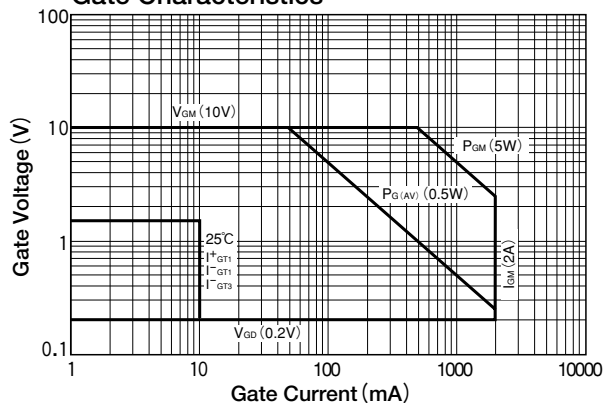
Electrical Characteristics

Symbol	Item		Reference	Ratings			Unit
				Min.	Typ.	Max.	
IDRM	Repetitive Peak Off-State Current		$V_D=V_{DRM}$, Single phase, half wave, $T_j=125^{\circ}\text{C}$			2	mA
VTM	Peak On-State Voltage		$I_T=12\text{A}$, Inst. measurement			1.4	V
I_{GT1}^{+}	1	Gate Trigger Current	$V_D=6\text{V}$, $R_L=10\Omega$			10	mA
I_{GT1}^{-}	2					10	
I_{GT3}^{+}	3					—	
I_{GT3}^{-}	4					10	
V_{GT1}^{+}	1	Gate Trigger Voltage				1.5	V
V_{GT1}^{-}	2					1.5	
V_{GT3}^{+}	3					—	
V_{GT3}^{-}	4					1.5	
VGD	Non-Trigger Gate Voltage		$T_j=125^{\circ}\text{C}$, $V_D=\frac{1}{2}V_{DRM}$	0.2			V
(dv/dt)c	Critical Rate of Rise of Off-State Voltage at Commutation		$T_j=125^{\circ}\text{C}$, $[di/dt]_c=-4\text{A/ms}$, $V_D=400\text{V}$	10			V/ μs
IH	Holding Current				15		mA
Rth	Thermal Resistance		Junction to case			2.0	$^{\circ}\text{C/W}$

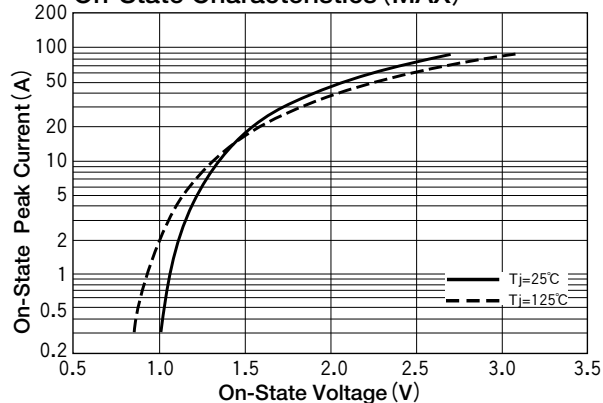
Trigger mode of the triac



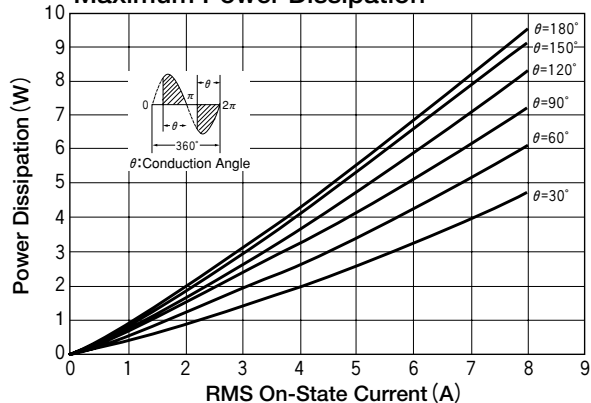
Gate Characteristics



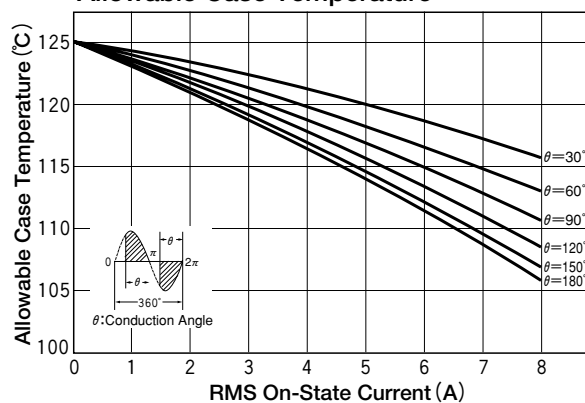
On-State Characteristics (MAX)



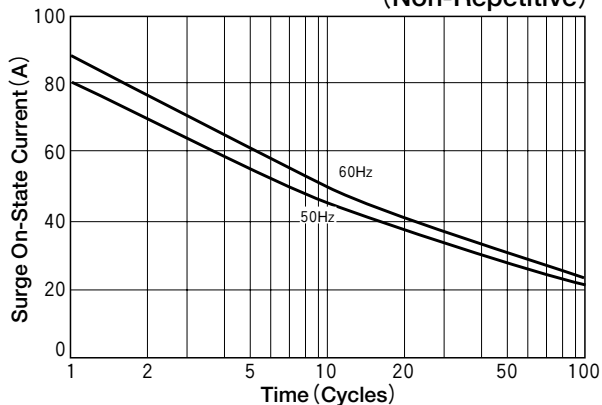
RMS On-State vs Maximum Power Dissipation



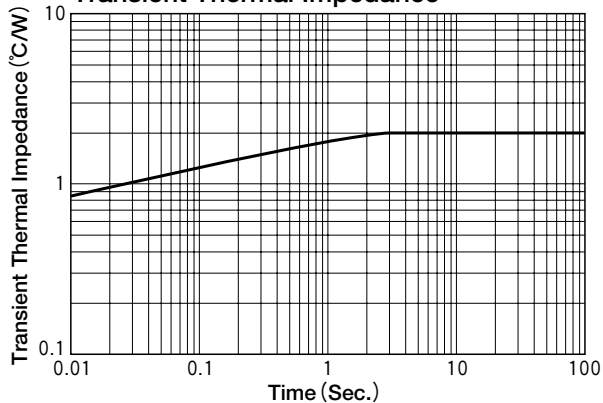
RMS On-State vs Allowable Case Temperature



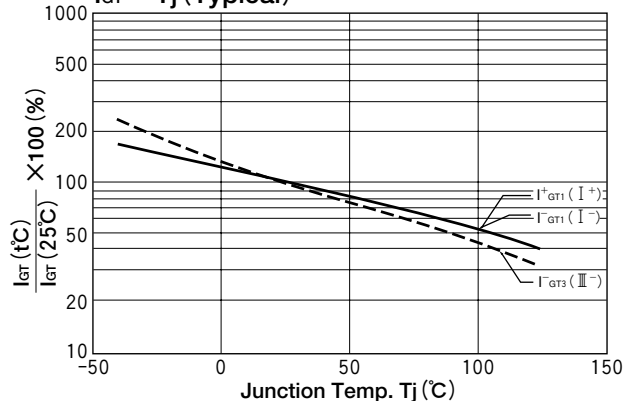
Surge On-State Current Rating (Non-Repetitive)



Transient Thermal Impedance



$I_{GT} - T_J$ (Typical)



$V_{GT} - T_J$ (Typical)

