

Toshiba Intelligent Power Device Silicon Monolithic Power MOS Integrated Circuit

## **TPD1030F**

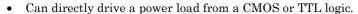
2-IN-1 Low-Side Switch for Motor, Solenoid and Lamp Drive

TPD1030F is a 2-IN-1 low-side switch.

The IC has a vertical MOSFET output which can be directly driven from a CMOS or TTL logic circuit (e.g., an MPU). The IC offers intelligent self-protection function.

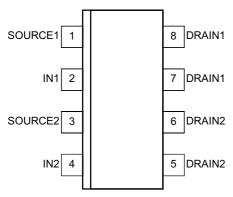
#### **Features**

• Built-in two power IC chips with a new structure combining a control block and a vertical power MOSFET (L2- $\pi$ -MOS) on each chip.



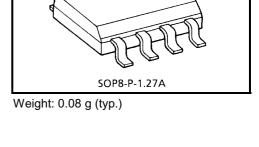
- Built-in protection circuits against overvoltage (active clamp), overtemperature (thermal shutdown), and overcurrent (current limiter).
- Low Drain-Source ON-resistance: RDS (ON) =  $0.6 \Omega$  (max) (@VIN = 5 V, ID = 0.5 A, Tch =  $25^{\circ}$ C)
- Low Leakage Current:  $IDSS = 10 \mu A (max) (@VIN = 0 \text{ V}, VDS = 30 \text{ V}, T_{ch} = 25 ^{\circ}\text{C})$
- Low Input Current: IIN =  $300 \mu A \text{ (max)} \text{ (@VIN = 5 V, Tch = } 25^{\circ}\text{C)}$
- 8-pin SOP package with embossed-tape packing.

### Pin Assignment (top view)



Note1: That because of its MOS structure, this product is sensitive to static electricity.

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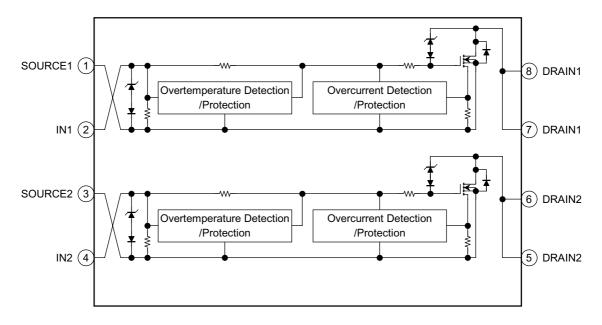


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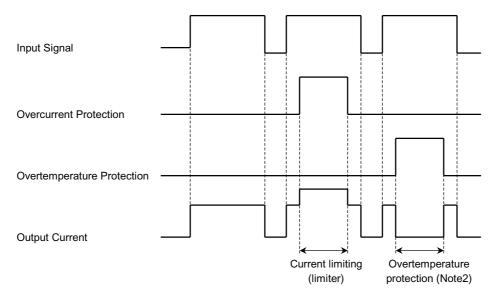
## **Block Diagram**



### **Pin Description**

Pin No.	Symbol	Pin Description
1	SOURCE1	Source pin 1
2	IN1	Input pin 1
		This pin is connected to a pull-down resistor internally, so that even when input wiring is open-circuited, output can never be turned on inadvertently.
3	SOURCE2	Source pin 2
	IN2	Input pin 2
4		This pin is connected to a pull-down resistor internally, so that even when input wiring is open-circuited, output can never be turned on inadvertently.
F 6	DRAIN2	Drain pin 2
5, 6		Drain current is limited (by current limiter) if it exceeds 1 A (min) in order to protect the IC.
7 0	DRAIN1	Drain pin 1
7, 8		Drain current is limited (by current limiter) if it exceeds 1 A (min) in order to protect the IC.

## **Timing Chart**



Note2: The overheating detector circuits feature hysteresis. After overheating is detected, normal operation is restored only when the channel temperature falls by the hysteresis amount (5°C typ.) in relation to the overheating detection temperature.

#### **Truth Table**

IN	V <sub>OUT</sub>	Mode		
L	Н	Normal		
Н	L	Nomia		
L	Н	Overcurrent		
Н	Н	Overcurrent		
L	Н	Overtemperature		
Н	Н	Overtemperature		

### **Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V <sub>DS (DC)</sub>	40	V
Drain current	ID	Internally Limited	Α
Input voltage	VIN	-0.3 to 7	٧
Power dissipation (t = 10 s)	P <sub>D</sub>	2.0 (Note3)	W
Operating temperature	T <sub>opr</sub>	-40 to 110	°C
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to 150	°C

Note3: Drive operation: Mount on glass epoxy boad [1 inch $^2 \times 0.8$  t] (in the two devices driving)



### Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note3)	R <sub>th (ch-a)</sub>	62.5	°C/W

Note3: Drive operation: Mount on glass epoxy boad [1 inch $^2 \times 0.8$  t] (in the two devices driving)

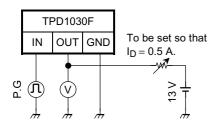
### **Electrical Characteristics (Tch = 25°C)**

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Drain-source clamp voltage	V (CL) DSS	_	$V_{IN} = 0 V$ , $I_D = 1 mA$	40	_	60	V
Input threshold voltage	V <sub>th</sub>	_	$V_{DS} = 13 \text{ V}, I_D = 10 \text{ mA}$	1.0	_	2.8	V
Protective circuit operation input voltage range	V <sub>IN (opr)</sub>	_	_	3	_	7	V
Draint cut-off current	I <sub>DSS</sub>	_	V <sub>IN</sub> = 0 V, V <sub>DS</sub> = 30 V	_	_	10	μΑ
	I <sub>IN (1)</sub>	_	V <sub>IN</sub> = 5 V, at normal operation	_	_	300	
Input current	I <sub>IN (2)</sub>	_	V <sub>IN</sub> = 5 V, when protective circuit is actuated	_	_	390	μΑ
Drain-source on resistance	R <sub>DS</sub> (ON)	_	V <sub>IN</sub> = 5 V, I <sub>D</sub> = 0.5 A	_	0.44	0.6	Ω
Overtemperature protection	T <sub>S</sub>	_	V <sub>IN</sub> = 5 V	150	160	_	°C
Overcurrent protection	IS	_	V <sub>IN</sub> = 5 V	1.0	_	_	Α
Cuitobing time	t <sub>ON</sub>	1	V <sub>DD</sub> = 13 V, V <sub>IN</sub> = 5 V,	_	_	30	μs
Switching time	toff	1	I <sub>D</sub> = 0.5 A	_	_	30	
Source-drain diode forward voltage	V <sub>DSF</sub>	_	I <sub>F</sub> = 1 A, V <sub>IN</sub> = 0 V	_	_	1.7	V

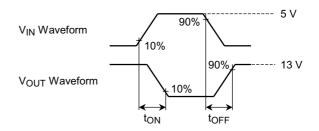
### **Test Circuit 1**

Switching time measuring circuit

#### **Test Circuit**

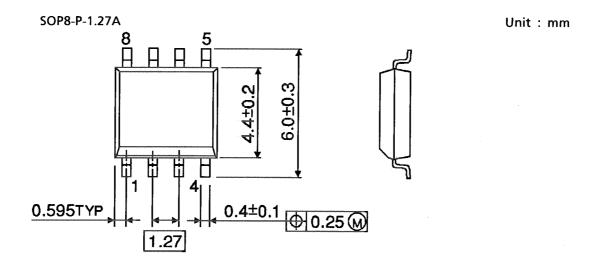


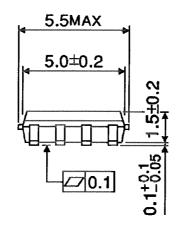
#### **Measured Waveforms**

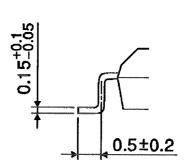




## **Package Dimensions**







Weight: 0.08 g (typ.)