

SLVS392A - JULY 2001 - REVISED JUNE 2007

# VOLTAGE DETECTOR

#### FEATURES

- Single Voltage Detector (TPS3803): Adjustable/1.5 V
- Dual Voltage Detector (TPS3805): Adjustable/3.3 V
- High ±1.5% Threshold Voltage Accuracy
- Supply Current: 3 μA Typical at V<sub>DD</sub> = 3.3 V
- Push/Pull Reset Output (TPS3805) Open-Drain Reset Output (TPS3803)
- Temperature Range: –40°C to +85°C
- 5-Pin SC-70 Package

### DESCRIPTION

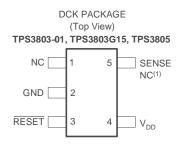
The TPS3803 and TPS3805 families of supervisory circuits provide circuit initialization and timing supervision, primarily for DSPs and processor-based systems.

The TPS3803G15 device has a fixed-sense threshold voltage V<sub>IT</sub> set by an internal voltage divider, whereas the TPS3803–01 has an adjustable SENSE input that can be configured by two external resistors. In addition to the fixed sense threshold monitored at V<sub>DD</sub>, the TPS3805 devices provide a second adjustable SENSE input. RESET is asserted in case any of the two voltages drops below V<sub>IT</sub>.

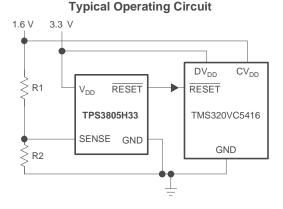
During power on,  $\overline{\text{RESET}}$  is asserted when supply voltage  $V_{DD}$  becomes higher than 0.8 V. Thereafter, the supervisory circuit monitors  $V_{DD}$  (and/or SENSE) and keeps  $\overline{\text{RESET}}$  active as long as  $V_{DD}$  or SENSE remains below the threshold voltage  $V_{IT}$ . As soon as  $V_{DD}$  (SENSE) rises above the threshold voltage  $V_{IT}$ ,  $\overline{\text{RESET}}$  is deasserted again. The product spectrum is designed for 1.5 V, 3.3 V, and adjustable supply voltages. The devices are available in a 5-pin SC-70 package. The TPS3803 and TPS3805 devices are characterized for operation over a temperature range of  $-40^{\circ}$ C to  $+85^{\circ}$ C.

# APPLICATIONS

- Applications Using DSPs, Microcontrollers, or Microprocessors
- Wireless Communication Systems
- Portable/Battery-Powered Equipment
- Programmable Controls
- Intelligent Instruments
- Industrial Equipment
- Notebook/Desktop Computers
- Automotive Systems







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This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

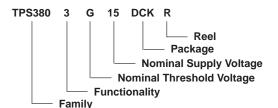
ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

#### PACKAGE INFORMATION

т <sub>А</sub>		THRESHOL		
	DEVICE NAME	V <sub>DD</sub>	SENSE	MARKING
–40°C to +85°C	TPS3803-01DCKR <sup>(1)</sup>	NA	1.226 V	AWG
	TPS3803G15DCKR <sup>(1)</sup>	1.40 V	NA	AWI
	TPS3805H33DCKR <sup>(1)</sup>	3.05 V	1.226 V	AWK

(1) The DCKR passive indicates tape and reel containing 3000 parts.

#### **ORDERING INFORMATION**



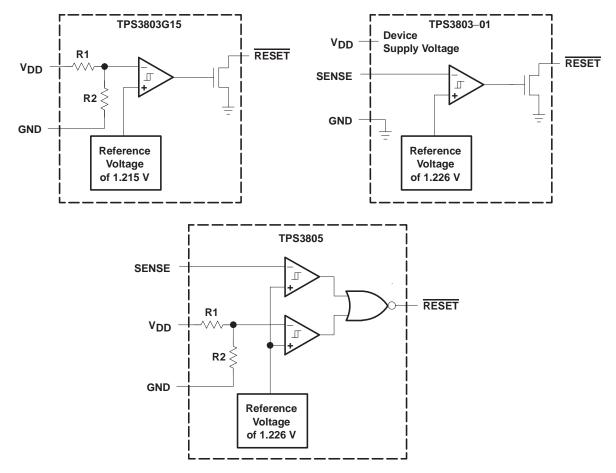
#### Function/Truth Tables

TPS3803-	01	TPS3803G15				
SENSE > V <sub>IT</sub>	RESET	V <sub>DD</sub> > V <sub>IT</sub> RESE				
0	L	0	L			
1	Н	1	Н			

TPS3805H33									
$V_{DD} > V_{IT}$ SENSE > $V_{IT}$ RESET									
0	0	L							
0	1	L							
1	0	L							
1	1	Н							



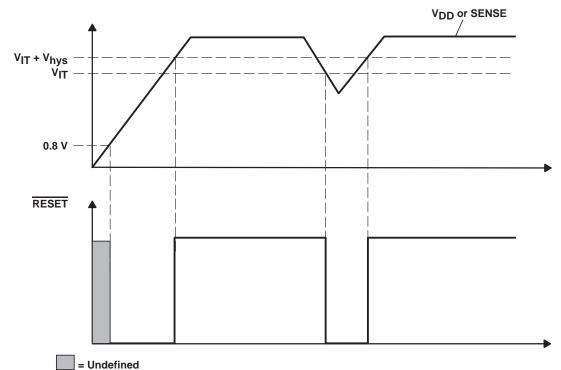
#### FUNCTIONAL BLOCK DIAGRAM



SLVS392A – JULY 2001 – REVISED JUNE 2007

## TIMING REQUIREMENTS





#### **Terminal Functions**

TERMINAL			DECODIDEION						
NAME	NO.	1/0	DESCRIPTION						
GND	2	Ι	Ground						
RESET	3	0	Active-low reset output (TPS3803—open–drain, TPS3805—push/pull)						
SENSE	5	I	Adjustable sense input						
NC	1		No internal connection						
NC (TPS3803G15)	5		No internal connection						
V <sub>DD</sub>	4	I	Input supply voltage, fixed sense input for TPS3803G15 and TPS3805						

#### ABSOLUTE MAXIMUM RATINGS(1)

Over operating free-air temperature range, unless otherwise noted.

Supply voltage, V <sub>DD</sub> <sup>(2)</sup>	
All other pins <sup>(2)</sup>	–0.3 V to +7 V
Maximum low-output current, I <sub>OL</sub>	+5 mA
Maximum high-output current, IOH	–5 mA
Input clamp current, $I_{IK}$ (V <sub>I</sub> < 0 or V <sub>I</sub> > V <sub>DD</sub> )	±10 mA
Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{DD}$ )	±10 mA
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range, T <sub>A</sub>	–40°C to +85°C
Storage temperature range, T <sub>stg</sub>	–65°C to +150°C
Soldering temperature	

(1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) All voltage values are with respect to GND. For reliable operation the device should not be continuously operated at 7 V for more than t = 1000 h.

# DISSIPATION RATING TABLEPACKAGE $T_A < +25^{\circ}C$ <br/>POWER RATINGDERATING FACTOR<br/>ABOVE $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$ <br/>POWER RATING $T_A = +85^{\circ}C$ <br/>POWER RATINGDCK321 mW2.6 mW/°C206 mW167 mW

#### **RECOMMENDED OPERATING CONDITIONS**

	MIN	MAX	UNIT
Supply voltage, V <sub>DD</sub>	1.3	6	V
Input voltage, VI	0	V <sub>DD</sub> + 0.3	V
Operating free-air temperature range, TA	-40	85	°C



SLVS392A - JULY 2001 - REVISED JUNE 2007

#### **ELECTRICAL CHARACTERISTICS**

Over recommended operating free-air temperature range, unless otherwise noted.

	PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT	
VOH	High-level output voltage (TPS3805 or	nly)		0.8 x V <sub>DD</sub>			V	
VOL	Low-level output voltage		-		0.3	V		
	Dower up react voltage(1)	VIT > 1.5 V, TA =	25°C	0.8			V	
	Power-up reset voltage <sup>(1)</sup>	VIT $\leq$ 1.5 V, TA =	25°C	1.0			V	
		SENSE		1.208	1.226	1.244	V	
VIT	Negative-going input threshold voltage <sup>(2)</sup>	TPS3803G15	$T_A = -40^{\circ}C$ to $+85^{\circ}C$	1.379	1.4	1.421		
	Vollago	TPS3805H33		3.004	3.05	3.096		
			$1.2 \text{ V} < \text{V}_{IT} < 2.5 \text{ V}$		15			
V <sub>hys</sub>	Hysteresis		$2.5 \text{ V} < \text{V}_{IT} < 3.5 \text{ V}$		30		mV	
Ιį	Input current	SENSE		-25		25	nA	
IOH	High-level output current at RESET	Open drain only	$V_{DD} = V_{IT} + 0.2V, V_{OH} = V_{DD}$			300	nA	
		TPS3803-01			2	4		
		TPS3805, TPS3803G15	V <sub>DD</sub> = 3.3 V, output unconnected		3	5		
IDD	Supply current	TPS3803-01			2	4	μA	
		TPS3805, TPS3803G15	V <sub>DD</sub> = 6 V, output unconnected		4	6		
Cl	Input capacitance		$V_{I} = 0 V \text{ to } V_{DD}$		1		pF	

(1) The lowest supply voltage at which  $\overline{\text{RESET}}$  (V<sub>OL</sub>(max) = 0.2 V, I<sub>OL</sub> = 50 µA) becomes active. t<sub>r</sub>(V<sub>DD</sub>)  $\geq$  15 µs/V. (2) To ensure the best stability of the threshold voltage, place a bypass capacitor (ceramic, 0.1 µF) near the supply terminals.

#### TIMING REQUIREMENTS

AT  $R_L = 1 M\Omega$ ,  $C_L = 50 PF$ ,  $T_A = -40^{\circ}C TO +85^{\circ}C$ .

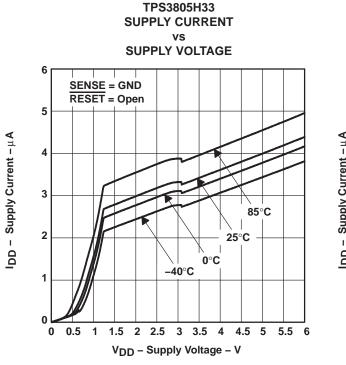
PARAMETER			TEST CONDITIONS	MIN	TYP	MAX	UNIT
	Dula a utidah	At V <sub>DD</sub>					
t <sub>w</sub> Pulse width		At SENSE	$V_{IH} = 1.05 \times V_{IT}, V_{IL} = 0.95 \times V_{IT}$	5.5			μs

#### SWITCHING CHARACTERISTICS

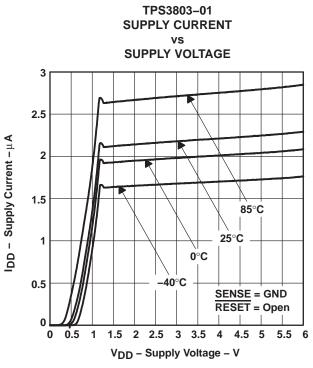
AT  $R_L = 1 M\Omega$ ,  $C_L = 50 PF$ ,  $T_A = -40^{\circ}C TO +85^{\circ}C$ .

	PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
<sup>t</sup> PHL	Propagation (delay) time, high-to-low-level output	V <sub>DD</sub> to RESET delay SENSE to RESET delay	V <sub>IH</sub> = 1.05 × V <sub>IT</sub> ,		5	100	_
<sup>t</sup> PLH	Propagation (delay) time, low-to-high-level output	V <sub>DD</sub> to RESET delay SENSE to RESET delay	VIL = 0.95 x VIT		5	100	μs

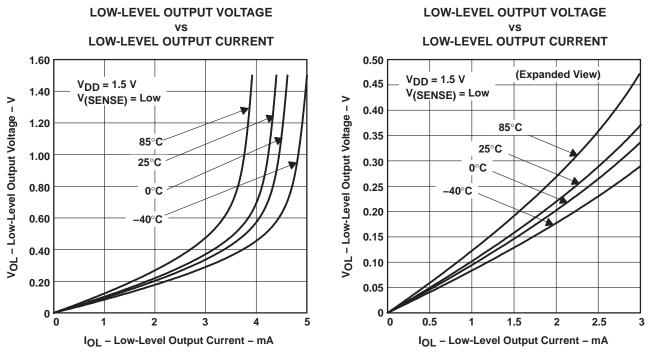
#### **TYPICAL CHARACTERISTICS**











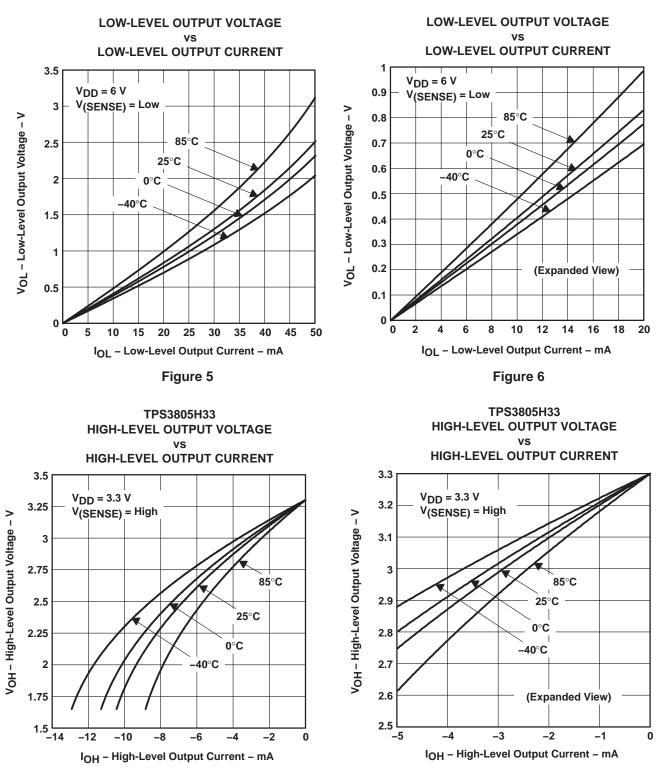




SLVS392A - JULY 2001 - REVISED JUNE 2007



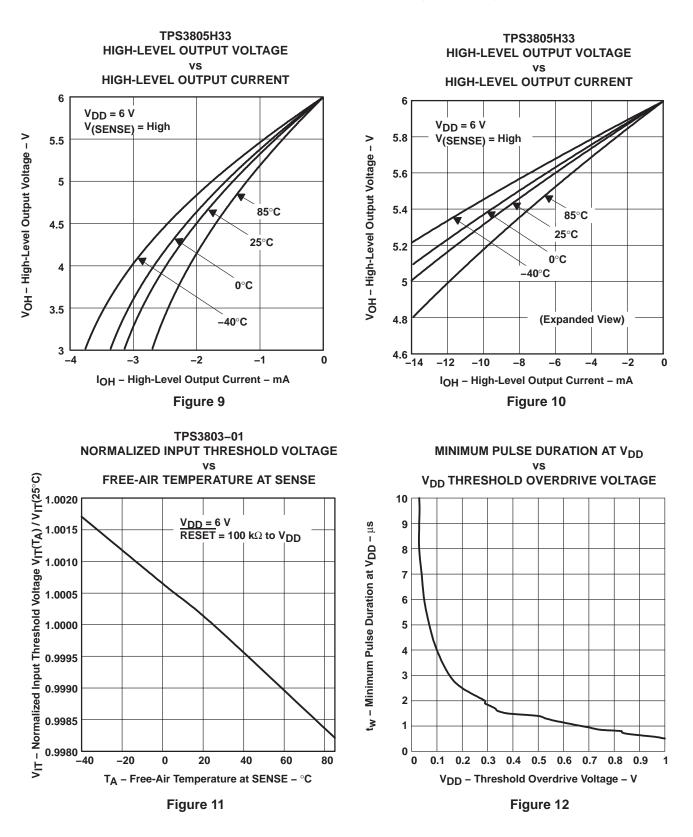
#### **TYPICAL CHARACTERISTICS (continued)**







#### **TYPICAL CHARACTERISTICS (continued)**



SLVS392A - JULY 2001 - REVISED JUNE 2007



# **TYPICAL CHARACTERISTICS (continued)**

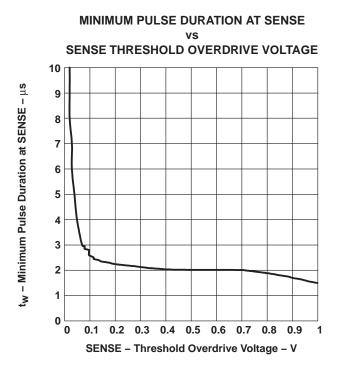


Figure 13

# **Revision History**

DATE	REV	PAGE	SECTION	DESCRIPTION
6/07	Δ	Front Page	—	Updated front page.
0/07	6/07 A 3		—	Functional block diagram change.

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

#### PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
TPS3803-01DCKR	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3803-01DCKRG4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3803G15DCKR	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3803G15DCKRG4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3805H33DCKR	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TPS3805H33DCKRG4	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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# **TAPE AND REEL INFORMATION**





# QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*	Il dimensions are nominal												
	Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
	TPS3803-01DCKR	SC70	DCK	5	3000	180.0	9.2	2.24	1.22	2.34	4.0	8.0	Q3
	TPS3803G15DCKR	SC70	DCK	5	3000	180.0	9.2	2.24	1.22	2.34	4.0	8.0	Q3
	TPS3805H33DCKR	SC70	DCK	5	3000	180.0	9.2	2.24	1.22	2.34	4.0	8.0	Q3



# PACKAGE MATERIALS INFORMATION

11-Mar-2008



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS3803-01DCKR	SC70	DCK	5	3000	180.0	180.0	85.0
TPS3803G15DCKR	SC70	DCK	5	3000	180.0	180.0	85.0
TPS3805H33DCKR	SC70	DCK	5	3000	180.0	180.0	85.0

DCK (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE



- NOTES: A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
  - D. Falls within JEDEC MO-203 variation AA.



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