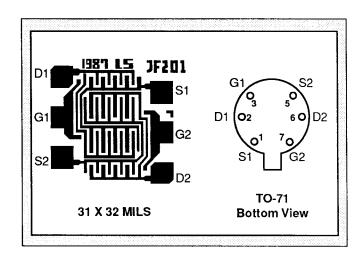


Linear Integrated Systems

| FEATUR | RES | | | | | |
|---|---|---|--|--|--|--|
| LOW DR | IFT | V _{GS1-2} / T = 10μV/°C TYP. | | | | |
| LOW NO | ISE | e _n = 6nV/ Hz @10Hz TYP. V _p = 2.5V TYP. | | | | |
| LOW PIN | | | | | | |
| 0.0000000000000000000000000000000000000 | TE MAXIMUM RATINGS NO unless otherwise noted) | TE1 | | | | |
| Maximu | n Temperatures | | | | | |
| Storage 7 | Temperature | -65° to +150°C | | | | |
| Operatin | g Junction Temperature | +150°C | | | | |
| Maximu | n Voltage and Current for Ea | ich Transistor <u>NOTE 1</u> | | | | |
| -V _{GSS} | Gate Voltage to Drain or Sc | ource 50V | | | | |
| -V _{DSO} | Drain to Source Voltage | 50V | | | | |
| - G(f) | Gate Forward Current | 10mA | | | | |
| Maximui | n Power Dissipation | | | | | |
| Device D | issipation @ Free Air - Total | 300m W | | | | |

LS-U401 - U406

LOW NOISE LOW DRIFT MONOLITHIC DUAL N-CHANNEL JFET



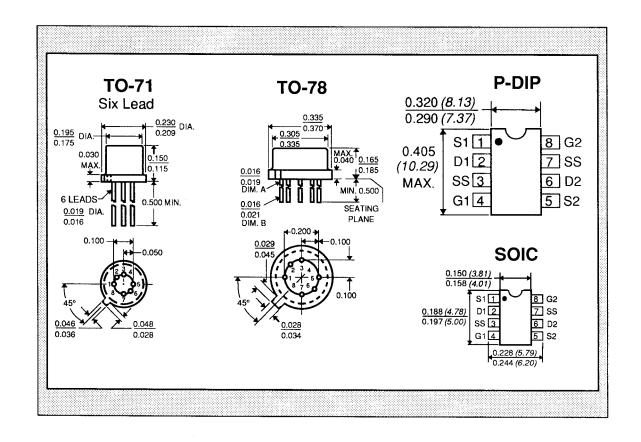
MATCHING CHARACTERISTICS @ 25°C (unless otherwise noted)

| SYMBOL | CHARACTERISTICS | LS-401 | LS-402 | LS-403 | LS-404 | LS-405 | LS-406 | UNITS | CONDITIONS |
|------------------------------|-----------------------|--------|--------|--------|--------|--------|--------|-------|--|
| V _{GS1-2} / T max. | Drift vs. Temperature | 10 | 10 | 25 | 25 | 40 | 80 | μV/°C | V _{DG} = 10V, I _D = 200μA T _A =-55°C to +125°C |
| ∣V _{GS1-2} max. | Offset Voltage | 5 | 10 | 10 | 15 | 20 | 40 | mV | V _{DG} =10V, I _D =200μA |

ELECTRICAL CHARACTERISTICS

| SYMBOL | CHARACTERISTICS | MIN. | TYP. | MAX. | UNITS | CONDITIONS | | |
|---|-----------------------------|------|------|------|-------|------------------------|------------------------|--------------------|
| BV _{gss} | Breakdown Voltage | 50 | 60 | | ٧ | V _{ps} = 0 | I _D = 1nA | |
| BV _{GGO} | Gate-to-Gate Breakdown | ±50 | | | ٧ | I _g = 1nA | I _D = 0 | I _s = 0 |
| | TRANSCONDUCTANCE | | | | | | | |
| Y _{fss} | Full Conduction | 2000 | | 7000 | μmho | V _{DG} = 10V | $V_{GS} = 0$ | f= 1kHz |
| Y _{fs} | Typical Operation | 1000 | | 2000 | µmho | V _{DG} = 15V | I _D = 200μA | f= 1kHz |
| Y _{fs1-2} /Y _{fs} | Mismatch | | 0.6 | 3 | % | | | |
| | DRAIN CURRENT | | | | | | | |
| I _{DSS} | Full Conduction | 0.5 | | 10 | mA | V _{DG} = 10V | V _{GS} = 0 | |
| IIDSS1-2/IDSS | Mismatch at Full Conduction | | 1 | 5 | % | | | |
| | GATE VOLTAGE | | | | | | | |
| V _{GS} (off) or V _P | Pinchoff Voltage | -0.5 | | -2.5 | ٧ | V _{DS} = 15V | I _D = 1nA | |
| V _{GS} (on) | Operating Range | | | -2.3 | ٧ | V _{DS} = 15V | I _D = 200μΑ | |
| | GATE CURRENT | | | | | | | |
| -I _G max. | Operating | | -4 | -15 | pА | V _{DG} = 15V | I _D = 200μΑ | |
| -I _g max. | High Temperature | - | | -10 | nA | T _A =+125°C | _ | |
| -I _{gss} max. | At Full Conduction | | | 100 | pΑ | V _{DS} = 0V | | |
| -I _{gss} max. | High Temperature | 5 | 5 | 5 | pА | V _{DG} = 15V | T _A =+125°C | |

| SYMBOL | CHARACTERISTICS | MIN. | TYP. | MAX. | UNITS | CONDITIONS |
|------------------|--|------|------|------|--------|--|
| | OUTPUTCONDUCTANCE | | | | | |
| Yoss | Full Conduction | | | 20 | µmho | $V_{DG} = 10V V_{GS} = 0$ |
| Yos | Operating | | 0.2 | 2 | μmho | V _{DG} = 15V I _D = 500μA |
| | COMMON MODE REJECTION | | | | | |
| CMR | -20 log V _{GS1-2} / V _{DS} | 95 | | | dB | V _{DS} = 10 to 20V I _D = 30μA |
| | NOISE | | | | | |
| NF | Figure | | | 0.5 | dB | $V_{DS} = 15V V_{GS} = 0 R_{G} = 10M$ |
| | | | | | | f= 100Hz NBW= 6Hz |
| e _n | Voltage | | 20 | | nV/ Hz | V _{DS} = 15V I _D = 200μA f= 10Hz |
| | | | | | | NBW= 1Hz |
| | CAPACITANCE | | | | | |
| C _{ISS} | Input | | | 8 | pF | V _{DS} = 15V I _D = 200μA f= 1MHz |
| C _{RSS} | Reverse Transfer | | | 1.5 | pF | |



NOTES:

^{1.} These ratings are limiting values above which the serviceability of any semiconductor may be impaired.

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