查询NJU7312A供应商

ANALOG FUNCTION SWITCH

GENERAL DESCRIPTION

JRC

The NJU7312A is a quad 3-channel and dual 2-channel analog function switch, especially suitable for input selector of audio equipments.

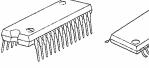
The high break down voltage analog switch controlled by 14-bit serial data based on logic operating voltage (5V) can ON and OFF of $\pm 15V$ signal.

The analog switch is realized superior linearity of on-resistance in all voltage range, low distortion and wide dynamic range.

Furthermore, the both of single and dual power supply application provides easy designing.

PACKAGE OUTLINE

捷多邦,专业P





2 A

NJU7312AL

PIN CONFIGURATION

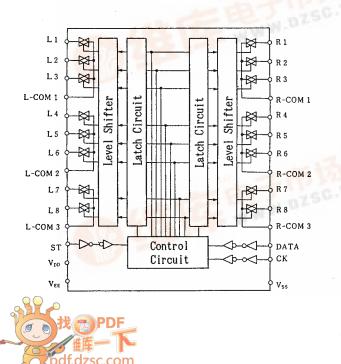


FEATURES

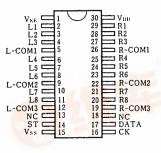
- Analog switch: quad 3 channel and dual 2 channel.
- High Break Down Voltage ----- ±15V.
- ----- THD: 0.002% (typ). Low Distortion
- Superior Linearity of ON Resistance.
- Serial Data Control.
- Package Outline SDIP 28 / DMP 30
- C-MOS Technology

VDD 28 ⊐ R 1 ⊐ R 2 27 1.2 26 L 3 25 ⊐ R 3 L-COM 1 24 ⇒ R-COM 1 L4 23 22 Ľ 5 L 6 21 20 R6 L-COM 2 L7 R-COM 2 ĭo 19 ~ R 8 R-COM 3 L-COM 3 UU 11 18 17 ST Vss 16 DATA 15 ⊐ск

BLOCK DIAGRAM







NJU7312AM





■ TERMINALS DESCRIPTION

1	lo.	CV NDOI		No.		OVMDOL			
DIP	DMP	SYMBOL	FUNCTIONS	DIP	DMP	SYMBOL	FUNCTIONS		
1	1	VEE	Negative Voltage Supply	15	16	CK	Clock input		
2	2	L1	Analog switch input/output	16	17	DATA	Data input		
3	3	L2		17	19	R-COM3	R7, L8 Common		
4	4	L3		18	20	R8	Analog switch input/output		
5	5	L-COM1	L1, L2, L3 Common	19	21	R7			
6	6	L4	Analog switch input/output	20	22	R-COM2	R4, R5, R6 Common		
7	7	L5		21	23	R6	Analog switch input/output		
8	8	L6		22	24	R5			
9	9	L-COM2	L4, L5, L6 Common	23	25	R4			
10	10	L7	Analog switch input/output	24	26	R-COM1	R1, R2, R3 Common		
11	11	L8		25	27	R3	Analog switch input/output		
12	12	L-COM3	L7, L8 Common	26	28	R2			
13	14	ST	Chip enable	27	29	R1			
14	15	Vss	GND	28	30	VDD	Positive voltage supply		

FUNCTIONAL DESCRIPTION

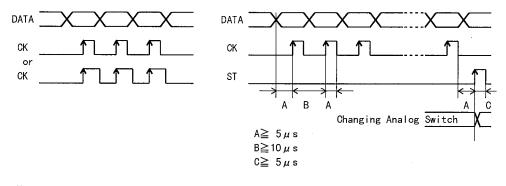
(1) Timing of DATA, CK, ST

The Serial Input Data is input to internal shift register sequentially synchronized by clock signal rising edge input from CK terminal(100 kHz max.).

The Serial Input Data in the shift register is transferred to latch circuit and renew by synchronized rising edge of Chip enable signal input from ST terminal.

(Timing Chart)

(Detailed Timing)



(2) Data Format

The 14-bit serial data strings format from MSB to LSB are 8-bit analog switch control data, 2-bit right and left channel selection data and 4-bit address data.

MSB													LSB	
1	2	3	4	5	6	7	8	9	10	11	12	13	14	
←			Swi	tch				← Cha	nnel→		Add	lress		,

6

(Switch)

Bit1 \sim bit8 select the analog switch ON and OFF

0: switch off 1: switch on

(Channel)

Bit9 and 10 select the channel.

bit9	bit10	CHANNEL
1	1	L and R
1	0	R only
0	1	L only

(Address)

Bit11 to 14 select the address. This address select is used for chip selection when this LSI is connected to the common bus line.

Type No.	bit11	bit12	bit13	bit14
NJU7311A	0	0	0	0
NJU7312A	1	0	0	0
NJU7313A	0	1	0	0

(3) Supply Voltage

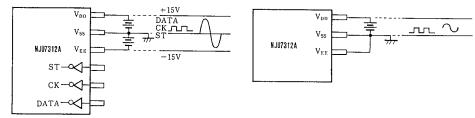
The power supply of NJU7312A is divided into two portions of analog switch part and control part. The analog switch part operate by dual power supply (+ and -) and control part is operated by single power supply (+) only.

The analog switch part can be also operated by single power supply. In this case, the supply voltage should be half of dual supply operation mode.

Furthermore , the CK , DATA and ST terminals realize direct interface with 5V operated family because of its input threshold level is adjusted.

Dual Power Supply (+ and -)

Single Power Supply (+)



ABSOLUTE MAXIMUM RATINGS

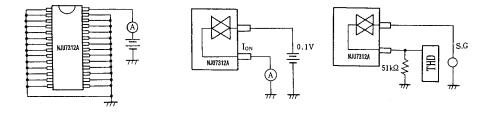
SOLUTE MAXIMUM RATINGS			(Ta=25℃)
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{DD} - V _{EE} V _{DD} - V _{SS} V _{EE} - V _{SS}	34 +17 -17	v
Input Voltage	VIN	Vss-0.3~Vdd+0.3	٧
Power Dissipation	PD	300	mW
Operating Temperature	Topr	-30 ~ +75	Ĉ
Storage Temperature	Tstg	-40 ~ +125	°C

ELECTRICAL CHARACTERISTICS

 $(V_{DD}=+16V, V_{SS}=0V, V_{EE}=-16V, Ta=25^{\circ}C)$

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT
Operating Voltage	V _{DD} -V _{ss} V _{ee} -V _{ss}		-16		16 -8	۷
Operating Current		V_{DD} =+16V, V_{EE} =-16V, V_{SS} =0V			3	mA
Back-Up Voltage	Vв		4		16	۷
Back-Up Current	Ів	V _{DD} =+4V, V _{SS} =V _{EE} =0V, Circ.1			10	μA
High-Level Input Voltage	V _{1H}	CK, DATA, ST Terminals	4		16	۷
Low-Level Input Voltage	VIL	CK, CATA, ST Terminals	0		1	۷
Min. Operating Pulse Width	t _{MIN}		5			μS
Switch ON Resistance	Ron	Circ.2		100	200	Ω
Total Harmonic Distortion	THD	f _{1N} =20~20kHz,V _{1N} =1V _{rms} Circ.3		0.002	0.005	%

MEASUREMENT CIRCUIT DIAGRAMS



(Circ.1)

(Circ.2)

(Circ.3)

MEMO

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