



UNIVERSAL ACTIVE FILTER

FEATURES

- **VERSATILE:**
 - Low-Pass, High-Pass
 - Band-Pass, Band-Reject
- **SIMPLE DESIGN PROCEDURE**
- **ACCURATE FREQUENCY AND Q:**
 - Includes On-Chip 1000pF $\pm 0.5\%$ Capacitors

APPLICATIONS

- TEST EQUIPMENT
- COMMUNICATIONS EQUIPMENT
- MEDICAL INSTRUMENTATION
- DATA ACQUISITION SYSTEMS
- MONOLITHIC REPLACEMENT FOR UAF41

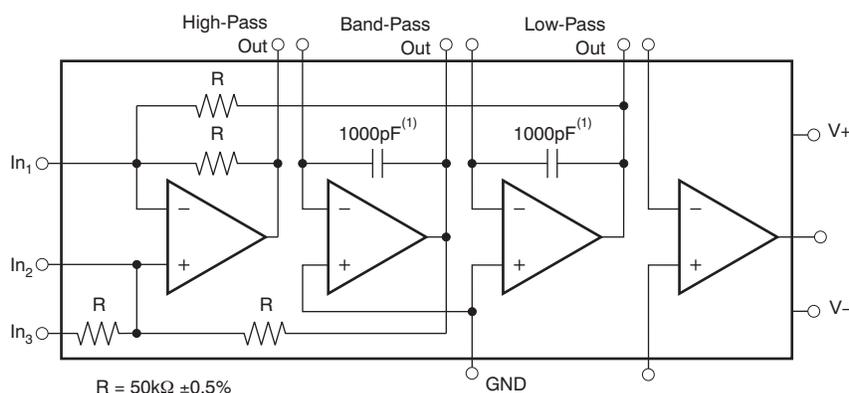
DESCRIPTION

The UAF42 is a universal active filter that can be configured for a wide range of low-pass, high-pass, and band-pass filters. It uses a classic state-variable analog architecture with an inverting amplifier and two integrators. The integrators include on-chip 1000pF capacitors trimmed to 0.5%. This architecture solves one of the most difficult problems of active filter design—obtaining tight tolerance, low-loss capacitors.

A DOS-compatible filter design program allows easy implementation of many filter types, such as Butterworth, Bessel, and Chebyshev. A fourth, uncommitted FET-input op amp (identical to the other three) can be used to form additional stages, or for special filters such as band-reject and Inverse Chebyshev.

The classical topology of the UAF42 forms a time-continuous filter, free from the anomalies and switching noise associated with switched-capacitor filter types.

The UAF42 is available in 14-pin plastic DIP and SOIC-16 surface-mount packages, specified for the -25°C to $+85^{\circ}\text{C}$ temperature range.



NOTE: (1) $\pm 0.5\%$.



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This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Over operating free-air temperature range unless otherwise noted.

	UAF42	UNIT
Power Supply Voltage	±18	V
Input Voltage	±V _S ±0.7	V
Output Short-Circuit	Continuous	
Operating Temperature	–40 to +85	°C
Storage Temperature	–40 to +125	°C
Junction Temperature	+125	°C

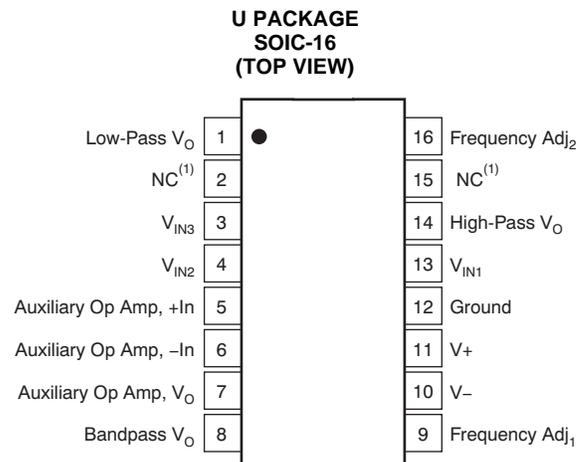
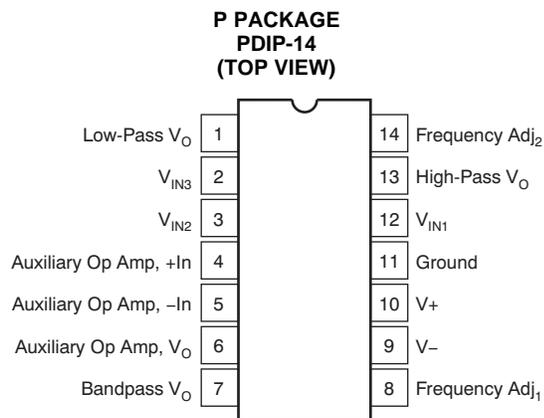
- (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended period may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not supported.

ORDERING INFORMATION⁽¹⁾

PRODUCT	PACKAGE-LEAD	PACKAGE DESIGNATOR	PACKAGE MARKING
UAF42AP	PDIP-14	N	UAF42APS
UAF42APG4			
UAF42AU	SOIC-16	DW	UAF42AUS
UAF42AUE4			

- (1) For the most current package and ordering information see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.

PIN CONFIGURATIONS



NOTE: (1) NC = no connection. For best performance connect all NC pins to ground to minimize inter-lead capacitance.

ELECTRICAL CHARACTERISTICSAt $T_A = +25^\circ\text{C}$, and $V_S = \pm 15\text{V}$, unless otherwise noted.

PARAMETER	CONDITIONS	UAF42AP, AU			UNIT
		MIN	TYP	MAX	
FILTER PERFORMANCE					
Frequency Range, f_n			0 to 100		kHz
Frequency Accuracy vs Temperature	$f = 1\text{kHz}$		0.01	1	%/ $^\circ\text{C}$
Maximum Q			400		—
Maximum (Q • Frequency) Product			500		kHz
Q vs Temperature	$(f_o \cdot Q) < 10^4$		0.01		%/ $^\circ\text{C}$
Q Repeatability	$(f_o \cdot Q) < 10^5$		0.025		%/ $^\circ\text{C}$
Offset Voltage, Low-Pass Output	$(f_o \cdot Q) < 10^5$		2		%
Resistor Accuracy			0.5	1	%
OFFSET VOLTAGE⁽¹⁾					
Input Offset Voltage vs Temperature vs Power Supply	$V_S = \pm 6\text{V to } \pm 18\text{V}$	80	± 0.5 ± 3 96	± 5	mV $\mu\text{V}/^\circ\text{C}$ dB
INPUT BIAS CURRENT⁽¹⁾					
Input Bias Current	$V_{CM} = 0\text{V}$		10	50	pA
Input Offset Current	$V_{CM} = 0\text{V}$		5		pA
NOISE					
Input Voltage Noise					
Noise Density: $f = 10\text{Hz}$			25		$\text{nV}/\sqrt{\text{Hz}}$
Noise Density: $f = 10\text{kHz}$			10		$\text{nV}/\sqrt{\text{Hz}}$
Voltage Noise: $\text{BW} = 0.1\text{Hz to } 10\text{Hz}$			2		μV_{PP}
Input Bias Current Noise					
Noise Density: $f = 10\text{kHz}$			2		$\text{fA}/\sqrt{\text{Hz}}$
INPUT VOLTAGE RANGE⁽¹⁾					
Common-Mode Input Range			± 11.5		V
Common-Mode Rejection	$V_{CM} = \pm 10\text{V}$	80	96		dB
INPUT IMPEDANCE⁽¹⁾					
Differential			$10^{13} \parallel 2$		$\Omega \parallel \text{pF}$
Common-Mode			$10^{13} \parallel 6$		$\Omega \parallel \text{pF}$
OPEN-LOOP GAIN⁽¹⁾					
Open-Loop Voltage Gain	$V_O = \pm 10\text{V}, R_L = 2\text{k}\Omega$	90	126		dB
FREQUENCY RESPONSE					
Slew Rate			10		$\text{V}/\mu\text{s}$
Gain-Bandwidth Product	$G = +1$		4		MHz
Total Harmonic Distortion	$G = +1, f = 1\text{kHz}$		0.1		%
OUTPUT⁽¹⁾					
Voltage Output	$R_L = 2\text{k}\Omega$	± 11	± 11.5		V
Short Circuit Current			± 25		mA

(1) Specifications apply to uncommitted op amp, A_4 . The three op amps forming the filter are identical to A_4 but are tested as a complete filter.

ELECTRICAL CHARACTERISTICS (continued)

At $T_A = +25^\circ\text{C}$, and $V_S = \pm 15\text{V}$, unless otherwise noted.

PARAMETER	CONDITIONS	UAF42AP, AU			UNIT
		MIN	TYP	MAX	
POWER SUPPLY					
Specified Operating Voltage			± 15		V
Operating Voltage Range		± 6		± 18	V
Current			± 6	± 7	mA
TEMPERATURE RANGE					
Specified		-25		+85	$^\circ\text{C}$
Operating		-25		+85	$^\circ\text{C}$
Storage		-40		+125	$^\circ\text{C}$
Thermal Resistance, θ_{JA}			100		$^\circ\text{C}/\text{W}$

APPLICATION INFORMATION

The UAF42 is a monolithic implementation of the proven state-variable analog filter topology. This device is pin-compatible with the popular UAF41 analog filter, and it provides several improvements.

The slew rate of the UAF42 has been increased to 10V/μs, versus 1.6V/μs for the UAF41. Frequency • Q product of the UAF42 has been improved, and the useful natural frequency extended by a factor of four to 100kHz. FET input op amps on the UAF42 provide very low input bias current. The monolithic construction of the UAF42 provides lower cost and improved reliability.

DESIGN PROGRAM

Application report [SBFA002](#) (available for download at www.ti.com) and a computer-aided design program also available from Texas Instruments, make it easy to design and implement many kinds of active filters. The DOS-compatible program guides you through the design process and automatically calculates component values.

Low-pass, high-pass, band-pass and band-reject (notch) filters can be designed. The program supports the three most commonly-used all-pole filter types: Butterworth, Chebyshev and Bessel. The less-familiar inverse Chebyshev is also supported, providing a smooth passband response with ripple in the stop band.

With each data entry, the program automatically calculates and displays filter performance. This feature allows a spreadsheet-like *what-if* design approach. For example, a user can quickly determine, by trial and error, how many poles are required for a desired attenuation in the stopband. Gain/phase plots may be viewed for any response type.

The basic building element of the most commonly-used filter types is the second-order section. This section provides a complex-conjugate pair of poles. The natural frequency, ω_n , and Q of the pole pair determine the characteristic response of the section. The low-pass transfer function is shown in [Equation 1](#):

$$\frac{V_O(s)}{V_I(s)} = \frac{A_{LP}\omega_n^2}{s^2 + s\omega_n/Q + \omega_n^2} \quad (1)$$

The high-pass transfer function is given by [Equation 2](#):

$$\frac{V_{HP}(s)}{V_I(s)} = \frac{A_{HP}s^2}{s^2 + s\omega_n/Q + \omega_n^2} \quad (2)$$

The band-pass transfer function is calculated using [Equation 3](#):

$$\frac{V_{BP}(s)}{V_I(s)} = \frac{A_{BP}(\omega_n/Q) s}{s^2 + s\omega_n/Q + \omega_n^2} \quad (3)$$

A band-reject response is obtained by summing the low-pass and high-pass outputs, yielding the transfer function shown in [Equation 4](#):

$$\frac{V_{BR}(s)}{V_I(s)} = \frac{A_{BR}(s^2 + \omega_n^2)}{s^2 + s\omega_n/Q + \omega_n^2} \quad (4)$$

The most common filter types are formed with one or more cascaded second-order sections. Each section is designed for ω_n and Q according to the filter type (Butterworth, Bessel, Chebyshev, etc.) and cutoff frequency. While tabulated data can be found in virtually any filter design text, the design program eliminates this tedious procedure.

Second-order sections may be noninverting ([Figure 1](#)) or inverting ([Figure 2](#)). Design equations for these two basic configurations are shown for reference. The design program solves these equations, providing complete results, including component values.

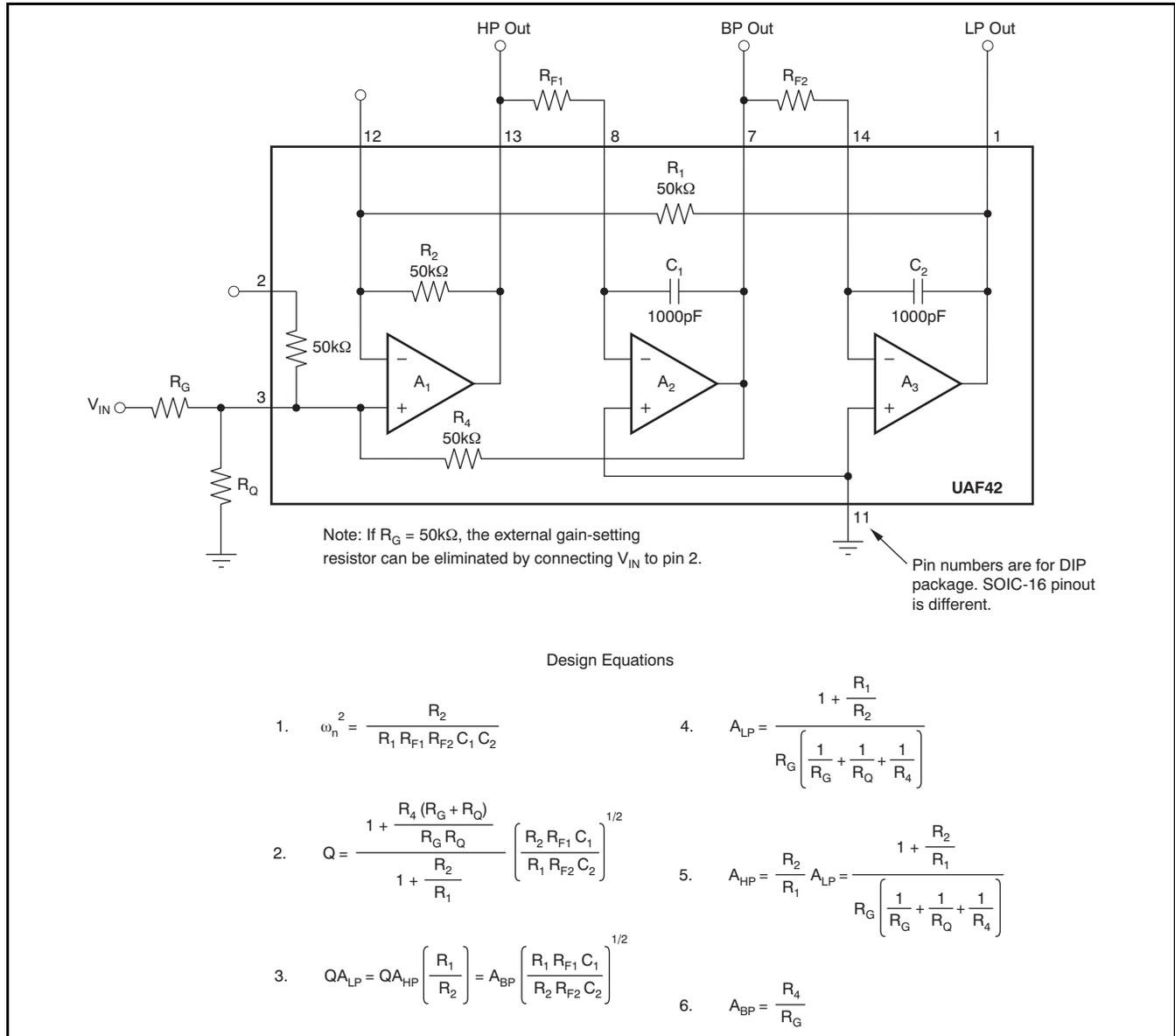


Figure 1. Noninverting Pole-Pair

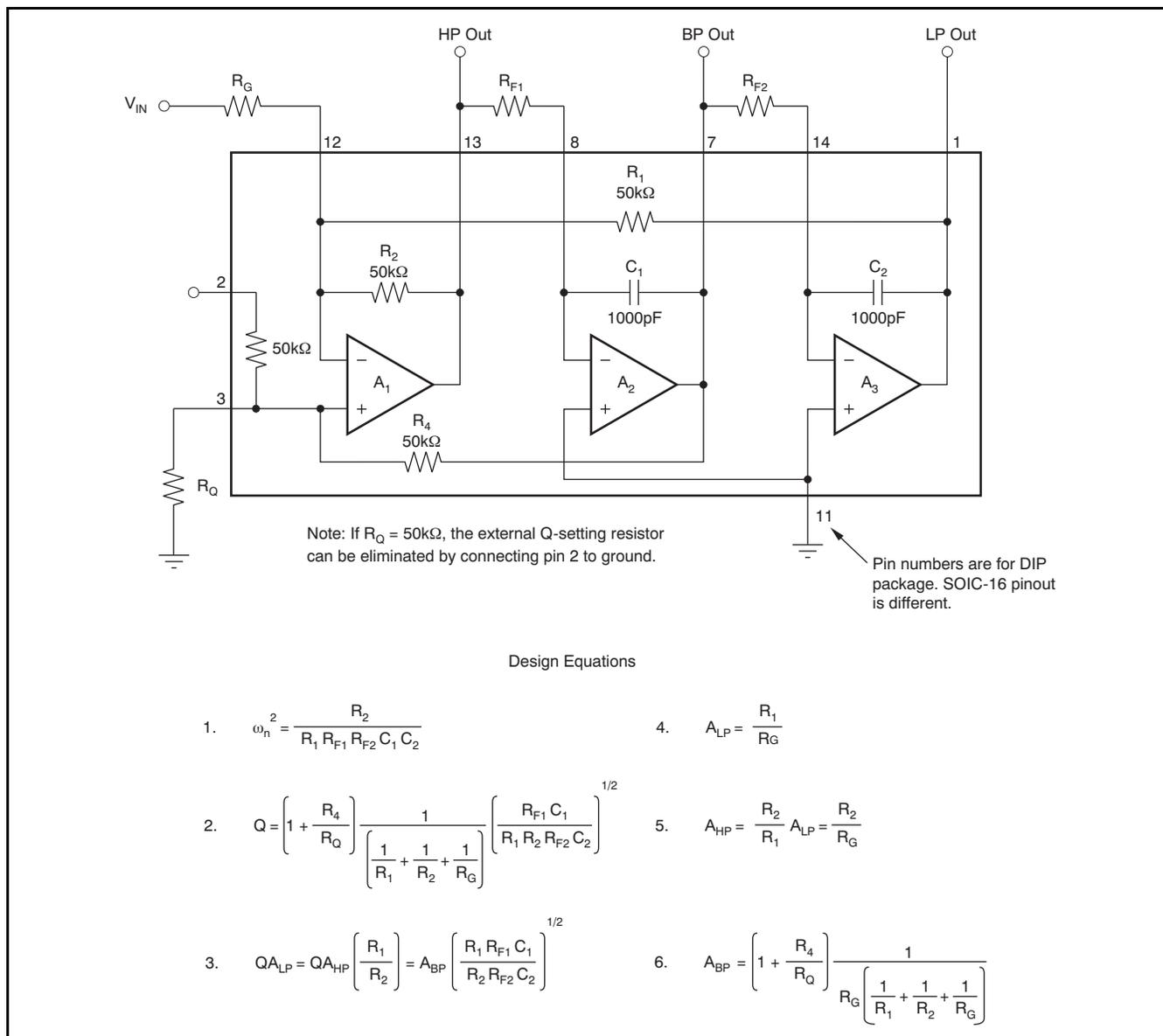


Figure 2. Inverting Pole-Pair

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
UAF42AP	ACTIVE	PDIP	N	14	25	Green (RoHS & no Sb/Br)	CU NIPDAU	N / A for Pkg Type
UAF42AP-1	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI
UAF42APG4	ACTIVE	PDIP	N	14	25	Green (RoHS & no Sb/Br)	CU NIPDAU	N / A for Pkg Type
UAF42AU	ACTIVE	SOIC	DW	16	48	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR
UAF42AU-1	OBSOLETE	SOIC	DW	16		TBD	Call TI	Call TI
UAF42AUE4	ACTIVE	SOIC	DW	16	48	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

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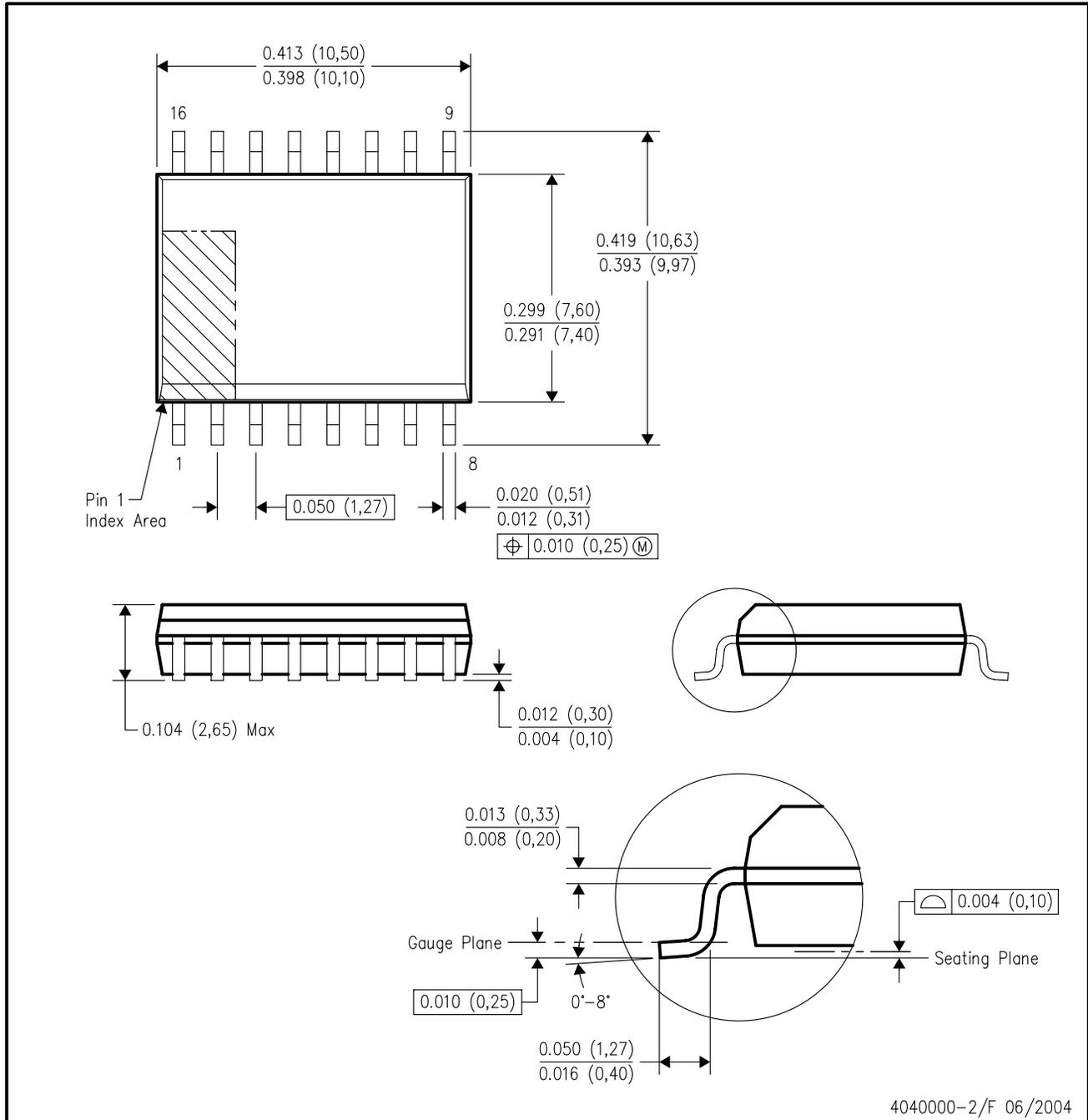
⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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DW (R-PDSO-G16)

PLASTIC SMALL-OUTLINE PACKAGE



4040000-2/F 06/2004

- NOTES:
- A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
 - D. Falls within JEDEC MS-013 variation AA.

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