UC3175B

UNITRODE

Full-Bridge Power Amplifier

FEATURES

- Precision Current Control
- ±800mA Load Current
- 1.25V Total Vsat at 800mA
- Controlled Velocity Head Parking
- Precision Dual Supply Monitor with Indicator
- Limit Input to Force Output Extremes
- Inhibit Input and UVLO
- 4V to 15V operation

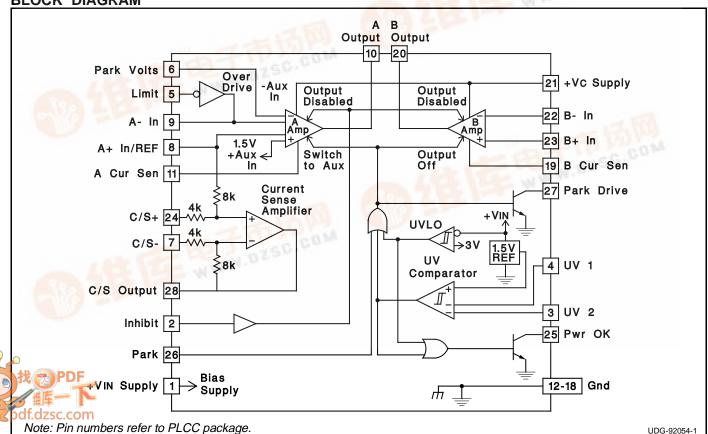
DESCRIPTION

This full-bridge power amplifier is rated for continuous output current of 0.8 Amperes and is intended for use in demanding servo applications such as head positioning for high-density disk drives. The device includes a precision current sense amplifier that provides accurate control of load current. Current is sensed with a single resistor in series with the load. The power amplifier has a very low output saturation voltage and will operate down to 4V supply levels. Power output stage protection includes current limiting and thermal shutdown.

Auxiliary functions on this device include a dual-input under-voltage comparator, which can monitor two independent supply voltages and force a built-in head park function when either is below minimum. When activated by either the UV comparator, or a command at the separate PARK input, the park circuitry will override the amplifier inputs to convert the power outputs to a programmable constant voltage source which will hold regulation as the supply voltage falls to below 3.0 Volts. Added features include a POWER OK flag output, a LIMIT input to force the drive output to its maximum level in either polarity, and a over-riding INHIBIT input to disable all amplifiers and reduce quiescent supply current.

This device is packaged in a power PLCC surface mount configuration which maintains a standard 28-pin outline, but with 7 pins along one edge allocated to ground for optimum thermal transfer. And is also available in a 24-pin surface mount SOIC package.

BLOCK DIAGRAM



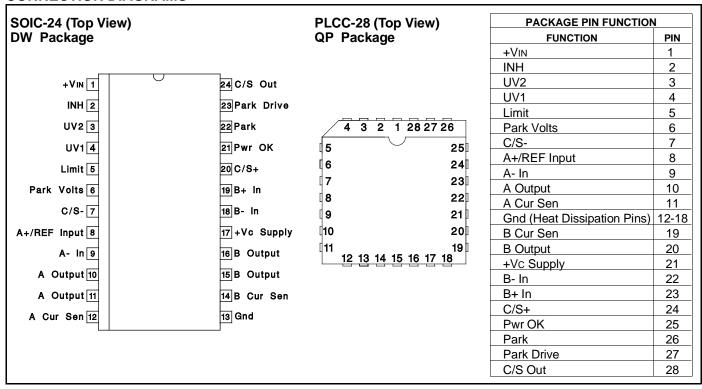
ABSOLUTE MAXIMUM RATINGS

Input Supply Voltage, (+VIN,+Vc)
UV Comparator, and Digital Inputs
Maximum forced voltage0.3V to 10V
Maximum forced current
C/S Inputs
Maximum forced voltage0.3V to 20V
A and B Amplifier Inputs0.3V to +VIN
Open Collector Output Voltages
A and B Output Currents (continuous)
Source Internally Limited
Source Internally Limited Sink
Sink
Sink
Sink
Sink 1.0A Parking Drive Output Current 150mA Continuous 150mA Pulsed 1A Output Diode Current (pulsed) 1A
Sink 1.0A Parking Drive Output Current 150mA Continuous 150mA Pulsed 1A Output Diode Current (pulsed) 1A Power OK Output Current(continuous) 30mA
Sink 1.0A Parking Drive Output Current 150mA Continuous 150mA Pulsed 1A Output Diode Current (pulsed) 1A

Note 1: Unless otherwise indicated, voltages are referenced to ground and currents are positive into, negative out of, the specified terminals. "Pulsed" is defined as a less than 10% duty cycle pulse with a maximum duration of 500µs. Note 2: See Unitrode Integrated Circuits databook for information regarding thermal specifications and limitations of packages.

Thermal Data QP Package:

CONNECTION DIAGRAMS



ELECTRICAL CHARACTERISTICS: Unless otherwise stated specifications apply for 0°C ≤ TA ≤ 70°C, +VIN = 12V, +VC = +VIN. A+/REF Input = 6V. TA=TJ.

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
INPUT SUPPLY		•			
+Vın Supply Current	All Amplifier Outputs = 6V		35	42	mA
+Vc Supply Current	IOUT = 0A		1		mA
+VIN UVLO Threshold	Low to High		2.8	3.0	V
UVLO Threshold Hysteresis			200		mV

ELECTRICAL Unless otherwise stated specifications apply for $0^{\circ}C \le TA \le 70^{\circ}C$, +VIN = 12V, +Vc = +VIN, A+/REF INPUT = 6V. TA=TJ.

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
UNDER VOLTAGE (UV) COMPARATOR					
Input Bias Current		-1.5	-0.5		μΑ
UV Thresholds	Low to High, Other Input = 5V	1.48	1.50	1.52	V
UV Threshold Hysteresis		15	25	40	mV
Pwr OK Vsat	IOUT = 5mA			0.45	V
Pwr OK Leakage	Vout = 20V			5	μΑ
POWER AMPLIFIERS A and B					
Input Offset Voltage	VcM = 6V, A Amplifier			8	mV
	B Amplifier			12	mV
Input Offset Drift	Note 1, A Amplifier Only			25	μV/°C
Input Bias Current	VcM = 6V, except A+/REF Input	-500	-150		nA
Input Offset Current	VCM = 6V, B Amplifier Only			200	nA
Input Bias Current at A+/Ref Input	$(A+/Ref-C/S+)/12k$, $T_J = 25$ °C	60	84	105	μΑ/V
CMRR	1V ≤ Vcm ≤ 10V	70	90		dB
PSRR	+VIN = 4V to 15V, VCM = 1.5V	70	90		dB
Large Signal Voltage Gain	VOUT = 1V, Sinking 500mA to VOUT = 11V,				
	Sourcing 500mA	3.0	15.0		V/mV
Slew Rate	1 to 13V, 13 to 1V, T _J = 25°C		1	2.1	V/μs
Unity Gain Bandwidth	Note 1, A Amplifier		2		MHz
	Note 1, B Amplifier		1		MHz
High-Side Current Limit		0.8	1.0		Α
Output Saturation Voltage	High-Side, ISOURCE = 250mA		0.7		V
	High-Side, ISOURCE = 800mA		0.85		V
	Low-Side, Isink = 250mA		0.3		V
	Low-Side, Isink = 800mA		0.4		V
	Total, Iout = 250mA		1.0	1.2	V
	Total, Iout = 800mA		1.25	1.6	V
High Side Diode VF	ID = 800mA, Inhibit Activated		1.0		V
Low Side Diode VF	ID = 800mA, Inhibit Activated		1.0		V
CURRENT SENSE AMPLIFIER		•			•
Input Offset Voltage	Vcm = 6V			2.0	mV
Input Offset Change with Common Mode	0V ≤ VcM ≤ 12V				
Input				1500	μV/V
Input Offset Drift	Note 1			8	μV/°C
Voltage Gain	-1.0V ≤ VDIFF ≤ +1.0V, VCM = 6V	1.95	2.00	2.05	V
Output Saturation Voltage	Low-Side, Isink = 1.5mA		0.3	0.5	V
·	High-Side, ISOURCE = 1.5mA		0.4	0.7	V
Maximum A+/Ref Input	Volts Below +VIN, C/S+ & C/S- = BOUTPUT Max @				
•	10mA Output Current, +VIN = 4.5V, C/S VIO ≤ 5mV		2.6	3.0	V
PARKING FUNCTION					
Park Input Threshold		0.7	1.1	1.7	V
Park Input Current	Park Input = 1.7V		60	100	μΑ
Park Drive Saturation Voltage, PDvsAT	ISINK = 100mA		0.3	0.5	V
Parking Drive Leakage	Vout = 20V			100	μΑ
Amplifier A Aux Input Bias Current		-500	-150		nA

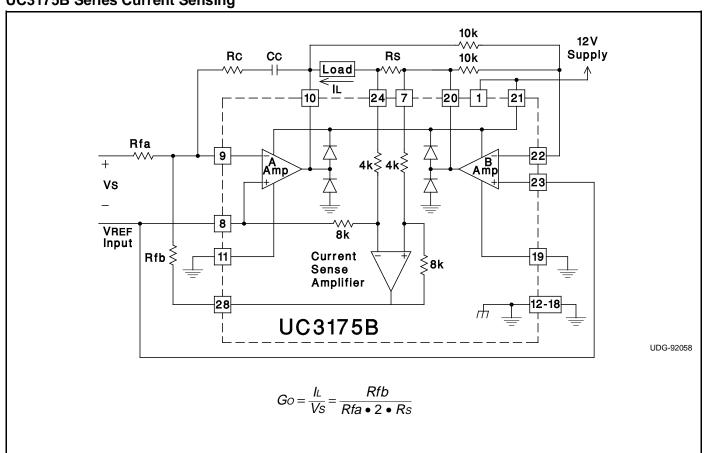
ELECTRICAL CHARACTERISTICS (cont.)

Unless otherwise stated specifications apply for $0^{\circ}C \le TA \le 70^{\circ}C$, +VIN = 12V, +Vc = +VIN, A+/REF Input = 6V. TA=TJ.

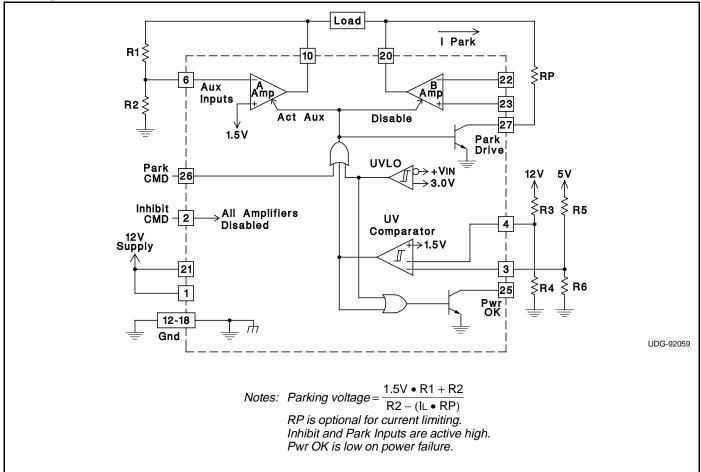
PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
PARKING FUNCTIONS (cont.)					_
Amplifier A Saturation Voltage, AHVSAT	ISOURCE = 50mA, +VIN = 3V		0.65	0.8	V
Regulating Voltage at Park Volts		1.47	1.50	1.53	V
Minimum Parking Supply Voltage	AHVSAT + PDVSAT ≤ 1.3V @ 50mA		1.7	1.9	V
AUXILIARY FUNCTIONS					
Limit Input Low Voltage	A Output Forced Low	0.7	8.0		V
Limit Input High Voltage	A Output Forced High		2.2	2.3	V
Limit Inactive		1.2		1.8	V
Limit Open Circuit Voltage		1.45	1.50	1.55	V
Limit Input Resistance	1.2V ≤ Limit Input ≤ 1.8V		10		kΩ
Inhibit Input Threshold		0.7	1.1	1.7	V
Inhibit Input Current	Inhibit Input = 1.7V		400	700	μΑ
Supply Current when Inhibited	The sum of +Vin and +Vc currents		2	6	mA
Thermal Shutdown Temperature			165		°C

Note 1: This specification not tested in production.

UC3175B Series Current Sensing







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