UMH8N / IMH8A

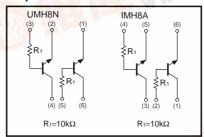
Transistors

General purpose (dual digital transistors) UMH8N / IMH8A

Features

 Two DTC114T chips in a EMT or UMT or SMT package.

Equivalent circuits



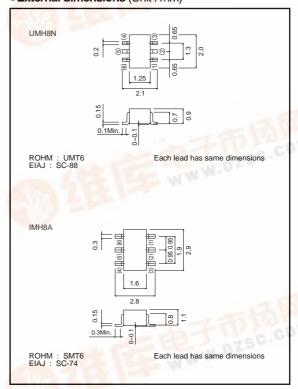
Package, marking, and packaging specifications

Туре	UMH8N	IMH8A	
Package	UMT6	SMT6	
Marking	H8	H8	
Code	TR	T108	
Basic ordering unit (pieces)	3000	3000	

● **Absolute maximum ratings** (Ta=25°C)

Parameter		Symbol	Limits	Unit
Collector-base voltage		Vсво	50	V
Collector-emitter voltage		Vceo	50	V
Emitter-base voltage		VEBO	5	V
Collector current		lc	100	mA
Power dissipation	UMH8N	Pd	150(TOTAL)	mW *1
	IMH8A	7 '"	300(TOTAL)	*2
Junction temperature		Tj	150	°C
Storage temperature		Tstg	Tstg -55 to +150	

●External dimensions (Unit: mm)



● Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	-	W =	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	50	-	_	V	Ic=1mA
Emitter-base breakdown voltage	ВУЕВО	5	-	_	V	Iε=50μA
Collector cutoff current	Ісво	-	-	0.5	μΑ	Vcb=50V
Emitter cutoff current	Ієво	-	-	0.5	μΑ	V _{EB} =4V
Collector-emitter saturation voltage	VCE(sat)	_	-	0.3	V	Ic/I _B =10mA/1mA
DC current transfer ratio	hre	100	250	600	-	Vce=5V, Ic=1mA
Transition frequency	f⊤	_	250	-	MHz	VcE=10V, IE=-5mA, f=100MHz *
Input resistance	R ₁	7	10	13	kΩ	_

^{*}Transition frequency of the device.



•Electrical characteristics curves

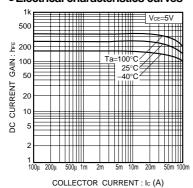


Fig.1 DC current gain vs. collector current

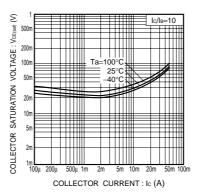


Fig.2 Collector-emitter saturation voltage vs. collector current

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