

**N-CHANNEL MOS FIELD EFFECT TRANSISTOR  
FOR SWITCHING****DESCRIPTION**

The  $\mu$ PA1902 is a switching device, which can be driven directly by a 4.5 V power source.

This  $\mu$ PA1902 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power management switch of portable machine and so on.

**FEATURES**

- 4.5 V drive available
- Low on-state resistance

$R_{DS(on)1} = 17 \text{ m}\Omega \text{ TYP. } (V_{GS} = 10 \text{ V, } I_D = 3.5 \text{ A})$   
 $R_{DS(on)2} = 22 \text{ m}\Omega \text{ TYP. } (V_{GS} = 4.5 \text{ V, } I_D = 3.5 \text{ A})$

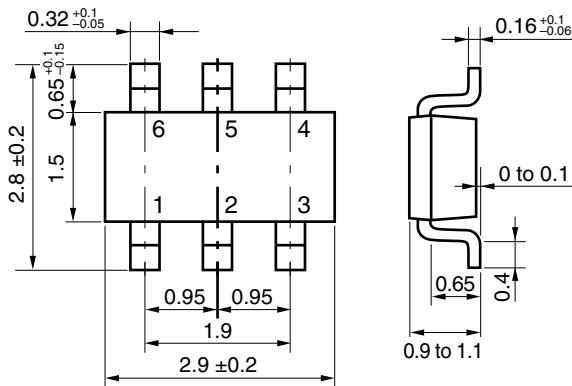
**ORDERING INFORMATION**

PART NUMBER	PACKAGE
$\mu$ PA1902TE	SC-95 (Mini Mold Thin Type)

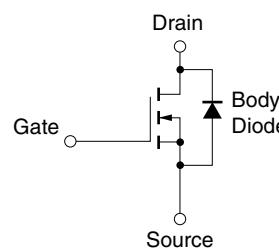
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**ABSOLUTE MAXIMUM RATINGS (TA = 25°C)**

Drain to Source Voltage ( $V_{GS} = 0 \text{ V}$ )	$V_{DSS}$	30	V
Gate to Source Voltage ( $V_{DS} = 0 \text{ V}$ )	$V_{GSS}$	$\pm 20$	V
Drain Current (DC)	$I_{D(\text{DC})}$	$\pm 7.0$	A
Drain Current (pulse) <sup>Note1</sup>	$I_{D(\text{pulse})}$	$\pm 28$	A
Total Power Dissipation	$P_{T1}$	0.2	W
Total Power Dissipation <sup>Note2</sup>	$P_{T2}$	2.0	W
Channel Temperature	$T_{ch}$	150	°C
Storage Temperature	$T_{stg}$	-55 to +150	°C

**PACKAGE DRAWING (Unit: mm)**

1, 2, 5, 6: Drain  
 3 : Gate  
 4 : Source

**EQUIVALENT CIRCUIT**

**Notes 1.** PW  $\leq 10 \mu\text{s}$ , Duty Cycle  $\leq 1\%$

2. Mounted on FR-4 board of 50 mm x 50 mm x 1.6 mm, t  $\leq 5 \text{ sec.}$

**Remark** Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

**Caution** This product is electrostatic-sensitive device due to low ESD capability and should be handled with caution for electrostatic discharge. (It does not have built-in G-S protection diode.)

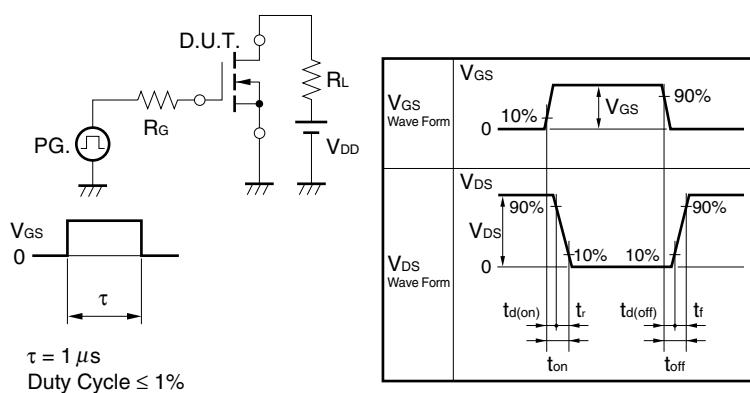
When this product actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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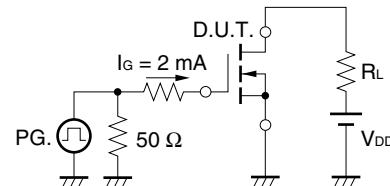
ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ C$ )

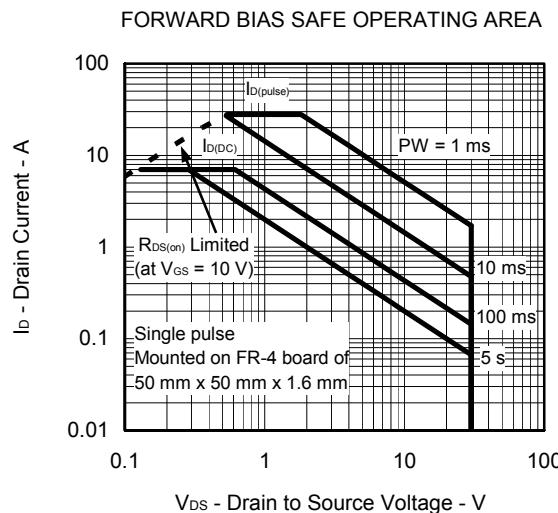
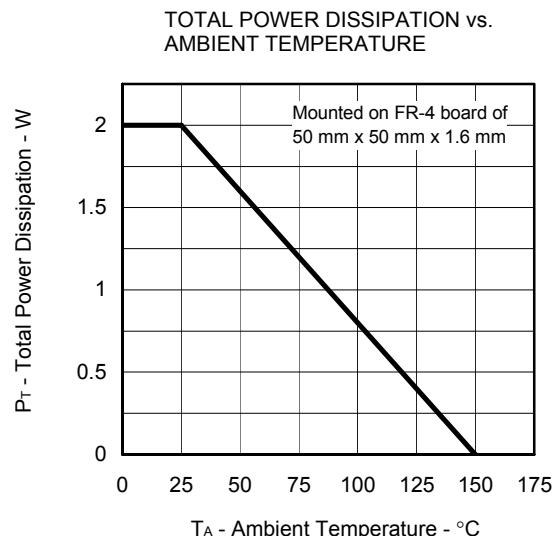
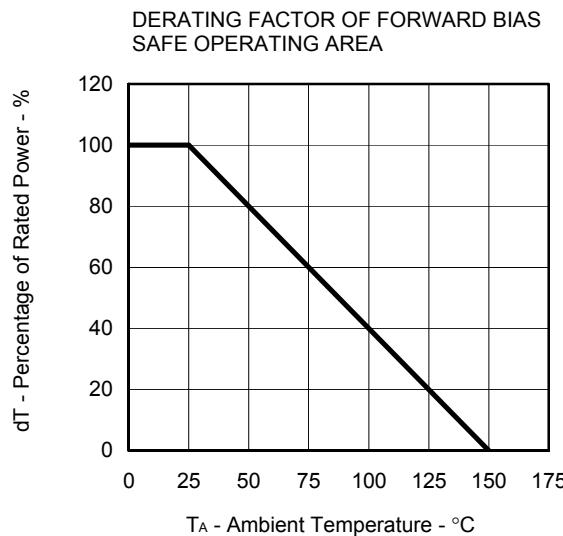
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30 V, V_{GS} = 0 V$			1.0	$\mu A$
Gate Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20 V, V_{DS} = 0 V$			$\pm 100$	nA
Gate Cut-off Voltage	$V_{GS(off)}$	$V_{DS} = 10 V, I_D = 1.0 \text{ mA}$	1.5	2.0	2.5	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = 10 V, I_D = 3.5 \text{ A}$	3.0			S
Drain to Source On-state Resistance	$R_{DS(on)1}$	$V_{GS} = 10 V, I_D = 3.5 \text{ A}$		17	22	$m\Omega$
	$R_{DS(on)2}$	$V_{GS} = 4.5 V, I_D = 3.5 \text{ A}$		22	30	$m\Omega$
Input Capacitance	$C_{iss}$	$V_{DS} = 10 V$ $V_{GS} = 0 V$ $f = 1 \text{ MHz}$		780		pF
Output Capacitance	$C_{oss}$			180		pF
Reverse Transfer Capacitance	$C_{rss}$			120		pF
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 15 V, I_D = 1.0 \text{ A}$ $V_{GS} = 10 V$ $R_G = 6.0 \Omega$		16		ns
Rise Time	$t_r$			10		ns
Turn-off Delay Time	$t_{d(off)}$			108		ns
Fall Time	$t_f$			56		ns
Total Gate Charge	$Q_G$	$V_{DD} = 15 V$ $V_{GS} = 5.0 V$ $I_D = 7.0 \text{ A}$		8.0		nC
Gate to Source Charge	$Q_{GS}$			2.7		nC
Gate to Drain Charge	$Q_{GD}$			3.4		nC
Body Diode Forward Voltage	$V_{F(S-D)}$	$I_F = 7.0 \text{ A}, V_{GS} = 0 V$		0.84		V

## TEST CIRCUIT 1 SWITCHING TIME

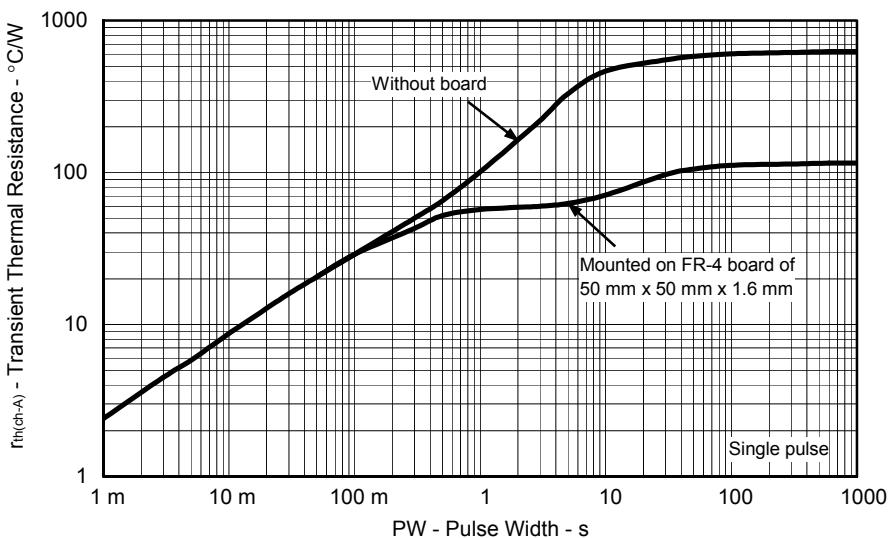


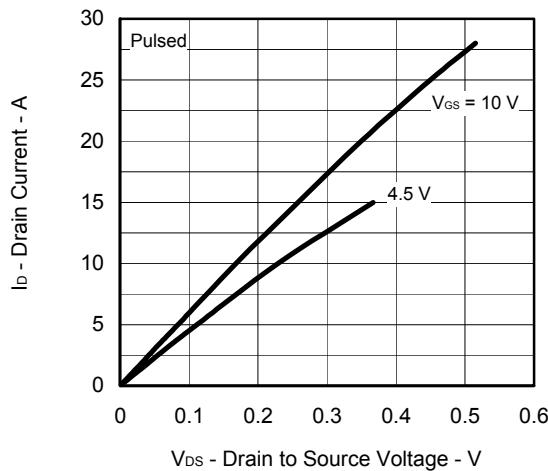
## TEST CIRCUIT 2 GATE CHARGE



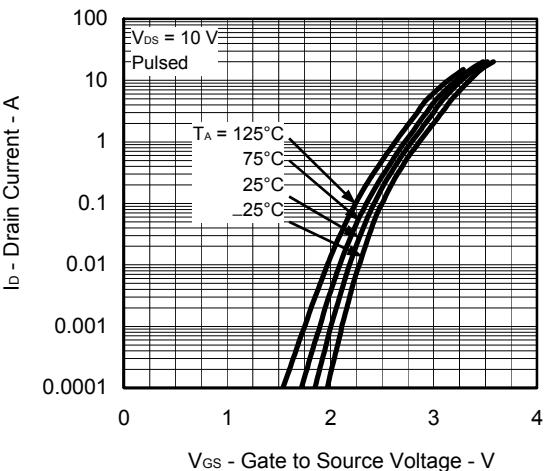
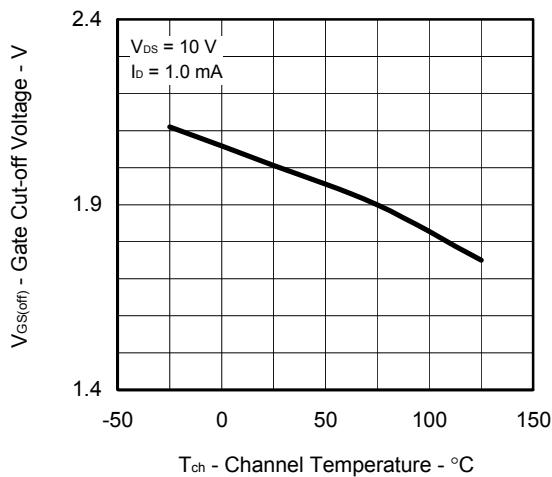
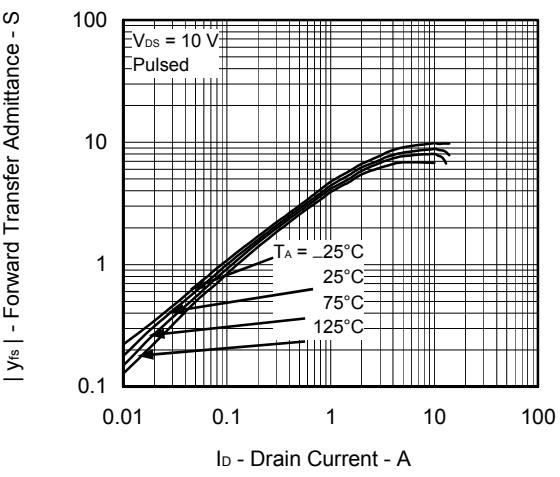
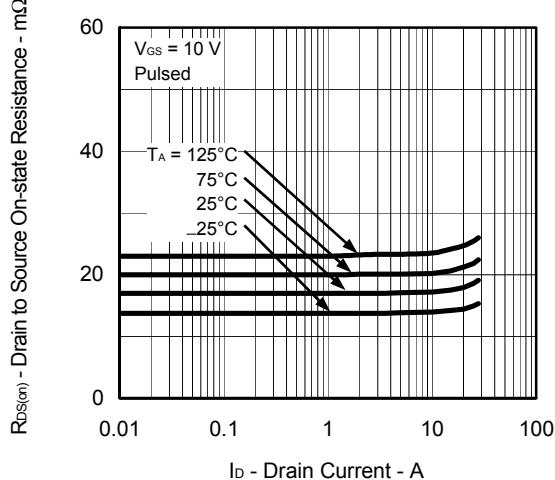
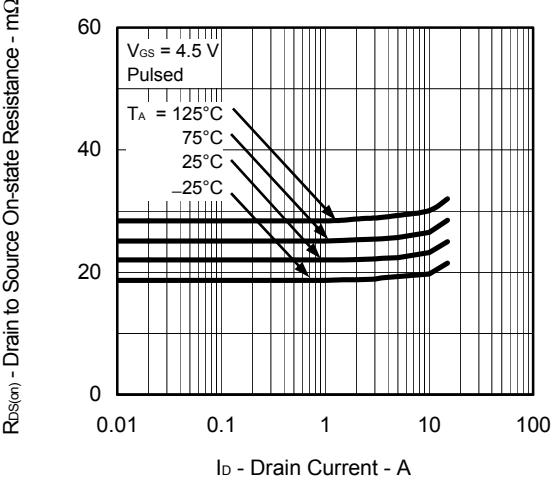
TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ )

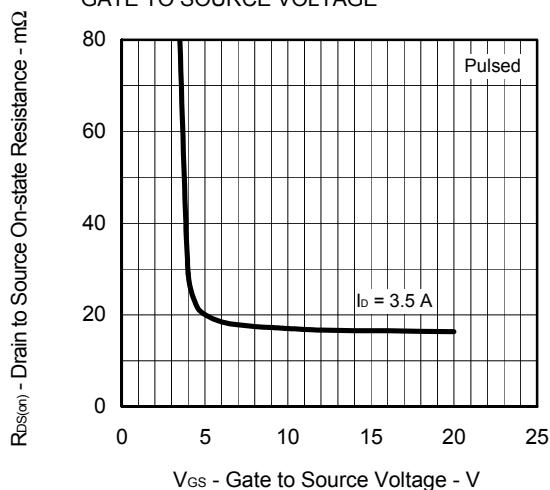
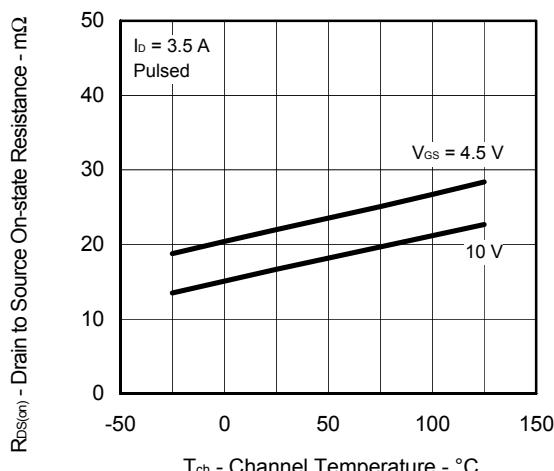
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



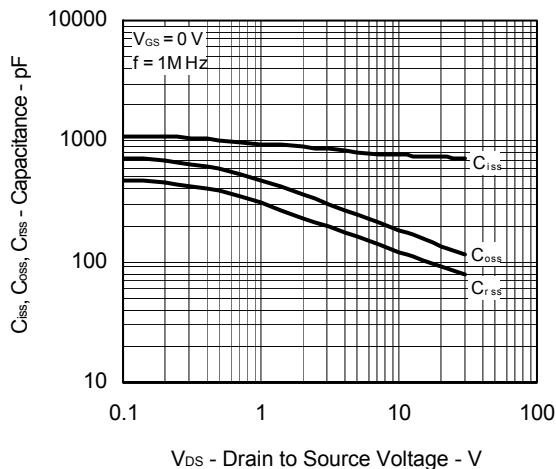
DRAIN CURRENT vs.  
DRAIN TO SOURCE VOLTAGE

FORWARD TRANSFER CHARACTERISTICS

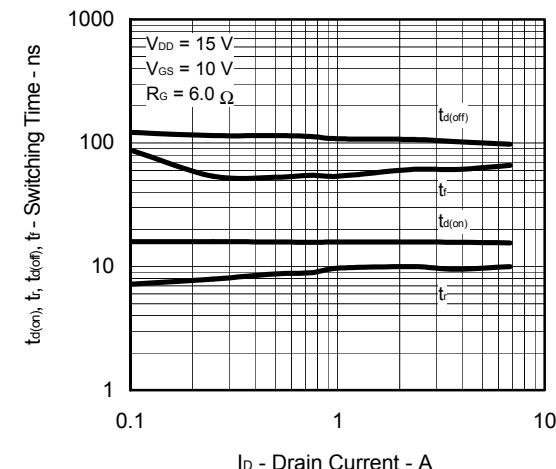
GATE CUT-OFF VOLTAGE vs.  
CHANNEL TEMPERATUREFORWARD TRANSFER ADMITTANCE vs.  
DRAIN CURRENTDRAIN TO SOURCE ON-STATE RESISTANCE vs.  
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DRAIN CURRENT

DRAIN TO SOURCE ON-STATE RESISTANCE vs.  
GATE TO SOURCE VOLTAGEDRAIN TO SOURCE ON-STATE RESISTANCE vs.  
CHANNEL TEMPERATURE

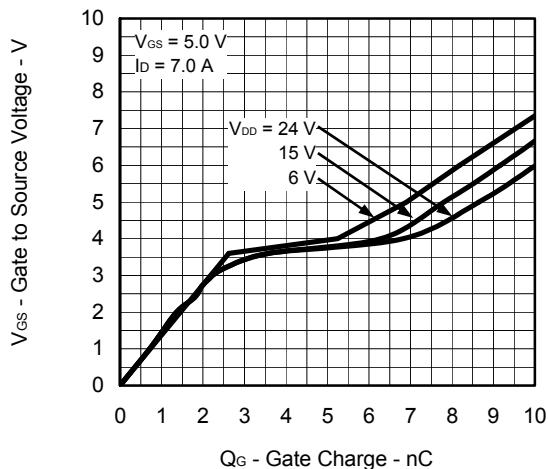
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



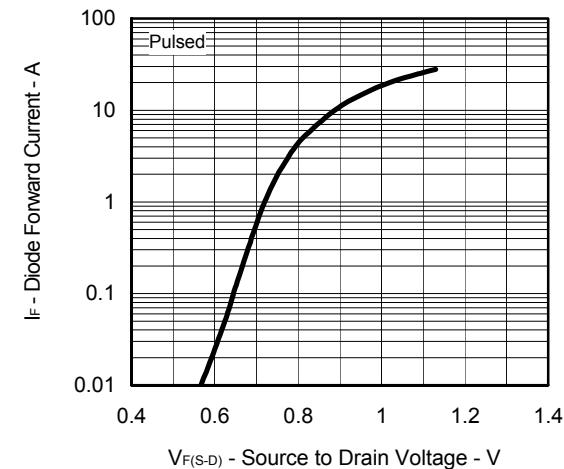
SWITCHING CHARACTERISTICS



DYNAMIC INPUT/OUTPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



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