LINEAR INTEGRATED CIRCUIT UTC 51494

DESCRIPTION

The UTC 51494 is a monolithic bipolar integrate circuit that provides both 494 function and built in power good signal circuit for easy using 51494 can be easily implemented by just adding a capacitor.

FEATURES

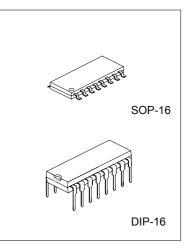
*Fully integrated with compact 16-pin dip

*All necessary functions included for most popular half bridge circuit.

*Built-in power good delay and power fail lead function. *Power good delay time is linearly.

*Proportional to external capacitor value.

- *Reduced external components for cost down and
- components for cost down and compact size.



MAXIMUM RATINGS(Unless otherwise noted ,all is over operating free-air temperature Range)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	42	V
Voltage from any pin to ground (except pin8 & pin11)	Vin	Vcc+0.3	V
Collector Output Voltage	Vc1, Vc2	42	V
Peak Collector Output	lc1, lc2	250	mA
Power dissipation	PD	1500	mW
Operating Temperature Range	Topr	0 to +70	°C
Storage Temperature Range	Tsag	-65 to +150	°C

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ELECTRICAL CHARACTERISTICS

(Unless otherwise specified	, Ta=0~70°C ,Vcc=15V,f=10kHz,)
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(Unless otherwise specified, 1a	a=0~70°C,V	/CC=15V,f=10KHZ,)				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reference Section						
Reference voltage	Vref	Iref =1.0mA	4.75	5	5.25	V
Line Regulation	Vline	7V <vcc<40v< td=""><td></td><td>2</td><td>25</td><td>mV</td></vcc<40v<>		2	25	mV
Load Regulation	Viload	1mA <iref<5ma< td=""><td></td><td>1</td><td>15</td><td>mV</td></iref<5ma<>		1	15	mV
Temperature Coefficient		0°C <fa<70°c< td=""><td></td><td>0.01</td><td>0.03</td><td>%/°C</td></fa<70°c<>		0.01	0.03	%/°C
Oscillator section						
Oscillator Frequency	Fosc	CT=0.01μF,RT=12kΩ		10		kHz
Oscillator Frequency Change	∆fosc	CT=0.01μF,RT=12kΩ			2	%
Over Operating Temperature						
Range						
Dead Time Control Section	r			-		1
Input Bias Current (Pin 4)	lib(dt)	Vcc =15V ; 0V <v4<5.25v< td=""><td></td><td>-2</td><td>-10</td><td>μA</td></v4<5.25v<>		-2	-10	μA
Maximum Duty cycle,	Dc(max)	Vcc = 15V; Pin 4 = 0V	43		45	%
Each output		Output Control Pin = Vref		ļ		<u> </u>
Input Threshold Voltage :	Vth					
Zero Duty				3	3.3	V
Max Duty			0			
Error Amplifier Section	r	I		r –	1	
Input offset Voltage	Vics	V3=2.5V	_	2	10	mV
Input offset Current	lics	V3=2.5V		25	250	nA
Input bias Current	lib	V3=2.5V		0.2	1	μA
Input Common-mode Voltage	Vicr	7V <vcc< 40v<="" td=""><td>-0.3</td><td></td><td>Vcc</td><td>V</td></vcc<>	-0.3		Vcc	V
Range			_			
Large Signal Open-Loop Voltage	Gvo	0.5V < V3 < 3.5V	60	74		dB
Range						
Unity-Gain Band width	fc			650		kHz
Output Section						
Collector off-state current	Ic(off)	Vcc=Vc=40V,VE=0		2	100	μA
Emitter off-state Current	le(off)	VCC=VC=40V,VE=0			-100	μA
Output Saturation Voltage	Vce(sat)	VE=15V,Lc=200mA		1.1	1.3	V
Common-Emitter						
Output Control (pin13)			-			
Standby Power Supply Current	lcc			6	10	mA
Output AC Characteristic						1
Raise Time	Tr			100	200	ns
Common-Emitter	-			6-	465	
Fall Time	Tr			25	100	ns
Common-Emitter	L			L		
PWM Comparator Section	1/41- !	Zana Dutu aval-		4	45	
Inhibit Threshold Voltage	Vthi	Zero Duty cycle	0	4	4.5	V
Output Source Current	lo+	0.5V < V3 < 3.5V	2	0.0		mA
Output Sink Current	lo-	0.5V < V3 < 3.5V	-0.2	-0.6		mA

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PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		TYP	MAX	UNIT	
Power Good Section							
Power Good Delay Time	tpd	Cd = 1µ	230	280	330	ms	
		Cd = 0.47µ	108	130	160		
Power Fail Lead Time	Tp1			4		ms	
Output High Voltage	Voh	Vpinn = 5V, IL = 1mA	4.75			V	
Output Saturation Voltage	Vsat	Vpinn = 5V, Isink = 4mA			0.4	V	
Output Leakage Current	loh				100	μA	

PIN DESCRIPTION

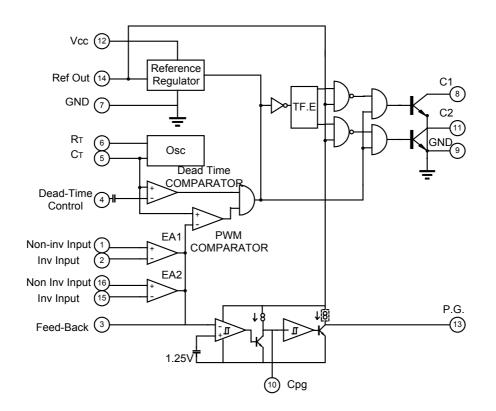
PIN	NAME	FUNCTION
1	EA1+	Error amplifier noninverting input, same as pin 1 of 494
2	EA1-	Error amplifier inverting input, same as pin 2 of 494
3	EA0	Error amplifier output and feedback, same as pin 3 of 494
4	DT	Dead time control input, same as pin 4 of 494
5	CT	Connect capacitor to oscillator circuit for operating frequency, same as pin 5 of 494
6	RT	Connect resistor to oscillator circuit for operating frequency, same as pin 6 of 494
7	GND	Ground terminal of IC, same as pin 7 of 494
8	C1	Collector of output transistor one, same as pin 8 of 494
9	GND	Ground terminal of IC
10	Cpg	Terminal for capacitor to determine power good delay time
11	C2	Collector of output transistor two, same as pin 11 of 494
12	Vcc	Supply voltage, same as pin 12 of 494
13	P.G.	Output for power good signal
14	Vref	Reference voltage output, same as pin 14 of 494
15	EA2-	Error amplifier inverting input, same as pin 15 of 494
16	EA+	Error amplifier noninverting input, same as pin 16 of 494

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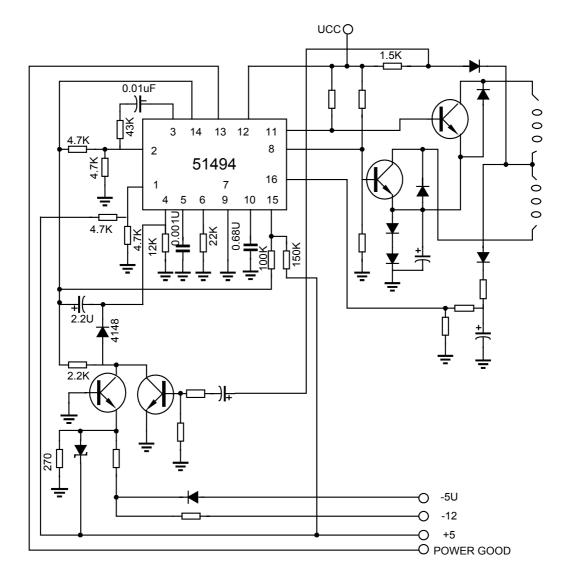
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BLOCK DIAGRAM



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