

UTC TA7368P LINEAR INTEGRATED CIRCUIT

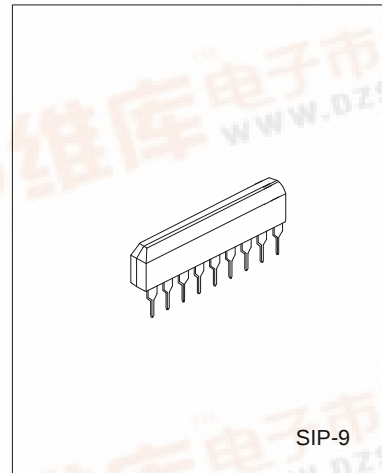
0.6W AUDIO POWER AMPLIFIER

DESCRIPTION

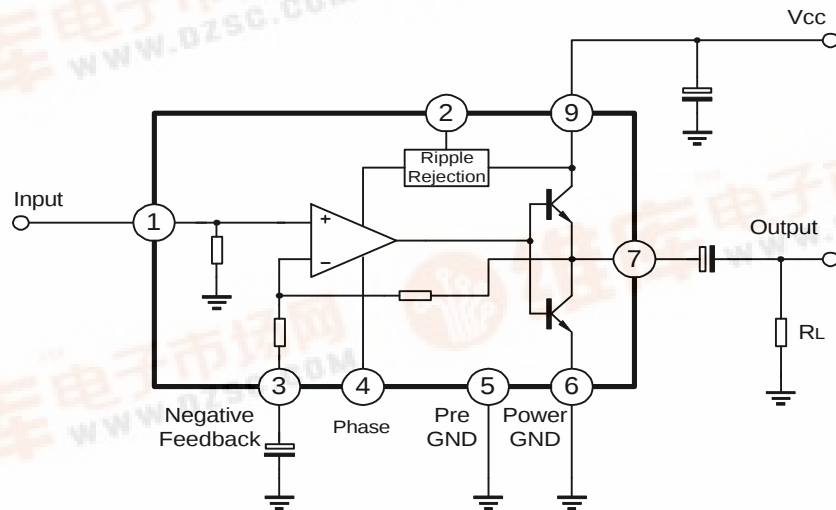
The UTC TA7368P is suitable for the audio power amplifier of portable cassette tape recorder and radio.

FEATURES

- *Very few external components(only three capacitors)
- *Low quiescent current
- *High voltage gain: $G_v=40\text{dB}$



BLOCK DIAGRAM



UTC UNISONIC TECHNOLOGIES CO., LTD.

1

QW-R107-009,A

UTC TA7368P LINEAR INTEGRATED CIRCUIT

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V _{CC}	14	V
Power Dissipation	P _D	900	mW
Operating Temperature	T _{OPR}	-20 ~ 75	°C
Storage Temperature	T _{STG}	-55 ~ 150	°C

Note: Derated Ta>25°C, in the proportion of 7.2mW/°C

ELECTRONIC CHARACTERISTICS (Ta=25°C, V_{CC}=6V, f=1kHz, R_g=600Ω, R_L=4Ω, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Quiescent Circuit Current	I _{CCQ}	V _{CC} =3V, V _{in} =0		5.5		mA
		V _{CC} =6V, V _{in} =0		6.6		
		V _{CC} =9V, V _{in} =0		7.5		
Voltage Gain	G _V	V _{in} =0.5mV _{rms}	37	40	43	dB
Output Power	P _{out}	V _{CC} =3V, R _L =4Ω, THD=10%		120		mW
		V _{CC} =6V, R _L =4Ω, THD=10%	500	720		
		V _{CC} =6V, R _L =8Ω, THD=10%	300	450		
		V _{CC} =9V, R _L =8Ω, THD=10%	800	1100		
		V _{CC} =9V, R _L =16Ω, THD=10%	450	610		
Total Harmonic Distortion	THD	P _{out} =100mW		0.3	1	%
Output Noise Voltage	V _{no}	R _g =10kΩ, BPF=20Hz~20kHz		0.2	0.5	mV _{rms}
Ripple Rejection	RR	f _r =100Hz, V _r =0.3V _{rms} , C _{rip} =0		25		dB
Input Resistance	R _{in}			27		kΩ

TERMINAL DC VOLTAGE

Pin No.	1	2	3	4	5	6	7	8	9
Value(V)	0	2.4	0.62	0.64	0	0	2.61	NC	6

PRECAUTION FOR APPLICATION

1. Input stage (Figure 1)

The input stage of power amplifier is comprised of a PNP differential pair(Q2 and Q3) preceded by a PNP emitter follower(Q1) which allows DC referencing of the source signal to GND. This eliminates the need for an input coupling capacitor. However, in case the brush noise of volume becomes a problem, provide serially a coupling capacitor to the input side.

2. Adjustment of the voltage gain (figure 2)

The voltage gain is fixed at G_v=40dB by the resistors(R4 and R5) in IC. Its reduction is possible through adding R_f as shown in Figure 2. In this case, the voltage gain is obtained by the following equation: $G_v = 20 \log(R_5 + R_4 + R_f) / (R_4 + R_f)$. It is recommended to use this IC with the voltage gain of G_v=28dB or over.

UTC TA7368P LINEAR INTEGRATED CIRCUIT

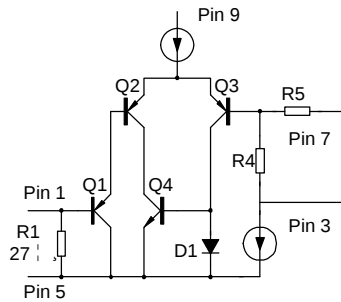


Figure 1

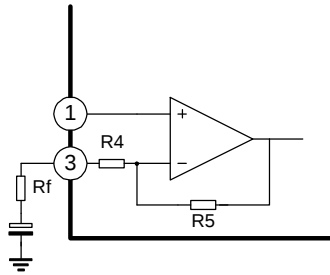


Figure 2

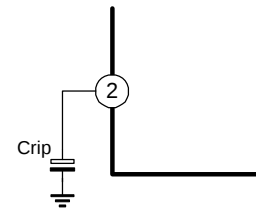


Figure 3

3. Ripple rejection (Figure 3)

Adding Crip, to the ripple terminal 2 as shown in Figure 3, the ripple rejection ratio is improved from -25dB to -45dB.

4. Power dissipation

Casre should be taken to use IC below maximum power dissipation because it may be over maximum rating depending on operating condition.

$P_d = 900\text{mW}$ ($T_a = 25^\circ\text{C}$)

5. Phase-compensation

Small temperature coefficient and excellent frequency characteristics is needed by capacitor below:

- *Oscillation preventing capacitor for power amplifier output
- *Bypass capacitor for ripple filter
- *Capacitor between V_{cc} and GND

TEST CIRCUIT

