Dual 2-input AND Gates

HITACHI

ADE-205-342A (Z)

Rev.1 July 2001

Description

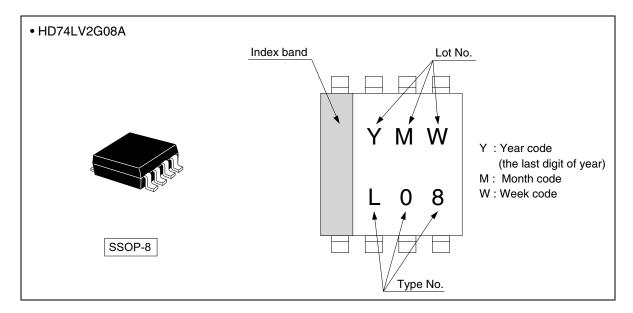
The HD74LV2G08A has dual two–inputs AND gates in a 8 pin package. Low voltage and high speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as hitachi uni logic series.
- Supplied on emboss taping for high speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV08A Supply voltage range: 1.65 to 5.5 V
 Operating temperature range: -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V) All outputs V_{O} (Max.) = 5.5 V (@ V_{CC} = 0 V)
- Output current $\pm 6 \text{ mA}$ (@V_{cc} = 3.0 V to 3.6 V), $\pm 12 \text{ mA}$ (@V_{cc} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.



Outline and Article Indication

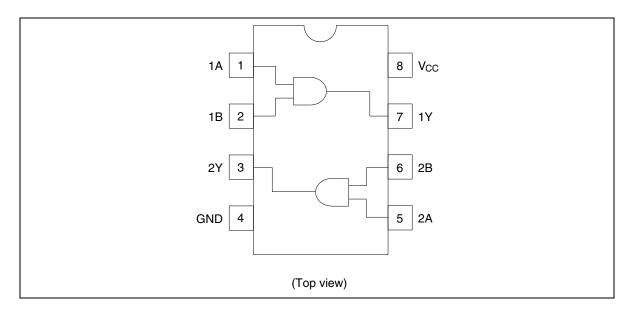


Function Table

Inputs		Output Y				
A	В	<u></u>				
L	L	L				
Н	L	L				
L	Н	L				
Н	Н	Н				

H : High level L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{cc}	-0.5 to 7.0	V	
Input voltage range *1	V _i	-0.5 to 7.0	V	
Output voltage range *1,2	V _o	-0.5 to V_{cc} + 0.5	V	Output : H or L
		-0.5 to 7.0		V _{cc} : OFF
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{ok}	±50	mA	$V_o < 0 \text{ or } V_o > V_{cc}$
Continuous output current	Io	±25	mA	$V_o = 0$ to V_{cc}
Continuous current through V_{cc} or GND	$I_{\rm cc}$ or $I_{\rm GND}$	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) ^{'3}	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes:

- The absolute maximum ratings are values which must not individually be exceeded, and furthermore no two of which may be realized at the same time.
- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{cc}	1.65	5.5	V	
Input voltage range	V _i	0	5.5	V	
Output voltage range	V _o	0	V _{cc}	V	
Output current	I _{OL}	_	1	mA	$V_{cc} = 1.65 \text{ to } 1.95 \text{ V}$
		_	2		$V_{cc} = 2.3 \text{ to } 2.7 \text{ V}$
		_	6		$V_{cc} = 3.0 \text{ to } 3.6 \text{ V}$
		_	12		$V_{cc} = 4.5 \text{ to } 5.5 \text{ V}$
	I _{OH}	_	-1		$V_{cc} = 1.65 \text{ to } 1.95 \text{ V}$
		_	-2	_	$V_{cc} = 2.3 \text{ to } 2.7 \text{ V}$
		_	-6		$V_{cc} = 3.0 \text{ to } 3.6 \text{ V}$
		_	-12		$V_{cc} = 4.5 \text{ to } 5.5 \text{ V}$
Input transition rise or fall rate	Δt / Δν	0	300	ns / V	V _{cc} = 1.65 to 1.95 V
		0	200		$V_{cc} = 2.3 \text{ to } 2.7 \text{ V}$
		0	100		$V_{cc} = 3.0 \text{ to } 3.6 \text{ V}$
		0	20		$V_{cc} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	T _a	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

• $Ta = -40 \text{ to } 85^{\circ}\text{C}$

Item	Symbol	V _{cc} (V) *	Min	Тур	Max	Unit	Test condition
Input voltage	V _{IH}	1.65 to 1.95	V _{cc} ×0.75	_	_	V	
		2.3 to 2.7	V _{cc} ×0.7	_	_	_	
		3.0 to 3.6	V _{cc} ×0.7	_	_	_	
		4.5 to 5.5	V _{cc} ×0.7	_	_	_	
	V _{IL}	1.65 to 1.95	_	_	V _{cc} ×0.25	=	
		2.3 to 2.7	_	_	V _{cc} ×0.3	=	
		3.0 to 3.6	_	_	V _{cc} ×0.3	_	
		4.5 to 5.5	_	_	V _{cc} ×0.3	=	
Hysteresis voltage	V _H	1.8	_	0.25	_	V	$V_{\scriptscriptstyle T}^{\;\scriptscriptstyle +} - V_{\scriptscriptstyle T}^{\;\scriptscriptstyle -}$
		2.5		0.30	_	_	
		3.3	_	0.35	_	=	
		5.0	_	0.45	_	_	
Output voltage	V _{OH}	Min to Max	V _{cc} -0.1	_	_	V	$I_{OH} = -50 \mu A$
		1.65	1.4	_	_	_	$I_{OH} = -1 \text{ mA}$
		2.3	2.0	_	_	_	$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	_	=	I _{он} = -6 mA
		4.5	3.8	_	_	=	I _{OH} = -12 mA
	V _{oL}	Min to Max	_	_	0.1	_	$I_{OL} = 50 \mu A$
		1.65	_	_	0.3	=	I _{OL} = 1 mA
		2.3	_	_	0.4	_	$I_{OL} = 2 \text{ mA}$
		3.0	_	_	0.44	=	$I_{OL} = 6 \text{ mA}$
		4.5	_	_	0.55	=	I _{oL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	I _{cc}	5.5	_		10	μΑ	$V_{IN} = V_{CC}$ or GND, $I_{O} = 0$
Output leakage current	I _{OFF}	0	_	_	5	μΑ	V_{IN} or $V_{O} = 0$ to 5.5 V
Input capacitance	C _{IN}	3.3	_	2.5	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

• $V_{cc} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	$T_a = 2$	5°C		$T_a = -4$	0 to 85°C	Unit	Test	FROM	ТО
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	12.8	22.7	1.0	25.0	ns	C _∟ = 15 pF	A or B	Υ
delay time	t _{PHL}	_	19.4	32.8	1.0	38.5		C _L = 50 pF	_	

• $V_{cc} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	$T_a = 2$	25°C	$T_a = -40 \text{ to } 85^{\circ}\text{C}$		Unit	Test	FROM	то	
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	7.9	13.8	1.0	16.0	ns	C _L = 15 pF	A or B	Υ
delay time	$t_{\scriptscriptstylePHL}$	_	10.5	17.3	1.0	20.0	_	C _L = 50 pF	_	

• $V_{cc} = 3.3 \pm 0.3 \text{ V}$

Item	Symbol	$T_a = 2$	25°C		$T_a = -4$	0 to 85°C	Unit	Test	FROM	то
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}		5.6	8.8	1.0	10.5	ns	C _L = 15 pF	A or B	Υ
delay time	t _{PHL}	_	7.5	12.3	1.0	14.0	=	C _L = 50 pF	_	

• $V_{CC} = 5.0 \pm 0.5 \text{ V}$

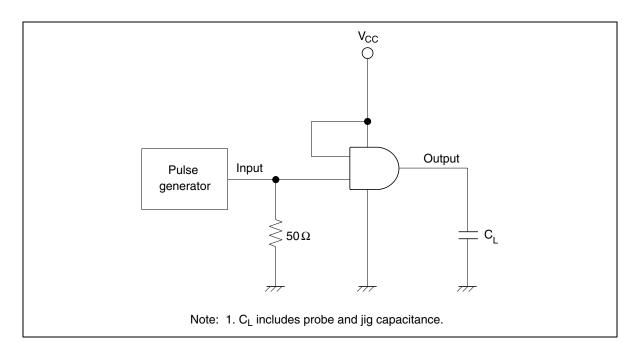
Item	Symbol	T _a = 2	25°C		$T_a = -4$	0 to 85°C	Unit	Test	FROM	то
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	4.1	5.9	1.0	7.0	ns	C _L = 15 pF	A or B	Υ
delay time	$t_{_{PHL}}$	_	5.5	7.9	1.0	9.0		C _L = 50 pF	_	

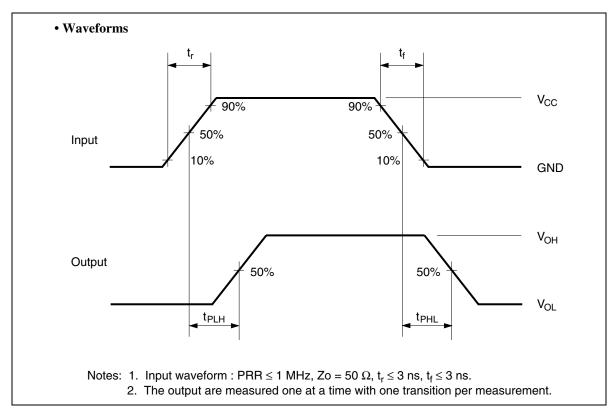
Operating Characteristics

• $C_L = 50 \text{ pF}$

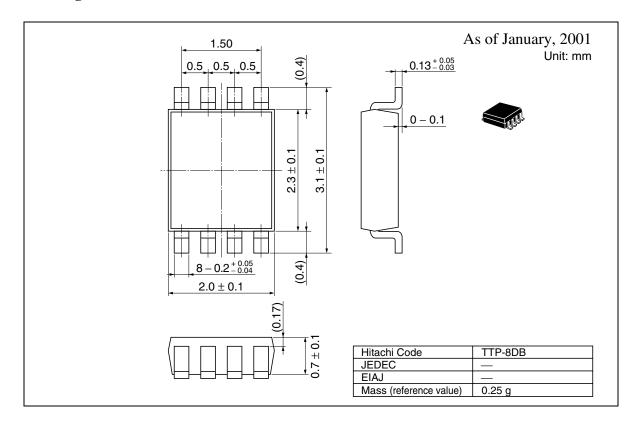
Item	Symbol	V_{cc} (V)	T _a = 25	$T_a = 25^{\circ}C$			Test Conditions
			Min	Тур	Max		
Power dissipation capacitance	C_{\scriptscriptstylePD}	3.3	_	8.0	_	pF	f = 10 MHz
		5.0	_	10.0	_		

Test Circuit





Package Dimensions



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