RENESAS

HD74LV2G125A

Dual Bus Buffer with 3-state Output

REJ03D0099-0500Z (Previous ADE-205-347D (Z)) Rev.5.00 Sep.30.2003

Description

The HD74LV2G125A has dual bus buffer with 3–state output in a 8 pin package. Output is disabled when the associated output enable (\overline{OE}) input is high. To ensure the high impedance state during power up or power down, \overline{OE} should be connected to V_{CC} through a pull-down resistor; the minimum value of the resistor is determined by the current souring capability of the driver. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

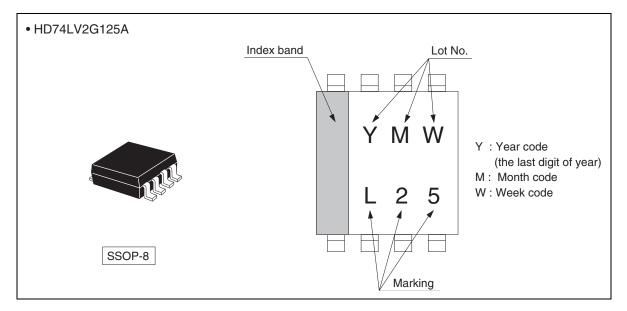
Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV125A Supply voltage range : 1.65 to 5.5 V Operating temperature range : -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V) All outputs V_0 (Max.) = 5.5 V (@V_{CC} = 0 V, Output : Z)
- Output current $\pm 6 \text{ mA}$ (@V_{CC} = 3.0 V to 3.6 V), $\pm 12 \text{ mA}$ (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV2G125AUSE	SSOP-8 pin	TTP-8DBV	US	E (3,000 pcs/reel)



Outline and Article Indication



Function Table

Inputs		Output Y					
ŌĒ	Α						
L	Н	Н					
L	L	L					
Н	Х	Z					

H : High level

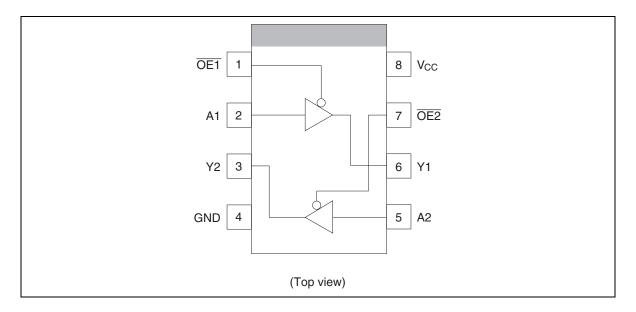
L : Low level

X : Immaterial

Z : High impedance



Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	–0.5 to V _{CC} + 0.5	V	Output : H or L
		-0.5 to 7.0		V _{CC} : OFF or output : Z
Input clamp current	l _{IK}	-20	mA	V _I < 0
Output clamp current	I _{ОК}	±50	mA	$V_0 < 0 \text{ or } V_0 > V_{CC}$
Continuous output current	Io	±25	mA	$V_{O} = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	±50	mA	
Maximum power dissipation at Ta = 25° C (in still air) ^{*3}	P _T	200	mW	
Storage temperature	Tstg	–65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 5.5 V maximum.

3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	Vcc	V	
		0	5.5		Output : Z
Output current	I _{OL}		1	mA	V_{CC} = 1.65 to 1.95 V
		_	2		V_{CC} = 2.3 to 2.7 V
		_	6		$V_{CC} = 3.0$ to 3.6 V
		_	12		$V_{CC} = 4.5$ to 5.5 V
	Іон	_	-1		V_{CC} = 1.65 to 1.95 V
		_	-2		V_{CC} = 2.3 to 2.7 V
		_	-6		V_{CC} = 3.0 to 3.6 V
		—	-12		V_{CC} = 4.5 to 5.5 V
Input transition rise or fall rate	Δt / Δv	0	300	ns / V	V_{CC} = 1.65 to 1.95 V
		0	200		V_{CC} = 2.3 to 2.7 V
		0	100		V_{CC} = 3.0 to 3.6 V
		0	20		V_{CC} = 4.5 to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.



Electrical Characteristic

• Ta = -40 to $85^{\circ}C$

Item	Symbol	V _{cc} (V) *	Min	Тур	Мах	Unit	Test condition
Input voltage	V _{IH}	1.65 to 1.95	V _{CC} ×0.75	_	_	V	
		2.3 to 2.7	V _{CC} ×0.7	—	—	-	
		3.0 to 3.6	V _{CC} ×0.7	—	_	-	
		4.5 to 5.5	V _{CC} ×0.7	—	—	-	
	V _{IL}	1.65 to 1.95	—	—	V _{CC} ×0.25	-	
		2.3 to 2.7		—	V _{CC} ×0.3	-	
		3.0 to 3.6		—	V _{CC} ×0.3	-	
		4.5 to 5.5		—	V _{CC} ×0.3	-	
Hysteresis voltage	V _H	1.8	_	0.25	_	V	$V_T^+ - V_T^-$
		2.5	—	0.30	_	-	
		3.3	—	0.35	_	-	
		5.0	—	0.45	_	-	
Output voltage	V _{OH}	Min to Max	V _{CC} -0.1	—	—	V	I _{OH} = -50 μA
		1.65	1.4	—	_	-	$I_{OH} = -1 \text{ mA}$
		2.3	2.0	—		_	$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	_	_	I _{OH} =6 mA
		4.5	3.8	—	_	-	I _{OH} = -12 mA
	Vol	Min to Max		—	0.1	-	I _{OL} = 50 μA
		1.65	—	—	0.3	-	I _{OL} = 1 mA
		2.3		—	0.4	-	$I_{OL} = 2 \text{ mA}$
		3.0	_	_	0.44	_	I _{OL} = 6 mA
		4.5	—	—	0.55	-	I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5		—	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Off state output current	l _{oz}	Min to Max	_	_	±5	μΑ	V_{O} = 5.5 V or GND
Quiescent supply current	I _{CC}	5.5	_	_	10	μA	$V_{IN} = V_{CC}$ or GND, $I_{O} = 0$
Output leakage current	I _{OFF}	0	_	_	5	μΑ	$V_{\rm IN}$ or $V_{\rm O}$ = 0 to 5.5 V
Input capacitance	C _{IN}	3.3	_	3.0	—	pF	$V_{IN} = V_{CC} \text{ or } GND$

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.



Switching Characteristics

• $V_{CC} = 1.8 \pm 0.15 \text{ V}$

ltem	Symbol $T_a = 25^{\circ}C$ $T_a = -40 \text{ to } 85^{\circ}C$		Unit		FROM	ТО				
		Min	Тур	Max	Min	Max	-	Conditions	(Input)	(Output)
Propagation	t _{PLH}	—	13.5	23.5	1.0	26.0	ns	C _L = 15 pF	А	Y
delay time	t _{PHL}	—	19.0	33.0	1.0	36.0	_	$C_L = 50 \text{ pF}$	_	
Enable time	t _{ZH}	_	13.7	26.5	1.0	29.0	ns	$C_L = 15 \text{ pF}$	ŌĒ	Y
	t _{ZL}	—	20.5	36.0	1.0	38.0	_	$C_L = 50 \text{ pF}$	_	
Disable time	t _{HZ}	—	8.3	20.0	1.0	22.5	ns	$C_L = 15 \text{ pF}$	ŌĒ	Y
	t _{LZ}	—	13.0	29.5	1.0	32.0		$C_L = 50 \text{ pF}$	=	

• $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol $T_a = 25^{\circ}C$ $T_a = -40 \text{ to } 85^{\circ}C$ U		Unit		FROM	ТО				
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}		6.8	13.0	1.0	15.5	ns	$C_L = 15 \text{ pF}$	А	Y
delay time	t _{PHL}	_	8.7	16.5	1.0	18.5		$C_L = 50 \text{ pF}$	_	
Enable time	t _{ZH}	_	7.0	13.0	1.0	15.5	ns	$C_L = 15 \text{ pF}$	ŌĒ	Y
	t _{ZL}	—	8.8	16.5	1.0	18.5	_	$C_L = 50 \text{ pF}$	_	
Disable time	t _{HZ}	_	5.1	14.7	1.0	17.0	ns	$C_L = 15 \text{ pF}$	ŌĒ	Y
	t _{LZ}	_	7.3	18.2	1.0	20.5		$C_L = 50 \text{ pF}$		

• $V_{CC} = 3.3 \pm 0.3 V$

Item	Symbol	T _a = 25°C		$T_a = -40$ to $85^{\circ}C$		Unit		FROM	ТО	
		Min	n Typ Max Min Max		_	Conditions	(Input)	(Output)		
Propagation	t _{PLH}		4.8	8.0	1.0	9.5	ns	$C_L = 15 \text{ pF}$	А	Y
delay time	t _{PHL}	_	6.1	11.5	1.0	13.0	_	$C_L = 50 \text{ pF}$	_	
Enable time	t _{ZH}		4.8	8.0	1.0	9.5	ns	$C_L = 15 \text{ pF}$	ŌĒ	Y
	t_{ZL}	_	6.2	11.5	1.0	13.0	_	$C_L = 50 \text{ pF}$	_	
Disable time	t _{HZ}	—	4.1	9.7	1.0	11.5	ns	$C_L = 15 \text{ pF}$	ŌĒ	Y
	t _{LZ}	_	5.5	13.2	1.0	15.0		$C_L = 50 \text{ pF}$	_	

Switching Characteristics (cont)

• $V_{CC} = 5.0 \pm 0.5 \text{ V}$

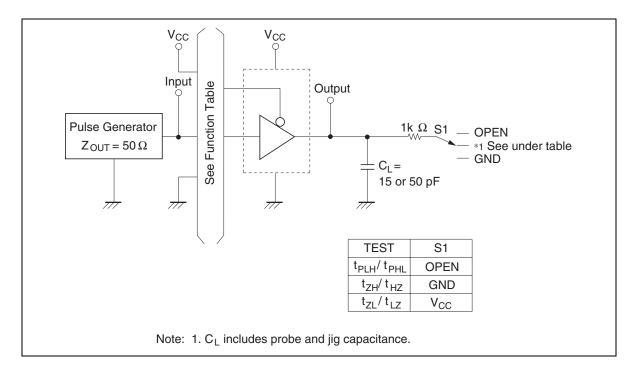
ltem	Symbol $T_a = 25^{\circ}C$ $T_a = -40$ to $85^{\circ}C$		0 to 85°C	Unit		FROM	то			
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}	—	3.4	5.5	1.0	6.5	ns	C _L = 15 pF	А	Y
delay time	t _{PHL}	—	4.3	7.5	1.0	8.5	_	$C_L = 50 \text{ pF}$	_	
Enable time	t _{ZH}		3.4	5.1	1.0	6.0	ns	$C_L = 15 \text{ pF}$	ŌĒ	Y
	t _{ZL}	—	4.4	7.1	1.0	8.0	_	$C_L = 50 \text{ pF}$	_	
Disable time	t _{HZ}	_	3.2	6.8	1.0	8.0	ns	$C_L = 15 \text{ pF}$	ŌĒ	Y
	t _{LZ}	—	4.0	8.8	1.0	10.0		$C_L = 50 \text{ pF}$	_	

Operating Characteristics

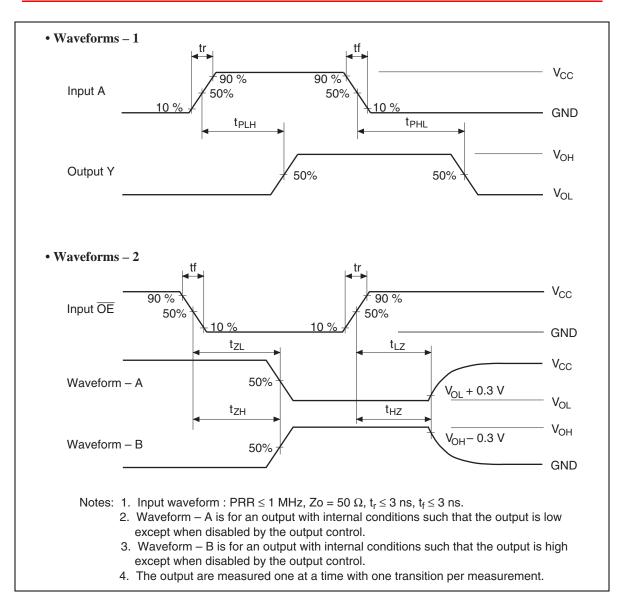
• $C_L = 50 \text{ pF}$

ltem	Symbol	V _{cc} (V)	T _a = 2	T _a = 25°C			Test Conditions
			Min	Тур	Max		
Power dissipation	C _{PD}	3.3	—	10.5	_	pF	f = 10 MHz
capacitance		5.0	—	11.5	—		

Test Circuit

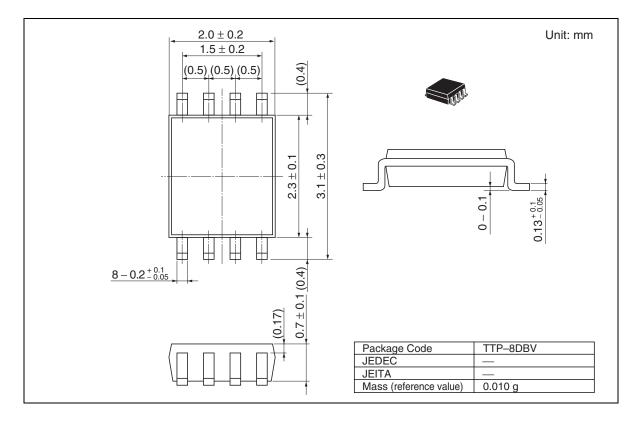








Package Dimensions





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